

Lu Tang

List of Publications by Year in descending order

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papers

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10
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citing authors

#	ARTICLE	IF	CITATIONS
1	A W-band injection-locked frequency divider with modified transformer coupling resonant. Microwave and Optical Technology Letters, 2021, 63, 1091-1096.	1.4	2
2	A High-Speed Programmable Frequency Divider for a Ka-Band Phase Locked Loop-Type Frequency Synthesizer in 90-nm CMOS. Electronics (Switzerland), 2021, 10, 2494.	3.1	2
3	Design of 850 GHz 2 ^Ã 2 Array Heterodyne-receiver Chips Based on Schottky-diode GaAs Process. , 2021, , .		1
4	A switchable dual-mode fully-differential common-source low-noise amplifier in 0.18-µm CMOS technology. Microwave and Optical Technology Letters, 2020, 62, 1163-1168.	1.4	1
5	An E-band digitally controlled oscillator with <i>i</i> effective tuning bank. Microwave and Optical Technology Letters, 2020, 62, 2493-2498.	1.4	1
6	A CMOS fully differential ring VCO with active inductors and I/Q outputs. Microwave and Optical Technology Letters, 2019, 61, 937-942.	1.4	6
7	Interference Alignment in Two-Cell LTE-Advanced Heterogeneous Networks. IEICE Transactions on Communications, 2015, E98.B, 126-133.	0.7	0
8	An ultra-high speed monolithic clock recovery circuit in 0.2-µm GaAs process. Analog Integrated Circuits and Signal Processing, 2015, 83, 45-53.	1.4	2
9	A multi-band CMOS PLL-based frequency synthesizer for DRM/DRM+/DAB systems. Analog Integrated Circuits and Signal Processing, 2014, 80, 293-304.	1.4	3
10	5 Gb/s 2:1 fully-integrated full-rate multiplexer with on-chip clock generation circuit in 0.18-µm CMOS. Analog Integrated Circuits and Signal Processing, 2012, 72, 469-480.	1.4	1