

Phan Trong Tue

List of Publications by Year in descending order

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18
docs citations

18
times ranked

370
citing authors

#	ARTICLE	IF	CITATIONS
1	High-Performance Solution-Processed ZrInZnO Thin-Film Transistors. IEEE Transactions on Electron Devices, 2013, 60, 320-326.	3.0	60
2	Peptide aptamer-modified single-walled carbon nanotube-based transistors for high-performance biosensors. Scientific Reports, 2017, 7, 17881.	3.3	42
3	Totally solution-processed ferroelectric-gate thin-film transistor. Applied Physics Letters, 2010, 97, .	3.3	36
4	A low-temperature crystallization path for device-quality ferroelectric films. Applied Physics Letters, 2010, 97, .	3.3	33
5	Combustion synthesized indium-tin-oxide (ITO) thin film for source/drain electrodes in all solution-processed oxide thin-film transistors. Applied Physics A: Materials Science and Processing, 2016, 122, 1.	2.3	20
6	Ferroelectric-Gate Thin-Film Transistor Fabricated by Total Solution Deposition Process. Japanese Journal of Applied Physics, 2011, 50, 04DD09.	1.5	19
7	Electrochemical Immunoassay Using Open Circuit Potential Detection Labeled by Platinum Nanoparticles. Sensors, 2018, 18, 444.	3.8	18
8	Optimization of Pt and PZT Films for Ferroelectric-Gate Thin Film Transistors. Ferroelectrics, 2010, 405, 281-291.	0.6	12
9	Developing Conductive Highly Ordered Zinc Oxide Nanorods by Acetylacetonate-Assisted Growth. Materials, 2020, 13, 1136.	2.9	10
10	Investigation of Polysilazane-Based SiO_2 Gate Insulator for Oxide Semiconductor Thin-Film Transistors. IEEE Transactions on Electron Devices, 2013, 60, 1149-1153.	3.0	9
11	Solution-based process with thermal UV treatment for fabrication of piezoelectric PZT films for an actuator array at temperatures under 450 Å°C. Sensors and Actuators A: Physical, 2017, 267, 287-292.	4.1	6
12	A facile solution-combustion-synthetic approach enabling low-temperature PZT thin-films. APL Materials, 2020, 8, .	5.1	6
13	Fabrication and characterization of a ferroelectric-gate FET With a ITO/PZT/SRO/Pt stacked structure. , 2010, , .		5
14	Interdiffusion during heteroepitaxial Au growth on Pd thin films by electroless Au plating (ELGP) at room temperature. Applied Physics Express, 2020, 13, 015006.	2.4	5
15	High-performance oxide thin film transistor fully fabricated by a direct rheology-imprinting. Applied Physics Letters, 2017, 111, 223504.	3.3	4
16	Nano-rheology printing of sub-0.2 μm channel length oxide thin-film transistors. Nano Futures, 2018, 2, 035006.	2.2	4
17	Thermoelectric Properties and Carrier Localization in Ultrathin Layer of Nb-Doped MoS_2 . Physica Status Solidi (B): Basic Research, 2018, 255, 1800125.	1.5	3
18	Electric Properties and Interface Charge Trap Density of Ferroelectric Gate Thin Film Transistor Using $(\text{Bi},\text{La})\text{Ti}_3\text{O}_{12}/\text{Pb}(\text{Zr},\text{Ti})\text{O}_3$ Stacked Gate Insulator. Japanese Journal of Applied Physics, 2012, 51, 09LA09.	1.5	1