

Bassam J Mohd

List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

42
papers

589
citations

14
h-index

23
g-index

51
ext. papers

781
ext. citations

2.4
avg, IF

4.43
L-index

| # | Paper | IF | Citations |
|----|--|-----|-----------|
| 42 | A survey on lightweight block ciphers for low-resource devices: Comparative study and open issues. <i>Journal of Network and Computer Applications</i> , 2015 , 58, 73-93 | 7.9 | 96 |
| 41 | Security Vulnerabilities in Bluetooth Technology as Used in IoT. <i>Journal of Sensor and Actuator Networks</i> , 2018 , 7, 28 | 3.8 | 52 |
| 40 | Secure Authentication for Remote Patient Monitoring with Wireless Medical Sensor Networks. <i>Sensors</i> , 2016 , 16, 424 | 3.8 | 50 |
| 39 | QoS-aware health monitoring system using cloud-based WBANs. <i>Journal of Medical Systems</i> , 2014 , 38, 121 | 5.1 | 44 |
| 38 | Lightweight Block Ciphers for IoT: Energy Optimization and Survivability Techniques. <i>IEEE Access</i> , 2018 , 6, 35966-35978 | 3.5 | 42 |
| 37 | Modeling and optimization of the lightweight HIGHT block cipher design with FPGA implementation. <i>Security and Communication Networks</i> , 2016 , 9, 2200-2216 | 1.9 | 33 |
| 36 | Hardware design and modeling of lightweight block ciphers for secure communications. <i>Future Generation Computer Systems</i> , 2018 , 83, 510-521 | 7.5 | 26 |
| 35 | Hardware Security in IoT Devices with Emphasis on Hardware Trojans. <i>Journal of Sensor and Actuator Networks</i> , 2019 , 8, 42 | 3.8 | 26 |
| 34 | An energy-efficient and security aware route selection protocol for wireless sensor networks. <i>Security and Communication Networks</i> , 2014 , 7, 2015-2038 | 1.9 | 23 |
| 33 | Analyzing Cyber-Physical Threats on Robotic Platforms. <i>Sensors</i> , 2018 , 18, | 3.8 | 21 |
| 32 | FPGA Modeling and Optimization of a SIMON Lightweight Block Cipher. <i>Sensors</i> , 2019 , 19, | 3.8 | 19 |
| 31 | Low power Wallace multiplier design based on wide counters. <i>International Journal of Circuit Theory and Applications</i> , 2012 , 40, 1175-1185 | 2 | 19 |
| 30 | . <i>IEEE Systems Journal</i> , 2017 , 11, 2536-2545 | 4.3 | 18 |
| 29 | Performance and Information Security Evaluation with Firewalls. <i>International Journal of Security and Its Applications</i> , 2013 , 7, 355-372 | 2 | 17 |
| 28 | Wavelet-transform steganography: algorithm and hardware implementation. <i>International Journal of Electronic Security and Digital Forensics</i> , 2013 , 5, 241 | 1 | 14 |
| 27 | ANALYSIS AND MODELING OF FPGA IMPLEMENTATIONS OF SPATIAL STEGANOGRAPHY METHODS. <i>Journal of Circuits, Systems and Computers</i> , 2014 , 23, 1450018 | 0.9 | 12 |
| 26 | Extrinsic Calibration of Camera and 2D Laser Sensors without Overlap. <i>Sensors</i> , 2017 , 17, | 3.8 | 10 |

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| 25 | FPGA hardware of the LSB steganography method 2012 , | | 8 |
| 24 | A comparative study of steganography designs based on multiple FPGA platforms. <i>International Journal of Electronic Security and Digital Forensics</i> , 2016 , 8, 164 | 1 | 8 |
| 23 | Secure and efficient data delivery for fog-assisted wireless body area networks. <i>Peer-to-Peer Networking and Applications</i> , 2019 , 12, 1289-1307 | 3.1 | 8 |
| 22 | Optimization and modeling of FPGA implementation of the Katan Cipher 2015 , | | 6 |
| 21 | An analysis and evaluation of lightweight hash functions for blockchain-based IoT devices. <i>Cluster Computing</i> , 2021 , 24, 3065 | 2.1 | 5 |
| 20 | Performance evaluation of the SM4 cipher based on field-programmable gate array implementation. <i>IET Circuits, Devices and Systems</i> , 2021 , 15, 121-135 | 1.1 | 4 |
| 19 | Power-Aware Adaptive Encryption 2019 , | | 3 |
| 18 | Hierarchical steganography using novel optimum quantization technique. <i>Signal, Image and Video Processing</i> , 2013 , 7, 1029-1040 | 1.6 | 3 |
| 17 | MPLS technology in wireless networks. <i>Wireless Networks</i> , 2014 , 20, 1037-1051 | 2.5 | 3 |
| 16 | Public-Key Authentication for Cloud-based WBANs 2014 , | | 3 |
| 15 | Kinect-Based Virtual Try-on System: A Case Study 2019 , | | 2 |
| 14 | 2017 , | | 2 |
| 13 | Carry-based reduction parallel counter design. <i>International Journal of Electronics</i> , 2013 , 100, 1510-1528 | 1.2 | 2 |
| 12 | Implementation of speech feature extraction for low-resource devices. <i>IET Circuits, Devices and Systems</i> , 2019 , 13, 863-872 | 1.1 | 2 |
| 11 | Software comprehension based on database relational algebra. <i>International Journal of Information and Communication Technology</i> , 2014 , 6, 58 | 0.1 | 1 |
| 10 | Image Steganography Optimization Technique. <i>Lecture Notes of the Institute for Computer Sciences, Social-Informatics and Telecommunications Engineering</i> , 2012 , 205-209 | 0.2 | 1 |
| 9 | Contention-free switch-based implementation of 1024-point Radix-2 Fourier Transform Engine 2007 , | | 1 |
| 8 | Automatic License Plate Detection and Recognition for Jordanian Vehicles. <i>Advances in Science, Technology and Engineering Systems</i> , 2020 , 5, 699-709 | 0.3 | 1 |

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| 7 | Run-time Monitoring and Validation using Reverse Function (RMVRF) for Hardware Trojans Detection. <i>IEEE Transactions on Dependable and Secure Computing</i> , 2020 , 1-1 | 3.9 | 1 |
| 6 | Hardware Trojan detection for lightweight ciphers implemented on field-programmable gate arrays using the replay algorithm. <i>International Journal of Circuit Theory and Applications</i> , 2021 , 49, 3607 ² | | 1 |
| 5 | POSIT vs. Floating Point in Implementing IIR Notch Filter by Enhancing Radix-4 Modified Booth Multiplier. <i>Electronics (Switzerland)</i> , 2022 , 11, 163 | 2.6 | 0 |
| 4 | Erratum to An Energy-Efficient and Security Aware Route Selection Protocol for Wireless Sensor Networks <i>Security and Communication Networks</i> , 2020 , 2020, 1-1 | 1.9 | |
| 3 | A novel approach to enhance distributed virtual memory. <i>Computers and Electrical Engineering</i> , 2012 , 38, 388-398 | 4.3 | |
| 2 | The Hazard-Free Superscalar Pipeline Fast Fourier Transform Architecture and Algorithm. <i>International Federation for Information Processing</i> , 2009 , 1-22 | | |
| 1 | A Methodology for Distributed Virtual Memory Improvement. <i>Communications in Computer and Information Science</i> , 2011 , 378-384 | 0.3 | |