Bassam J Mohd

List of Publications by Year in descending order

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| 51 | 937 | 16 | 30 g-index |
|----------------|----------------------|--------------------|--------------------|
| papers | citations | h-index | g-index |
| 51 all docs | 51 docs citations | 51 times ranked | 830 citing authors |

| # | Article | IF | CITATIONS |
|----|---|-----|-----------|
| 1 | A survey on lightweight block ciphers for low-resource devices: Comparative study and open issues. Journal of Network and Computer Applications, 2015, 58, 73-93. | 5.8 | 144 |
| 2 | Security Vulnerabilities in Bluetooth Technology as Used in IoT. Journal of Sensor and Actuator Networks, 2018, 7, 28. | 2.3 | 103 |
| 3 | Lightweight Block Ciphers for IoT: Energy Optimization and Survivability Techniques. IEEE Access, 2018, 6, 35966-35978. | 2.6 | 72 |
| 4 | Secure Authentication for Remote Patient Monitoring with Wireless Medical Sensor Networks. Sensors, 2016, 16, 424. | 2.1 | 65 |
| 5 | QoS-Aware Health Monitoring System Using Cloud-Based WBANs. Journal of Medical Systems, 2014, 38, 121. | 2.2 | 57 |
| 6 | Hardware Security in IoT Devices with Emphasis on Hardware Trojans. Journal of Sensor and Actuator Networks, 2019, 8, 42. | 2.3 | 56 |
| 7 | Modeling and optimization of the lightweight HIGHT block cipher design with FPGA implementation. Security and Communication Networks, 2016, 9, 2200-2216. | 1.0 | 42 |
| 8 | Hardware design and modeling of lightweight block ciphers for secure communications. Future Generation Computer Systems, 2018, 83, 510-521. | 4.9 | 38 |
| 9 | An energyâ€efficient and security aware route selection protocol for wireless sensor networks. Security and Communication Networks, 2014, 7, 2015-2038. | 1.0 | 33 |
| 10 | FPGA Modeling and Optimization of a SIMON Lightweight Block Cipher. Sensors, 2019, 19, 913. | 2.1 | 29 |
| 11 | Low power Wallace multiplier design based on wide counters. International Journal of Circuit Theory and Applications, 2012, 40, 1175-1185. | 1.3 | 28 |
| 12 | Analyzing Cyber-Physical Threats on Robotic Platforms. Sensors, 2018, 18, 1643. | 2.1 | 28 |
| 13 | Performance and Information Security Evaluation with Firewalls. International Journal of Security and Its Applications, 2013, 7, 355-372. | 0.5 | 27 |
| 14 | An Enhanced WLAN Security System With FPGA Implementation for Multimedia Applications. IEEE Systems Journal, 2017, 11, 2536-2545. | 2.9 | 26 |
| 15 | An analysis and evaluation of lightweight hash functions for blockchain-based IoT devices. Cluster Computing, 2021, 24, 3065-3084. | 3.5 | 23 |
| 16 | FPGA hardware of the LSB steganography method. , 2012, , . | | 18 |
| 17 | Wavelet-transform steganography: algorithm and hardware implementation. International Journal of Electronic Security and Digital Forensics, 2013, 5, 241. | 0.1 | 17 |
| 18 | ANALYSIS AND MODELING OF FPGA IMPLEMENTATIONS OF SPATIAL STEGANOGRAPHY METHODS. Journal of Circuits, Systems and Computers, 2014, 23, 1450018. | 1.0 | 16 |

| # | Article | lF | CITATIONS |
|----|--|-----|-----------|
| 19 | Secure and efficient data delivery for fog-assisted wireless body area networks. Peer-to-Peer Networking and Applications, 2019, 12, 1289-1307. | 2.6 | 16 |
| 20 | Extrinsic Calibration of Camera and 2D Laser Sensors without Overlap. Sensors, 2017, 17, 2346. | 2.1 | 15 |
| 21 | MPLS technology in wireless networks. Wireless Networks, 2014, 20, 1037-1051. | 2.0 | 9 |
| 22 | A comparative study of steganography designs based on multiple FPGA platforms. International Journal of Electronic Security and Digital Forensics, 2016, 8, 164. | 0.1 | 9 |
| 23 | Optimization and modeling of FPGA implementation of the Katan Cipher. , 2015, , . | | 8 |
| 24 | Hierarchical steganography using novel optimum quantization technique. Signal, Image and Video Processing, 2013, 7, 1029-1040. | 1.7 | 6 |
| 25 | Kinect-Based Virtual Try-on System: A Case Study. , 2019, , . | | 6 |
| 26 | Contention-free switch-based implementation of 1024-point Radix-2 Fourier Transform Engine. , 2007, , . | | 5 |
| 27 | Security risk assessment of the PeopleBot mobile robot research platform. , 2017, , . | | 4 |
| 28 | Power-Aware Adaptive Encryption., 2019,,. | | 4 |
| 29 | Performance evaluation of the SM4 cipher based on fieldâ€programmable gate array implementation. IET Circuits, Devices and Systems, 2021, 15, 121-135. | 0.9 | 4 |
| 30 | Carry-based reduction parallel counter design. International Journal of Electronics, 2013, 100, 1510-1528. | 0.9 | 3 |
| 31 | Implementation of speech feature extraction for lowâ€resource devices. IET Circuits, Devices and Systems, 2019, 13, 863-872. | 0.9 | 3 |
| 32 | Run-Time Monitoring and Validation Using Reverse Function (RMVRF) for Hardware Trojans Detection. IEEE Transactions on Dependable and Secure Computing, 2021, 18, 2689-2704. | 3.7 | 3 |
| 33 | Public-Key Authentication for Cloud-based WBANs. , 2014, , . | | 3 |
| 34 | A Power-Scalable Switch-Based Multi-processor FFT. , 2009, , . | | 2 |
| 35 | Image Steganography Optimization Technique. Lecture Notes of the Institute for Computer Sciences, Social-Informatics and Telecommunications Engineering, 2012, , 205-209. | 0.2 | 2 |
| 36 | Relational database approach for execution trace analysis., 2012,,. | | 2 |

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|----|--|-----|-----------|
| 37 | Indoor Wi-Fi tracking system using fingerprinting and Kalman filter. , 2017, , . | | 2 |
| 38 | Automatic License Plate Detection and Recognition for Jordanian Vehicles. Advances in Science, Technology and Engineering Systems, 2020, 5, 699-709. | 0.4 | 2 |
| 39 | POSIT vs. Floating Point in Implementing IIR Notch Filter by Enhancing Radix-4 Modified Booth Multiplier. Electronics (Switzerland), 2022, 11, 163. | 1.8 | 2 |
| 40 | Constructing reusable testbenches., 0,,. | | 1 |
| 41 | The hazard-free superscalar pipeline fast fourier transform algorithm and architecture. , 2007, , . | | 1 |
| 42 | A novel approach to enhance distributed virtual memory. Computers and Electrical Engineering, 2012, 38, 388-398. | 3.0 | 1 |
| 43 | Software comprehension based on database relational algebra. International Journal of Information and Communication Technology, 2014, 6, 58. | 0.1 | 1 |
| 44 | Hardware Trojan detection for lightweight ciphers implemented on fieldâ€programmable gate arrays using the replay algorithm. International Journal of Circuit Theory and Applications, 2021, 49, 3607. | 1.3 | 1 |
| 45 | Dynamic power analysis for custom designs. , 2009, , . | | O |
| 46 | Design and implementation of interfacing two FPGAs. , 2012, , . | | 0 |
| 47 | Security Issues in WSNs with Cooperative Communication. , 2015, , . | | O |
| 48 | Erratum to "An Energy-Efficient and Security Aware Route Selection Protocol for Wireless Sensor Networks― Security and Communication Networks, 2020, 2020, 1-1. | 1.0 | 0 |
| 49 | The Hazard-Free Superscalar Pipeline Fast Fourier Transform Architecture and Algorithm. International Federation for Information Processing, 2009, , 1-22. | 0.4 | O |
| 50 | A Methodology for Distributed Virtual Memory Improvement. Communications in Computer and Information Science, 2011, , 378-384. | 0.4 | 0 |
| 51 | An Efficient Backup Technique for Database Systems Based on Threshold Sharing. Journal of Computers, 2013, 8, . | 0.4 | 0 |