

Diego Vazquez Garcia De La Vega

List of Publications by Year in descending order

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42
papers

505
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759233

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all docs

42
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42
times ranked

144
citing authors

| # | ARTICLE | IF | CITATIONS |
|----|--|-----|-----------|
| 1 | Practical oscillation-based test of integrated filters. IEEE Design and Test of Computers, 2002, 19, 64-72. | 1.0 | 67 |
| 2 | Testing mixed-signal cores: a practical oscillation-based test in an analog macrocell. IEEE Design and Test of Computers, 2002, 19, 73-82. | 1.0 | 57 |
| 3 | Testable switched-capacitor filters. IEEE Journal of Solid-State Circuits, 1993, 28, 719-724. | 5.4 | 52 |
| 4 | On-chip sinusoidal signal generation with harmonic cancelation for analog and mixed-signal BIST applications. Analog Integrated Circuits and Signal Processing, 2015, 82, 67-79. | 1.4 | 28 |
| 5 | Improving the testability of switched-capacitor filters. Journal of Electronic Testing: Theory and Applications (JETTA), 1993, 4, 299-313. | 1.2 | 21 |
| 6 | Oscillation-based test in bandpass oversampled A/D converters. Microelectronics Journal, 2003, 34, 927-936. | 2.0 | 21 |
| 7 | Test of switched-capacitor ladder filters using OBT. Microelectronics Journal, 2005, 36, 1073-1079. | 2.0 | 21 |
| 8 | A high-Q bandpass fully differential SC filter with enhanced testability. IEEE Journal of Solid-State Circuits, 1998, 33, 976-986. | 5.4 | 20 |
| 9 | On-Chip Evaluation of Oscillation-Based-Test Output Signals for Switched-Capacitor Circuits. Analog Integrated Circuits and Signal Processing, 2002, 33, 201-211. | 1.4 | 20 |
| 10 | Oscillation-based test in oversampled $\hat{\Delta}\hat{\Sigma}^m$ modulators. Microelectronics Journal, 2002, 33, 799-806. | 2.0 | 19 |
| 11 | A low-cost digital frequency testing approach for mixed-signal devices using $\hat{\Delta}\hat{\Sigma}^m$ modulation. Microelectronics Journal, 2005, 36, 1080-1090. | 2.0 | 17 |
| 12 | Practical DfT strategy for fault diagnosis in active analogue filters. Electronics Letters, 1995, 31, 1221-1222. | 1.0 | 15 |
| 13 | Sine-Wave Signal Characterization Using Square-Wave and $\Delta\Sigma$ -Modulation: Application to Mixed-Signal BIST. Journal of Electronic Testing: Theory and Applications (JETTA), 2005, 21, 221-232. | 1.2 | 14 |
| 14 | 2.4-GHz single-ended input low-power low-voltage active front-end for ZigBee applications in 90 nm CMOS. , 2011, , . | | 14 |
| 15 | Improving the testability of switched-capacitor filters. Analog Integrated Circuits and Signal Processing, 1993, 4, 199-213. | 1.4 | 13 |
| 16 | On-Chip Analog Sinewave Generator with Reduced Circuitry Resources. Midwest Symposium on Circuits and Systems, 2006, , . | 1.0 | 12 |
| 17 | Analog Sinewave Signal Generators for Mixed-Signal Built-in Test Applications. Journal of Electronic Testing: Theory and Applications (JETTA), 2011, 27, 305-320. | 1.2 | 10 |
| 18 | On-chip characterisation of RF systems based on envelope response analysis. Electronics Letters, 2010, 46, 36. | 1.0 | 9 |

| # | ARTICLE | IF | CITATIONS |
|----|---|-----|-----------|
| 19 | A Simple and Secure Start-Up Circuitry for Oscillation-Based-Test Application. Analog Integrated Circuits and Signal Processing, 2002, 32, 187-190. | 1.4 | 8 |
| 20 | A 1.2V 5.14mW quadrature frequency synthesizer in 90nm CMOS technology for 2.4GHz ZigBee applications. , 2008, , . | | 7 |
| 21 | Sinusoidal signal generation for mixed-signal BIST using a harmonic-cancellation technique. , 2013, , . | | 7 |
| 22 | Practical oscillation-based test in analog integrated filters: experimental results. , 0, , . | | 6 |
| 23 | Low-cost signature test of RF blocks based on envelope response analysis. , 2010, , . | | 6 |
| 24 | A BIST Solution for Frequency Domain Characterization of Analog Circuits. Journal of Electronic Testing: Theory and Applications (JETTA), 2010, 26, 429-441. | 1.2 | 5 |
| 25 | A 3.6mW @ 1.2V high linear &sup>th&/sup>-order CMOS complex filter for IEEE 802.15.4 standard. , 2011, , . | | 5 |
| 26 | A Sinewave Analyzer for Mixed-Signal BIST Applications in a 0.35 um Technology. , 2006, , . | | 4 |
| 27 | A BIST Solution for the Functional Characterization of RF Systems Based on Envelope Response Analysis. , 2009, , . | | 4 |
| 28 | Switch-level fault coverage analysis for switched-capacitor systems. , 0, , . | | 3 |
| 29 | Practical solutions for the application of the oscillation-based-test: start-up and on-chip evaluation. , 0, , . | | 3 |
| 30 | Guidelines for the efficient design of sinewave generators for analog/mixed-signal BIST. , 2010, , . | | 3 |
| 31 | LP-LV high-performance monolithic DTMF receiver with on-chip test facilities. , 2003, , . | | 2 |
| 32 | A 2.4GHz LNA in a 90-nm CMOS technology designed with ACM model. , 2008, , . | | 2 |
| 33 | Practical implementation of a network analyzer for analog BIST applications. , 2008, , . | | 2 |
| 34 | Efficient functional built-in test for RF systems using two-tone response envelope analysis. , 2009, , . | | 2 |
| 35 | A Proposal for Yield Improvement with Power Tradeoffs in CMOS LNAs. IEEE Latin America Transactions, 2016, 14, 13-19. | 1.6 | 2 |
| 36 | A Switched Opamp-Based Bandpass Filter: Design and Implementation in a 0.35 μ m CMOS Technology. Analog Integrated Circuits and Signal Processing, 2003, 34, 201-209. | 1.4 | 1 |

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|----|--|-----|-----------|
| 37 | Practical Implementation of a Network Analyzer for Analog BIST Applications. , 2008, , . | | 1 |
| 38 | Practical test cores for the on-chip generation and evaluation of analog test signals: Application to a network/spectrum analyzer for analog BIST. , 2009, , . | | 1 |
| 39 | Analysis of process variations' impact on a 2.4 GHz 90 nm CMOS LNA. , 2013, , . | | 1 |
| 40 | Testing mixed-signal cores. , 0, , . | | 0 |
| 41 | Design of a switched opamp-based bandpass filter in a 0.35 μ m CMOS technology. , 0, , . | | 0 |
| 42 | Energy-Aware Low-Power CMOS LNA with Process-Variations Management. Active and Passive Electronic Components, 2016, 2016, 1-10. | 0.3 | 0 |