

Chao Luo

List of Publications by Year in descending order

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6
papers

91
citations

2682572

2
h-index

2550090

3
g-index

6
all docs

6
docs citations

6
times ranked

60
citing authors

#	ARTICLE	IF	CITATIONS
1	Side-channel power analysis of a GPU AES implementation. , 2015, , .		46
2	Side-channel Timing Attack of RSA on a GPU. Transactions on Architecture and Code Optimization, 2019, 16, 1-18.	2.0	17
3	Power Analysis Attack of an AES GPU Implementation. Journal of Hardware and Systems Security, 2018, 2, 69-82.	1.3	15
4	Side-channel power analysis of XTS-AES. , 2017, , .		7
5	GPU acceleration of RSA is vulnerable to side-channel timing attacks. , 2018, , .		4
6	Comprehensive Side-Channel Power Analysis of XTS-AES. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 2191-2200.	2.7	2