Syed Aziz

List of Publications by Year in descending order

72 papers	1,126 citations	471509 17 h-index	454955 30 g-index
77 all docs	77 docs citations	77 times ranked	1302 citing authors

#	Article	IF	Citations
1	Energy Economy of Households With Photovoltaic System and Battery Storage Under Time of Use Tariff With Demand Charge. IEEE Access, 2022, 10, 33069-33082.	4.2	9
2	Remote reconfiguration of FPGA-based wireless sensor nodes for flexible Internet of Things†. Computers and Electrical Engineering, 2022, 100, 107935.	4.8	3
3	Experimental validation of direction of arrival estimation for high chirp€rate linear frequency modulated radar signals. IET Radar, Sonar and Navigation, 2021, 15, 627-640.	1.8	3
4	Evaluating Energy Generation Capacity of PVDF Sensors: Effects of Sensor Geometry and Loading. Materials, 2021, 14, 1895.	2.9	11
5	FlexiS€"A Flexible Sensor Node Platform for the Internet of Things. Sensors, 2021, 21, 5154.	3.8	6
6	Low-Complexity Aggregation Techniques for DOA Estimation over Wide-RF Bandwidths. Electronics (Switzerland), 2021, 10, 1707.	3.1	2
7	Effects of high solar photovoltaic penetration on distribution feeders and the economic impact. Renewable and Sustainable Energy Reviews, 2020, 131, 110021.	16.4	43
8	Evaluation of aggregation techniques for DOA estimation of wideband radar signals. , 2020, , .		3
9	Response Time Determinism in Healthcare Data Analytics Using Machine Learning. Communications in Computer and Information Science, 2020, , 203-210.	0.5	0
10	PV generation and load profile data of net zero energy homes in South Australia. Data in Brief, 2019, 25, 104235.	1.0	18
11	Energy cost minimization for net zero energy homes through optimal sizing of battery storage system. Renewable Energy, 2019, 141, 278-286.	8.9	85
12	Comparative Evaluation of Alternative Tariffs on Energy Cost of Households with PV and Battery. , 2019, , .		4
13	Annual Electricity Cost Minimization for South Australian Dwellings through Optimal Battery Sizing. , 2019, , .		4
14	Application of Battery Energy Storage Systems to Enhance Power System Inertia. , 2019, , .		0
15	DOA estimation of wideband LFM RADAR signals. , 2019, , .		4
16	Structure and flexibility of LDPC codes. , 2018, , 11-28.		0
17	LDPC decoding algorithms. , 2018, , 29-53.		1
18	LDPC decoder architectures. , 2018, , 55-67.		0

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19	LDPC decoders in multimedia communication. , 2018, , 105-115.		0
20	Application of Battery Energy Storage System to Improve Damping of a Simple Power System. , 2018, , .		3
21	Optimal battery size for grid connected rooftop solar photovoltaic systems in South Australia. , 2017, , .		4
22	Identification of Foot Pathologies Based on Plantar Pressure Asymmetry. Sensors, 2015, 15, 20392-20408.	3.8	96
23	A simple and inexpensive enteric-coated capsule for delivery of acid-labile macromolecules to the small intestine. Journal of Zhejiang University: Science B, 2015, 16, 586-592.	2.8	16
24	Sensor Anomaly Detection in Wireless Sensor Networks for Healthcare. Sensors, 2015, 15, 8764-8786.	3.8	109
25	Resource efficient LDPC decoders for multimedia communication. The Integration VLSI Journal, 2015, 48, 213-220.	2.1	13
26	A Real-Time Localization System for an Endoscopic Capsule Using Magnetic Sensors. Sensors, 2014, 14, 20910-20929.	3.8	55
27	Review of Cyber-Physical System in Healthcare. International Journal of Distributed Sensor Networks, 2014, 10, 217415.	2.2	170
28	A real-time localization system for an endoscopic capsule. , 2014, , .		9
29	On Efficient Design of LDPC Decoders for Wireless Sensor Networks. Journal of Networks, 2014, 9, .	0.4	4
30	False Alarm Detection in Cyber-physical Systems for Healthcare Applications. AASRI Procedia, 2013, 5, 54-61.	0.6	17
31	Object extraction scheme and protocol for energy efficient image communication over wireless sensor networks. Computer Networks, 2013, 57, 2949-2960.	5.1	37
32	An energy efficient image compression scheme for Wireless Sensor Networks. , 2013, , .		3
33	Energy Efficient Image Transmission in Wireless Multimedia Sensor Networks. IEEE Communications Letters, 2013, 17, 1084-1087.	4.1	86
34	Storage node based routing protocol for Wireless Sensor Networks. , 2013, , .		3
35	Exploring ways that ePortfolios can support the progressive development of graduate qualities and professional competencies. Higher Education Research and Development, 2013, 32, 871-887.	2.9	21
36	A real-time tracking system for in vivo endofunctional capsule using magnetic sensors. , 2012, 2012, 598-601.		3

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37	An automated design methodology for FPGA-based Multi-Gbps LDPC decoders. , 2012, , .		5
38	Magnetic sensing technology for in vivo tracking. , 2012, , .		3
39	Efficient parallel architecture for multi-level forward discrete wavelet transform processors. Computers and Electrical Engineering, 2012, 38, 1325-1335.	4.8	25
40	An area efficient LDPC decoder using a reduced complexity min-sum algorithm. The Integration VLSI Journal, 2012, 45, 141-148.	2.1	28
41	A Highly Flexible LDPC Decoder using Hierarchical Quasi-Cyclic Matrix with Layered Permutation. Journal of Networks, 2012, 7, .	0.4	8
42	FPGA architecture for object extraction in Wireless Multimedia Sensor Network. , 2011, , .		8
43	FPGA-Based Image Processor Architecture for Wireless Multimedia Sensor Network. , 2011, , .		13
44	A multi-level Hierarchical Quasi-Cyclic matrix for implementation of flexible partially-parallel LDPC decoders. , 2011, , .		8
45	Two-Way Normalization of Min-Sum Decoding Algorithm for Medium and Short Length Low Density Parity Check Codes. , 2011, , .		4
46	FPGA Implementation of a LDPC Decoder using a Reduced Complexity Message Passing Algorithm. Journal of Networks, 2011, 6, .	0.4	24
47	FPGA Implementation of High Performance LDPC Decoder Using Modified 2-Bit Min-Sum Algorithm. , 2010, , .		26
48	Effective Teaching of the Physical Design of Integrated Circuits Using Educational Tools. IEEE Transactions on Education, 2010, 53, 517-531.	2.4	22
49	A near infrared instrument to monitor relative hemoglobin concentrations of human bone tissue *in vitro* and *in vivo*. Review of Scientific Instruments, 2010, 81, 043111.	1.3	18
50	Analysis of performance and implementation complexity of simplified algorithms for decoding Low-Density Parity-Check codes. , 2010, , .		8
51	Construction of a multi-level Hierarchical Quasi-Cyclic matrix with layered permutation for partially-parallel LDPC decoders. , 2010, , .		4
52	Implementation of Low Density Parity Check Decoders using a New High Level Design Methodology. Journal of Computers, 2010, 5, .	0.4	9
53	A cycle-accurate transaction level SystemC model for a serial communication bus. Computers and Electrical Engineering, 2009, 35, 790-802.	4.8	3
54	A reduced complexity message passing algorithm with improved performance for LDPC decoding. , 2009, , .		10

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55	New methodologies for high level modeling and synthesis of low density parity check decoders. , 2008, , .		2
56	A Real-Time Tracking System for an Endoscopic Capsule using Multiple Magnetic Sensors. Lecture Notes in Electrical Engineering, 2008, , 201-218.	0.4	21
57	A Conceptual Framework for a SIM-based Electronic Transaction Authentication System. , 2007, , .		0
58	Algorithms for Alignment of Co-Channel Speech Signals in Adaptive Speech Separation. , 2007, , .		0
59	Modeling for Performamnce: SystemC Model of A Communication Bus in A Distributed Network. , 2007, , .		0
60	Automated Speech Discrimination using Frequency Derivative Threshold Detection. , 2007, , .		1
61	A Conceptual Framework for a SIM-based Electronic Transaction Authentication System. , 2007, , .		0
62	Development of a Tracking Algorithm for an In-Vivo RF Capsule Prototype. , 2006, , .		17
63	High Speed Area Efficient Multi-resolution 2-D 9/7 filter DWT Processor. , 2006, , .		4
64	An electrically controlled micromachined drug delivery device employing two silicon wafers. , 2006, , .		0
65	Design of a Wireless Power Supply Receiver for Biomedical Applications. , 2006, , .		2
66	Design of an Area Efficient High-Speed Color FDWT Processor. , 2006, , .		0
67	A Bus Level SystemC Model for Evaluation of Avionics Mission System Data Bus. , 2005, , .		4
68	Testing differential split-level CMOS circuits. IET Circuits, Devices and Systems, 1994, 141, 451.	0.6	1
69	A high-speed two's complement multiplier using differential split-level CMOS. Microelectronics Journal, 1993, 24, 217-223.	2.0	0
70	Character recognition by double backpropagation neural network. , 0, , .		2
71	On C-testability of carry free dividers. , 0, , .		0
72	A synthesisable VHDL model for an easily testable generalised multiplier. , 0, , .		0