

Monodeep Kar

List of Publications by Year in descending order

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papers

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#	ARTICLE	IF	CITATIONS
1	Improved Power/EM Side-Channel Attack Resistance of 128-Bit AES Engines With Random Fast Voltage Dithering. IEEE Journal of Solid-State Circuits, 2019, 54, 569-583.	5.4	47
2	Reducing Power Side-Channel Information Leakage of AES Engines Using Fully Integrated Inductive Voltage Regulator. IEEE Journal of Solid-State Circuits, 2018, 53, 2399-2414.	5.4	45
3	8.1 Improved power-side-channel-attack resistance of an AES-128 core via a security-aware integrated buck voltage regulator. , 2017, , .		43
4	Energy Efficient and Side-Channel Secure Cryptographic Hardware for IoT-Edge Nodes. IEEE Internet of Things Journal, 2019, 6, 421-434.	8.7	38
5	An All-Digital Fully Integrated Inductive Buck Regulator With A 250-MHz Multi-Sampled Compensator and a Lightweight Auto-Tuner in 130-nm CMOS. IEEE Journal of Solid-State Circuits, 2017, 52, 1825-1835.	5.4	36
6	Enhanced Power and Electromagnetic SCA Resistance of Encryption Engines via a Security-Aware Integrated All-Digital LDO. IEEE Journal of Solid-State Circuits, 2020, 55, 478-493.	5.4	30
7	Exploring power attack protection of resource constrained encryption engines using integrated low-drop-out regulators. , 2015, , .		25
8	25.3 A 128b AES Engine with Higher Resistance to Power and Electromagnetic Side-Channel Attacks Enabled by a Security-Aware Integrated All-Digital Low-Dropout Regulator. , 2019, , .		24
9	Exploiting Fully Integrated Inductive Voltage Regulators to Improve Side Channel Resistance of Encryption Engines. , 2016, , .		18
10	Multigated Carbon Nanotube Field Effect Transistors-Based Physically Unclonable Functions As Security Keys. IEEE Internet of Things Journal, 2019, 6, 325-334.	8.7	14
11	Application Inference using Machine Learning based Side Channel Analysis. , 2019, , .		14
12	A 4900- μm^2 839-Mb/s Side-Channel Attack-Resistant AES-128 in 14-nm CMOS With Heterogeneous Sboxes, Linear Masked MixColumns, and Dual-Rail Key Addition. IEEE Journal of Solid-State Circuits, 2020, 55, 945-955.	5.4	13
13	Improved power side channel attack resistance of a 128-bit AES engine with random fast voltage dithering. , 2017, , .		9
14	Reducing Side-Channel Leakage of Encryption Engines Using Integrated Low-Dropout Voltage Regulators. Journal of Hardware and Systems Security, 2017, 1, 340-355.	1.3	5
15	Exploiting on-chip power management for side-channel security. , 2018, , .		4
16	A 2.9-33.0 TOPS/W Reconfigurable 1-D/2-D Compute-Near-Memory Inference Accelerator in 10-nm FinFET CMOS. IEEE Solid-State Circuits Letters, 2020, 3, 118-121.	2.0	4
17	Performance based tuning of an inductive integrated voltage regulator driving a digital core against process and passive variations. , 2018, , .		3
18	Autotuning of Integrated Inductive Voltage Regulator Using On-Chip Delay Sensor to Tolerate Process and Passive Variations. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 1768-1778.	3.1	3

#	ARTICLE	IF	CITATIONS
19	Securing IoT Devices Using Dynamic Power Management: Machine Learning Approach. IEEE Internet of Things Journal, 2021, 8, 16379-16394.	8.7	3
20	(Invited paper) energy delivery for self-powered IoT devices. , 2016, , .		2
21	A 9.0-TOPS/W Hash-Based Deep Neural Network Accelerator Enabling 128Å— Model Compression in 10-nm FinFET CMOS. IEEE Solid-State Circuits Letters, 2020, 3, 338-341.	2.0	2
22	A case for low frequency single cycle multi hop NoCs for energy efficiency and high performance. , 2017, , .		1
23	An Inductive Voltage Regulator With Overdrive Tracking Across Input Voltage in Cascoded Power Stage. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 3083-3087.	3.0	0
24	VDPred: Predicting Voltage Droop for Power-Effient 3D Multi-core Processor Design. , 2021, , .		0