

Jiro Ida

List of Publications by Year in descending order

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27
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citing authors

#	ARTICLE	IF	CITATIONS
1	P-Channel and N-Channel Super-Steep Subthreshold Slope PN-Body Tied SOI-FET for Ultralow Power CMOS. IEEE Journal of the Electron Devices Society, 2018, 6, 1213-1219.	2.1	16
2	Diode Characteristics of a Super-Steep Subthreshold Slope PN-Body Tied SOI-FET for Energy Harvesting Applications. IEEE Journal of the Electron Devices Society, 2018, 6, 565-570.	2.1	12
3	Confirmation of high efficiency on rectenna with high impedance antenna and optimized gate controlled diode for RF energy harvesting. , 2016, , .		8
4	Mechanism of super steep subthreshold slope characteristics with body-tied SOI MOSFET. , 2013, , .		5
5	RF Characteristics of Rectifier Devices for Ambient RF Energy Harvesting. , 2019, , .		5
6	Confirmation of $SS=35\text{Å}\mu\text{V}/\text{dec}$ over 3 decades of drain current and hole accumulation effect on PN-body tied SOI super steep SS FET's. , 2016, , .		4
7	First Experimental Confirmation of Ultralow Voltage Rectification by Super Steep Subthreshold Slope "PN-Body Tied SOI-FET" for High Efficiency RF Energy Harvesting and Ultralow Voltage Sensing. , 2018, , .		3
8	Gate controlled diode characteristics of super steep subthreshold slope PN-body tied SOI-FET for high efficiency RF energy harvesting. , 2017, , .		2
9	Characterization of Hysteresis in SOI-Based Super-Steep Subthreshold Slope FETs. IEICE Transactions on Electronics, 2018, E101.C, 334-337.	0.6	2
10	Analysis of Transient Effect on Super-Steep SS PN-Body Tied SOI-FET. , 2019, , .		2
11	Investigation of capacitor-less integrate and fire neuron by using dual-gate a PN-body tied silicon on insulator field-effect transistor. Japanese Journal of Applied Physics, 2021, 60, SBBA04.	1.5	2
12	Transient Characteristics on Super-Steep Subthreshold Slope "PN-Body Tied SOI-FET" Simulation and Pulse Measurement " ". IEICE Transactions on Electronics, 2020, E103.C, 533-542.	0.6	2
13	Optimization of gate controlled diode in rectenna for high efficiency RF energy harvesting. , 2015, , .		1
14	Design of high-impedance wideband folded dipole antennas for energy harvesting applications. , 2015, , .		1
15	Analysis of Drain Current Enhancement in "PN-Body Tied SOI-FET" -Bulk vs Surface Conduction Mode and Low V_{ds} Saturation Effect-. , 2021, , .		1
16	Transfer Characteristics of CMOS Inverter using "Steep SS PN-Body Tied SOI-FET". , 2021, , .		1
17	Neuron Function with Single Device by using "PN-Body Tied SOI-FET"-Mimicking Leaky Integrate and Fire Characteristics-. , 2022, , .		1
18	High efficiency RF energy harvesting with threshold-voltage-adjusted gate control diode. , 2014, , .		0

#	ARTICLE	IF	CITATIONS
19	High-impedance wideband card-type folded dipole antenna. , 2015, , .		0
20	Super steep subthreshold slope PN-Body Tied SOI FET for ultra low power IoT edge applications. , 2017, , .		0
21	Analysis of Super-Steep Subthreshold Slope Body-Tied SOI MOSFET and its Possibility for Ultralow Voltage Application. IEICE Transactions on Electronics, 2018, E101.C, 916-922.	0.6	0
22	Evaluation of Q_{ss} on SOI back Si/SiO ₂ interface by newly designed charge pumping method-TEG. , 2018, , .		0
23	Precise Transient Mechanism of Steep Subthreshold Slope PN-Body-Tied SOI-FET and Proposal of a New Structure for Reducing Leakage Current upon Turn-off. , 2019, , .		0
24	Super steep SS “PN-Body tied SOI-FET” with 65 nm thin Box FD-SOI. , 2019, , .		0
25	Analysis of Steep SS Mechanism on PN-Body Tied SOI-FET with 65 nm Thin Box FD-SOI. , 2020, , .		0
26	Effect of V_{sub} and Positive Charge in Buried Oxide on Super Steep SS “PN Body-Tied SOI-FET” and Proposal of CMOS without V_{sub} Bias. , 2019, , .		0
27	RF Evaluation of Steep Subthreshold Slope “PN-Body Tied SOI-FET” , 2021, , .		0