

# Ashish Singhadia

## List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Efficient HEVC Encoding to Meet Bitrate and PSNR Requirements Using Parametric Modeling. <i>Circuits, Systems, and Signal Processing</i> , 2022, 41, 4479-4511.	2.0	4
2	A fast integrated deblocking filter and sample-adaptive-offset parameter estimation architecture for HEVC. <i>Microprocessors and Microsystems</i> , 2021, 85, 104317.	2.8	4
3	Hardware-Efficient 2D-DCT/IDCT Architecture for Portable HEVC-Compliant Devices. <i>IEEE Transactions on Consumer Electronics</i> , 2020, 66, 203-212.	3.6	17
4	A Novel Algorithmic Approach for Efficient Realization of 2-D-DCT Architecture for HEVC. <i>IEEE Transactions on Consumer Electronics</i> , 2019, 65, 264-273.	3.6	16