

Ashish Singhadia

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Hardware-Efficient 2D-DCT/IDCT Architecture for Portable HEVC-Compliant Devices. IEEE Transactions on Consumer Electronics, 2020, 66, 203-212.	3.6	17
2	A Novel Algorithmic Approach for Efficient Realization of 2-D-DCT Architecture for HEVC. IEEE Transactions on Consumer Electronics, 2019, 65, 264-273.	3.6	16
3	A fast integrated deblocking filter and sample-adaptive-offset parameter estimation architecture for HEVC. Microprocessors and Microsystems, 2021, 85, 104317.	2.8	4
4	Efficient HEVC Encoding to Meet Bitrate and PSNR Requirements Using Parametric Modeling. Circuits, Systems, and Signal Processing, 2022, 41, 4479-4511.	2.0	4