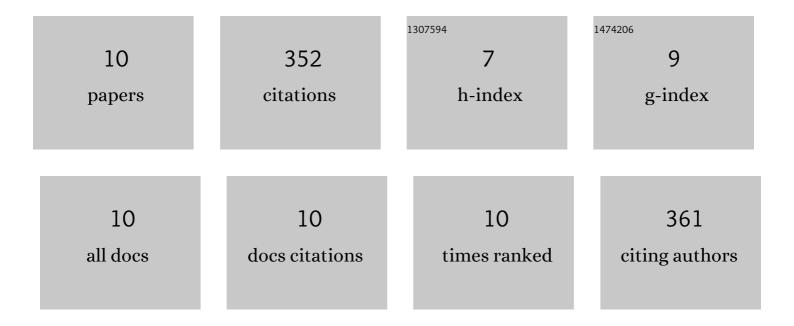
Jinbiao Liu

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	Improving Driving Current with High-Efficiency Landing Pads Technique for Reduced Parasitic Resistance in Gate-All-Around Si Nanosheet Devices. ECS Journal of Solid State Science and Technology, 2022, 11, 035010.	1.8	4
2	Low-Temperature (â‰ 9 00 °C) Complementary Schottky Source/Drain FinFETs for 3D Sequential Integration. Nanomaterials, 2022, 12, 1218.	4.1	0
3	Review of Si-Based GeSn CVD Growth and Optoelectronic Applications. Nanomaterials, 2021, 11, 2556.	4.1	42
4	Study of n-type doping in germanium by temperature based PF+ implantation. Journal of Materials Science: Materials in Electronics, 2020, 31, 161-166.	2.2	4
5	State of the Art and Future Perspectives in Advanced CMOS Technology. Nanomaterials, 2020, 10, 1555.	4.1	115
6	Miniaturization of CMOS. Micromachines, 2019, 10, 293.	2.9	81
7	Integration of Highly Strained SiGe in Source and Drain with HK and MG for 22Ânm Bulk PMOS Transistors. Nanoscale Research Letters, 2017, 12, 123.	5.7	22
8	pMOSFETs Featuring ALD W Filling Metal Using SiH4 and B2H6 Precursors in 22 nm Node CMOS Technology. Nanoscale Research Letters, 2017, 12, 306.	5.7	13
9	The Challenges of Advanced CMOS Process from 2D to 3D. Applied Sciences (Switzerland), 2017, 7, 1047.	2.5	53
10	Defect engineering for shallow nâ€ŧype junctions in germanium: Facts and fiction. Physica Status Solidi (A) Applications and Materials Science, 2016, 213, 2799-2808.	1.8	18