

Aurelio Mannara

List of Publications by Year in descending order

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Version: 2024-02-01

4
papers

95
citations

1937685

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2272923

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4
docs citations

4
times ranked

68
citing authors

#	ARTICLE	IF	CITATIONS
1	A comparison of modeling approaches for current transport in polysilicon-channel nanowire and macaroni GAA MOSFETs. Journal of Computational Electronics, 2021, 20, 537-544.	2.5	5
2	Compact modeling of GIDL-assisted erase in 3-D NAND Flash strings. Journal of Computational Electronics, 2019, 18, 561-568.	2.5	13
3	Characterization and Modeling of Temperature Effects in 3-D NAND Flash Arraysâ€™Part II: Random Telegraph Noise. IEEE Transactions on Electron Devices, 2018, 65, 3207-3213.	3.0	25
4	Characterization and Modeling of Temperature Effects in 3-D NAND Flash Arraysâ€™Part I: Polysilicon-Induced Variability. IEEE Transactions on Electron Devices, 2018, 65, 3199-3206.	3.0	52