

Bashir Mohammad Sabquat Bahar Taluk

List of Publications by Year in descending order

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52
citing authors

#	ARTICLE	IF	CITATIONS
1	Approximate MRAM: High-performance and Power-efficient Computing with MRAM Chips for Error-tolerant Applications. IEEE Transactions on Computers, 2022, , 1-1.	3.4	3
2	DRAM Retention Behavior with Accelerated Aging in Commercial Chips. Applied Sciences (Switzerland), 2022, 12, 4332.	2.5	5
3	True Random Number Generation Using Latency Variations of FRAM. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 14-23.	3.1	13
4	Memory-Based PUFs are Vulnerable as Well: A Non-Invasive Attack Against SRAM PUFs. IEEE Transactions on Information Forensics and Security, 2021, 16, 4035-4049.	6.9	12
5	True Random Number Generation using Latency Variations of Commercial MRAM Chips. , 2021, , .		6
6	PreLatPUF: Exploiting DRAM Latency Variations for Generating Robust Device Signatures. IEEE Access, 2019, 7, 81106-81120.	4.2	13
7	Exploiting DRAM Latency Variations for Generating True Random Numbers. , 2019, , .		13
8	Non-Invasive Detection Method for Recycled Flash Memory Using Timing Characteristics â€. Cryptography, 2018, 2, 17.	2.3	14