

Kai Kang

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/6626686/publications.pdf>

Version: 2024-02-01

10

papers

251

citations

1684188

5

h-index

1720034

7

g-index

10

all docs

10

docs citations

10

times ranked

257

citing authors

#	ARTICLE	IF	CITATIONS
1	An Improved Surface-Potential-Based Model for MOSFETs Considering the Carrier Gaussian Distribution. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2020, 68, 4082-4090.	4.6	5
2	A Ku-band Miniaturized LNA in 0.18- μ m CMOS Process for Low-cost Phased Array Application. , 2019, , .		9
3	66 GHz bias-dependent equivalent circuit model for CMOS transistor based on 90 nanometers CMOS technology. <i>Microwave and Optical Technology Letters</i> , 2018, 60, 1808-1812.	1.4	2
4	A 220-GHz Compact Equivalent Circuit Model of CMOS Transistors. <i>IEEE Microwave and Wireless Components Letters</i> , 2017, 27, 651-653.	3.2	11
5	Analysis and Design of CMOS Doherty Power Amplifier Based on Voltage Combining Method. <i>IEEE Access</i> , 2017, 5, 5001-5012.	4.2	17
6	A 54.4-90 GHz Low-Noise Amplifier in 65-nm CMOS. <i>IEEE Journal of Solid-State Circuits</i> , 2017, 52, 2892-2904.	5.4	91
7	High-Isolation CMOS T/R Switch Design Using a Two-Stage Equivalent Transmission Line Structure. <i>IEEE Access</i> , 2017, 5, 22704-22712.	4.2	1
8	A 60-GHz on-chip antenna using standard 0.18- μ m CMOS technology. , 2014, , .		3
9	A 60-GHz on-chip antenna using standard 0.18μm CMOS technology. , 2014, , .		0
10	A 60-GHz OOK Receiver With an On-Chip Antenna in 90 nm CMOS. <i>IEEE Journal of Solid-State Circuits</i> , 2010, 45, 1720-1731.	5.4	112