

# Kai Kang

## List of Publications by Year in descending order

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Version: 2024-02-01

10  
papers

251  
citations

1684188

5  
h-index

1720034

7  
g-index

10  
all docs

10  
docs citations

10  
times ranked

257  
citing authors

#	ARTICLE	IF	CITATIONS
1	A 60-GHz OOK Receiver With an On-Chip Antenna in 90 nm CMOS. IEEE Journal of Solid-State Circuits, 2010, 45, 1720-1731.	5.4	112
2	A 54.4â€“90 GHz Low-Noise Amplifier in 65-nm CMOS. IEEE Journal of Solid-State Circuits, 2017, 52, 2892-2904.	5.4	91
3	Analysis and Design of CMOS Doherty Power Amplifier Based on Voltage Combining Method. IEEE Access, 2017, 5, 5001-5012.	4.2	17
4	A 220-GHz Compact Equivalent Circuit Model of CMOS Transistors. IEEE Microwave and Wireless Components Letters, 2017, 27, 651-653.	3.2	11
5	A Ku-band Miniaturized LNA in 0.18-Åµm CMOS Process for Low-cost Phased Array Application. , 2019, , .		9
6	An Improved Surface-Potential-Based Model for MOSFETs Considering the Carrier Gaussian Distribution. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 4082-4090.	4.6	5
7	A 60-GHz on-chip antenna using standard 0.18&#x03BC;m CMOS technology. , 2014, , .		3
8	66 GHz biasâ€“dependent equivalent circuit model for CMOS transistor based on 90 nanometers CMOS technology. Microwave and Optical Technology Letters, 2018, 60, 1808-1812.	1.4	2
9	High-Isolation CMOS T/R Switch Design Using a Two-Stage Equivalent Transmission Line Structure. IEEE Access, 2017, 5, 22704-22712.	4.2	1
10	A 60-GHz on-chip antenna using standard 0.18&#x03BC;m CMOS technology. , 2014, , .		0