

Soyoung Cha

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	DSIP: A Scalable Inference Accelerator for Convolutional Neural Networks. IEEE Journal of Solid-State Circuits, 2018, 53, 605-618.	5.4	48
2	Low-Complexity Low-Latency Architecture for Matching of Data Encoded With Hard Systematic Error-Correcting Codes. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 1648-1652.	3.1	6