

Praveen J

List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

2

papers

3

citations

1

h-index

1

g-index

14

ext. papers

4

ext. citations

0.8

avg, IF

0.2

L-index

#	Paper	IF	Citations
2	BIST-Based Low Power Test Vector Generator and Minimizing Bulkiness of VLSI Architecture. <i>Journal of Circuits, Systems and Computers</i> , 2018 , 27, 1850078	0.9	2
1	Design and implementation of low power dynamic buffer circuit for nanotechnology application. <i>Materials Today: Proceedings</i> , 2021 , 35, 461-464	1.4	1