

# Leonard R Rockett

## List of Publications by Year in descending order

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15  
papers

306  
citations

1307594

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1588992

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all docs

15  
docs citations

15  
times ranked

170  
citing authors

#	ARTICLE	IF	CITATIONS
1	Characterization and qualification of radiation hardened nonvolatile phase change memory technology. , 2010, , .		6
2	A radiation hardened reconfigurable FPGA. , 2009, , .		12
3	Radiation Hardened 150nm Standard Cell ASIC Design Library for Space Applications. Aerospace Conference Proceedings IEEE, 2008, , .	0.0	7
4	A 4-Mb Non-volatile Chalcogenide Random Access Memory designed for space applications: Project status update. , 2008, , .		14
5	Radiation Hardened FPGA Technology for Space Applications. , 2007, , .		31
6	Proton and gamma radiation effects in a new first-generation SiGe HBT technology. Solid-State Electronics, 2006, 50, 181-190.	1.4	29
7	Structured ASIC Design for Space Systems Applications. , 2005, , .		1
8	Design Considerations for Next Generation Radiation Hardened SRAMs for Space Applications. , 2005, , .		5
9	Designing CMOS data cells for space systems. Microelectronics Journal, 2004, 35, 953-967.	2.0	8
10	A design based on proven concepts of an SEU-immune CMOS configuration data cell for reprogrammable FPGAs. Microelectronics Journal, 2001, 32, 99-111.	2.0	10
11	Development of a radiation tolerant 1M SRAM on fully-depleted SOI. IEEE Transactions on Nuclear Science, 1998, 45, 2436-2441.	2.0	12
12	Simulated SEU hardened scaled CMOS SRAM cell design using gated resistors. IEEE Transactions on Nuclear Science, 1992, 39, 1532-1541.	2.0	40
13	Cost-effective numerical simulation of SEU. IEEE Transactions on Nuclear Science, 1988, 35, 1608-1612.	2.0	21
14	An SEU-hardened CMOS data latch design. IEEE Transactions on Nuclear Science, 1988, 35, 1682-1687.	2.0	105
15	High performance radiation hardened static random access memory (SRAM) design for space applications. , 0, , .		5