## **Hector Gomez**

## List of Publications by Year in descending order

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2258059 2550090 6 34 3 3 citations h-index g-index papers 6 6 6 47 citing authors all docs docs citations times ranked

#	Article	IF	CITATIONS
1	Lowâ€cost TRNG IPs. IET Circuits, Devices and Systems, 2020, 14, 942-946.	1.4	3
2	Compact onâ€theâ€flyâ€enabled termination with highâ€current density and ESD compliance. IET Circuits, Devices and Systems, 2020, 14, 788-795.	1.4	0
3	Defeating Silicon Reverse Engineering Using a Layout-Level Standard Cell Camouflage. IEEE Transactions on Consumer Electronics, 2019, 65, 109-118.	3.6	8
4	Standard cell camouflage method to counter silicon reverse engineering. , $2018, \ldots$		4
5	A 65 nm CMOS key establishment core based on tree parity machines. The Integration VLSI Journal, 2017, 58, 430-437.	2.1	10
6	A system-on-chip platform for the internet of things featuring a 32-bit RISC-V based microcontroller. , 2017, , .		9