

# Hector Gomez

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/6582395/publications.pdf>

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6  
papers

34  
citations

2258059

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h-index

2550090

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g-index

6  
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6  
docs citations

6  
times ranked

47  
citing authors

#	ARTICLE	IF	CITATIONS
1	A 65 nm CMOS key establishment core based on tree parity machines. The Integration VLSI Journal, 2017, 58, 430-437.	2.1	10
2	A system-on-chip platform for the internet of things featuring a 32-bit RISC-V based microcontroller. , 2017, , .		9
3	Defeating Silicon Reverse Engineering Using a Layout-Level Standard Cell Camouflage. IEEE Transactions on Consumer Electronics, 2019, 65, 109-118.	3.6	8
4	Standard cell camouflage method to counter silicon reverse engineering. , 2018, , .		4
5	Low-cost TRNG IPs. IET Circuits, Devices and Systems, 2020, 14, 942-946.	1.4	3
6	Compact on-chip flywheel-enabled termination with high-current density and ESD compliance. IET Circuits, Devices and Systems, 2020, 14, 788-795.	1.4	0