

Kees Middelburg

List of Publications by Year in descending order

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54
papers

360
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1040056

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17
g-index

58
all docs

58
docs citations

58
times ranked

100
citing authors

#	ARTICLE	IF	CITATIONS
1	Imperative Process Algebra with Abstraction. Scientific Annals of Computer Science, 2022, XXXII, 137-179.	0.1	1
2	Using Hoare Logic in a Process Algebra Setting. Fundamenta Informaticae, 2021, 179, 321-344.	0.4	7
3	On the strongest three-valued paraconsistent logic contained in classical logic and its dual. Journal of Logic and Computation, 2021, 31, 597-611.	0.8	1
4	Probabilistic Process Algebra and Strategic Interleaving. Scientific Annals of Computer Science, 2020, 30, 205-243.	0.1	4
5	Process Algebra with Strategic Interleaving. Theory of Computing Systems, 2019, 63, 488-505.	1.1	3
6	Program Algebra for Turing-Machine Programs. Scientific Annals of Computer Science, 2019, 19, 113-139.	0.1	0
7	Contradiction-Tolerant Process Algebra with Propositional Signals. Fundamenta Informaticae, 2017, 153, 29-55.	0.4	1
8	Instruction Sequence Size Complexity of Parity. Fundamenta Informaticae, 2016, 149, 297-309.	0.4	4
9	Transformation of fractions into simple fractions in divisive meadows. Journal of Applied Logic, 2016, 16, 92-110.	1.1	5
10	On Algorithmic Equivalence of Instruction Sequences for Computing Bit String Functions. Fundamenta Informaticae, 2015, 138, 411-434.	0.4	2
11	Division by zero in non-involutive meadows. Journal of Applied Logic, 2015, 13, 1-12.	1.1	8
12	A Process Calculus with Finitary Comprehended Terms. Theory of Computing Systems, 2013, 53, 645-668.	1.1	4
13	Data Linkage Algebra, Data Linkage Dynamics, and Priority Rewriting. Fundamenta Informaticae, 2013, 128, 367-412.	0.4	1
14	On the Behaviours Produced by Instruction Sequences under Execution. Fundamenta Informaticae, 2012, 120, 111-144.	0.4	1
15	Instruction sequence processing operators. Acta Informatica, 2012, 49, 139-172.	0.5	5
16	On the Expressiveness of Single-Pass Instruction Sequences. Theory of Computing Systems, 2012, 50, 313-328.	1.1	5
17	Indirect Jumps Improve Instruction Sequence Performance. Scientific Annals of Computer Science, 2012, , 253-265.	0.1	2
18	Inversive meadows and divisive meadows. Journal of Applied Logic, 2011, 9, 203-220.	1.1	13

#	ARTICLE	IF	CITATIONS
19	Thread algebra for poly-threading. <i>Formal Aspects of Computing</i> , 2011, 23, 567-583.	1.8	1
20	Data Linkage Dynamics with Shedding. <i>Fundamenta Informaticae</i> , 2010, 103, 31-52.	0.4	2
21	On the operating unit size of load/store architectures. <i>Mathematical Structures in Computer Science</i> , 2010, 20, 395-417.	0.6	2
22	An Interface Group for Process Components. <i>Fundamenta Informaticae</i> , 2010, 99, 355-382.	0.4	2
23	Instruction Sequences with Dynamically Instantiated Instructions. <i>Fundamenta Informaticae</i> , 2009, 96, 27-48.	0.4	2
24	Machine structure oriented control code logic. <i>Acta Informatica</i> , 2009, 46, 375-401.	0.5	0
25	Parallel Processes with Implicit Computational Capital. <i>Electronic Notes in Theoretical Computer Science</i> , 2008, 209, 55-81.	0.9	1
26	Simulating Turing machines on Maurer machines. <i>Journal of Applied Logic</i> , 2008, 6, 1-23.	1.1	1
27	Program algebra with a jump-shift instruction. <i>Journal of Applied Logic</i> , 2008, 6, 553-563.	1.1	7
28	Distributed strategic interleaving with load balancing. <i>Future Generation Computer Systems</i> , 2008, 24, 530-548.	7.5	8
29	Maurer computers for pipelined instruction processing. <i>Mathematical Structures in Computer Science</i> , 2008, 18, .	0.6	6
30	Preferential choice and coordination conditions. <i>The Journal of Logic and Algebraic Programming</i> , 2007, 70, 172-200.	1.4	6
31	A Thread Algebra with Multi-Level Strategic Interleaving. <i>Theory of Computing Systems</i> , 2007, 41, 3-32.	1.1	9
32	Synchronous cooperation for explicit multi-threading. <i>Acta Informatica</i> , 2007, 44, 525-569.	0.5	3
33	Thread algebra for strategic interleaving. <i>Formal Aspects of Computing</i> , 2007, 19, 445-474.	1.8	18
34	Conditionals in Algebraic Process Calculi. <i>Electronic Notes in Theoretical Computer Science</i> , 2006, 162, 237-241.	0.9	0
35	Splitting bisimulations and retrospective conditions. <i>Information and Computation</i> , 2006, 204, 1083-1138.	0.7	10
36	A Thread Algebra with Multi-level Strategic Interleaving. <i>Lecture Notes in Computer Science</i> , 2005, , 35-48.	1.3	8

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37	Model Theory for Process Algebra. Lecture Notes in Computer Science, 2005, , 445-495.	1.3	3
38	An alternative formulation of operational conservativity with binding terms. The Journal of Logic and Algebraic Programming, 2003, 55, 1-19.	1.4	10
39	Process Algebra with Timing. Monographs in Theoretical Computer Science, 2002, , .	0.6	69
40	Variable binding operators in transition system specifications. The Journal of Logic and Algebraic Programming, 2001, 47, 15-45.	1.4	17
41	Real time process algebra with time-dependent conditions. The Journal of Logic and Algebraic Programming, 2001, 48, 1-38.	1.4	3
42	Truth of Duration Calculus Formulae in Timed Frames. Fundamenta Informaticae, 1998, 36, 235-263.	0.4	3
43	Network algebra for asynchronous dataflow. International Journal of Computer Mathematics, 1997, 65, 57-88.	1.8	8
44	Algebra of timed frames. International Journal of Computer Mathematics, 1996, 61, 227-255.	1.8	3
45	A typed logic of partial functions reconstructed classically. Acta Informatica, 1994, 31, 399-430.	0.5	52
46	Specification of interfering programs based on interconditions. Software Engineering Journal, 1992, 7, 205.	0.7	0
47	Modular structuring of VDM specifications in VVSL. Formal Aspects of Computing, 1992, 4, 13-47.	1.8	0
48	VVSL Specification of a Transaction-oriented Access Handler. Workshops in Computing, 1992, , 188-212.	0.4	0
49	LPF and MPL. A logical comparison of VDM SL and COLD-K. Lecture Notes in Computer Science, 1991, , 279-308.	1.3	4
50	Experiences with combining formalisms in VVSL. Lecture Notes in Computer Science, 1991, , 83-103.	1.3	1
51	VVSL: A language for structured VDM specifications. Formal Aspects of Computing, 1989, 1, 115-135.	1.8	20
52	The VIP VDM Specification Language. Lecture Notes in Computer Science, 1988, , 187-201.	1.3	6
53	The effect of the PDP-11 architecture on code generation for chill. ACM SIGPLAN Notices, 1982, 17, 149-157.	0.2	0
54	The effect of the PDP-11 architecture on code generation for chill. , 1982, , .		0