

Kees Middelburg

List of Publications by Year in descending order

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54
papers

360
citations

1040056

9
h-index

888059

17
g-index

58
all docs

58
docs citations

58
times ranked

100
citing authors

#	ARTICLE	IF	CITATIONS
1	Process Algebra with Timing. Monographs in Theoretical Computer Science, 2002, , .	0.6	69
2	A typed logic of partial functions reconstructed classically. Acta Informatica, 1994, 31, 399-430.	0.5	52
3	VVSL: A language for structured VDM specifications. Formal Aspects of Computing, 1989, 1, 115-135.	1.8	20
4	Thread algebra for strategic interleaving. Formal Aspects of Computing, 2007, 19, 445-474.	1.8	18
5	Variable binding operators in transition system specifications. The Journal of Logic and Algebraic Programming, 2001, 47, 15-45.	1.4	17
6	Inversive meadows and divisive meadows. Journal of Applied Logic, 2011, 9, 203-220.	1.1	13
7	An alternative formulation of operational conservativity with binding terms. The Journal of Logic and Algebraic Programming, 2003, 55, 1-19.	1.4	10
8	Splitting bisimulations and retrospective conditions. Information and Computation, 2006, 204, 1083-1138.	0.7	10
9	A Thread Algebra with Multi-Level Strategic Interleaving. Theory of Computing Systems, 2007, 41, 3-32.	1.1	9
10	Network algebra for asynchronous dataflow. International Journal of Computer Mathematics, 1997, 65, 57-88.	1.8	8
11	A Thread Algebra with Multi-level Strategic Interleaving. Lecture Notes in Computer Science, 2005, , 35-48.	1.3	8
12	Distributed strategic interleaving with load balancing. Future Generation Computer Systems, 2008, 24, 530-548.	7.5	8
13	Division by zero in non-involutive meadows. Journal of Applied Logic, 2015, 13, 1-12.	1.1	8
14	Program algebra with a jump-shift instruction. Journal of Applied Logic, 2008, 6, 553-563.	1.1	7
15	Using Hoare Logic in a Process Algebra Setting. Fundamenta Informaticae, 2021, 179, 321-344.	0.4	7
16	Preferential choice and coordination conditions. The Journal of Logic and Algebraic Programming, 2007, 70, 172-200.	1.4	6
17	Maurer computers for pipelined instruction processing. Mathematical Structures in Computer Science, 2008, 18, .	0.6	6
18	The VIP VDM Specification Language. Lecture Notes in Computer Science, 1988, , 187-201.	1.3	6

#	ARTICLE	IF	CITATIONS
19	Instruction sequence processing operators. <i>Acta Informatica</i> , 2012, 49, 139-172.	0.5	5
20	On the Expressiveness of Single-Pass Instruction Sequences. <i>Theory of Computing Systems</i> , 2012, 50, 313-328.	1.1	5
21	Transformation of fractions into simple fractions in divisive meadows. <i>Journal of Applied Logic</i> , 2016, 16, 92-110.	1.1	5
22	A Process Calculus with Finitary Comprehended Terms. <i>Theory of Computing Systems</i> , 2013, 53, 645-668.	1.1	4
23	Instruction Sequence Size Complexity of Parity. <i>Fundamenta Informaticae</i> , 2016, 149, 297-309.	0.4	4
24	LPF and MPLI%o "â€" A logical comparison of VDM SL and COLD-K. <i>Lecture Notes in Computer Science</i> , 1991, , 279-308.	1.3	4
25	Probabilistic Process Algebra and Strategic Interleaving. <i>Scientific Annals of Computer Science</i> , 2020, 30, 205-243.	0.1	4
26	Algebra of timed frames. <i>International Journal of Computer Mathematics</i> , 1996, 61, 227-255.	1.8	3
27	Truth of Duration Calculus Formulae in Timed Frames. <i>Fundamenta Informaticae</i> , 1998, 36, 235-263.	0.4	3
28	Real time process algebra with time-dependent conditions. <i>The Journal of Logic and Algebraic Programming</i> , 2001, 48, 1-38.	1.4	3
29	Synchronous cooperation for explicit multi-threading. <i>Acta Informatica</i> , 2007, 44, 525-569.	0.5	3
30	Process Algebra with Strategic Interleaving. <i>Theory of Computing Systems</i> , 2019, 63, 488-505.	1.1	3
31	Model Theory for Process Algebra. <i>Lecture Notes in Computer Science</i> , 2005, , 445-495.	1.3	3
32	Instruction Sequences with Dynamically Instantiated Instructions. <i>Fundamenta Informaticae</i> , 2009, 96, 27-48.	0.4	2
33	Data Linkage Dynamics with Shedding. <i>Fundamenta Informaticae</i> , 2010, 103, 31-52.	0.4	2
34	On the operating unit size of load/store architectures. <i>Mathematical Structures in Computer Science</i> , 2010, 20, 395-417.	0.6	2
35	An Interface Group for Process Components. <i>Fundamenta Informaticae</i> , 2010, 99, 355-382.	0.4	2
36	On Algorithmic Equivalence of Instruction Sequences for Computing Bit String Functions. <i>Fundamenta Informaticae</i> , 2015, 138, 411-434.	0.4	2

#	ARTICLE	IF	CITATIONS
37	Indirect Jumps Improve Instruction Sequence Performance. Scientific Annals of Computer Science, 2012, , 253-265.	0.1	2
38	Parallel Processes with Implicit Computational Capital. Electronic Notes in Theoretical Computer Science, 2008, 209, 55-81.	0.9	1
39	Simulating Turing machines on Maurer machines. Journal of Applied Logic, 2008, 6, 1-23.	1.1	1
40	Thread algebra for poly-threading. Formal Aspects of Computing, 2011, 23, 567-583.	1.8	1
41	On the Behaviours Produced by Instruction Sequences under Execution. Fundamenta Informaticae, 2012, 120, 111-144.	0.4	1
42	Data Linkage Algebra, Data Linkage Dynamics, and Priority Rewriting. Fundamenta Informaticae, 2013, 128, 367-412.	0.4	1
43	Contradiction-Tolerant Process Algebra with Propositional Signals. Fundamenta Informaticae, 2017, 153, 29-55.	0.4	1
44	Experiences with combining formalisms in VVSL. Lecture Notes in Computer Science, 1991, , 83-103.	1.3	1
45	On the strongest three-valued paraconsistent logic contained in classical logic and its dual. Journal of Logic and Computation, 2021, 31, 597-611.	0.8	1
46	Imperative Process Algebra with Abstraction. Scientific Annals of Computer Science, 2022, XXXII, 137-179.	0.1	1
47	The effect of the PDP-11 architecture on code generation for chill. ACM SIGPLAN Notices, 1982, 17, 149-157.	0.2	0
48	The effect of the PDP-11 architecture on code generation for chill. , 1982, , .		0
49	Specification of interfering programs based on interconditions. Software Engineering Journal, 1992, 7, 205.	0.7	0
50	Modular structuring of VDM specifications in VVSL. Formal Aspects of Computing, 1992, 4, 13-47.	1.8	0
51	Conditionals in Algebraic Process Calculi. Electronic Notes in Theoretical Computer Science, 2006, 162, 237-241.	0.9	0
52	Machine structure oriented control code logic. Acta Informatica, 2009, 46, 375-401.	0.5	0
53	VVSL Specification of a Transaction-oriented Access Handler. Workshops in Computing, 1992, , 188-212.	0.4	0
54	Program Algebra for Turing-Machine Programs. Scientific Annals of Computer Science, 2019, 19, 113-139.	0.1	0