

Yuval Cassuto

List of Publications by Year in descending order

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papers

685
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67
all docs

67
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67
times ranked

490
citing authors

#	ARTICLE	IF	CITATIONS
1	Codes for Asymmetric Limited-Magnitude Errors With Application to Multilevel Flash Memories. IEEE Transactions on Information Theory, 2010, 56, 1582-1595.	1.5	117
2	Sneak-path constraints in memristor crossbar arrays. , 2013, , .		83
3	Online Fountain Codes With Low Overhead. IEEE Transactions on Information Theory, 2015, 61, 3137-3149.	1.5	46
4	Information-Theoretic Sneak-Path Mitigation in Memristor Crossbar Arrays. IEEE Transactions on Information Theory, 2016, 62, 4801-4813.	1.5	45
5	Low Complexity Encoding for Network Codes. , 2006, , .		34
6	Channel Coding for Nonvolatile Memory Technologies: Theoretical Advances and Practical Considerations. Proceedings of the IEEE, 2017, 105, 1705-1724.	16.4	30
7	Burst-Erasure Correcting Codes with Optimal Average Delay. IEEE Transactions on Information Theory, 2017, , 1-1.	1.5	29
8	Codes for network switches. , 2013, , .		23
9	NAND flash architectures reducing write amplification through multi-write codes. , 2014, , .		23
10	Optimal binary switch codes with small query size. , 2015, , .		20
11	Short \mathbb{F}_q -ary Fixed-Rate WOM Codes for Guaranteed Rewrites and With Hot/Cold Write Differentiation. IEEE Transactions on Information Theory, 2014, 60, 3942-3958.	1.5	17
12	Detection and Coding Schemes for Sneak-Path Interference in Resistive Memory Arrays. IEEE Transactions on Communications, 2019, 67, 3821-3833.	4.9	14
13	Switch Codes: Codes for Fully Parallel Reconstruction. IEEE Transactions on Information Theory, 2017, 63, 2061-2075.	1.5	13
14	Multi-block interleaved codes for local and global read access. , 2017, , .		13
15	Iterative Decoding of LDPC Codes Over the \mathbb{F}_q -ary Partial Erasure Channel. IEEE Transactions on Information Theory, 2016, 62, 2658-2672.	1.5	12
16	LDPC Codes with Local and Global Decoding. , 2018, , .		11
17	Consecutive Switch Codes. IEEE Transactions on Information Theory, 2018, 64, 2485-2498.	1.5	10
18	On the Average Complexity of Reed-Solomon List Decoders. IEEE Transactions on Information Theory, 2013, 59, 2336-2351.	1.5	9

#	ARTICLE	IF	CITATIONS
19	On-line fountain codes for semi-random loss channels. , 2011, , .		8
20	LDPC codes for partial-erasure channels in multi-level memories. , 2014, , .		7
21	In-memory hamming similarity computation in resistive arrays. , 2015, , .		7
22	Low-complexity wire-tap codes with security and error-correction guarantees. , 2010, , .		6
23	LDPC Codes for 2D Arrays. IEEE Transactions on Information Theory, 2014, 60, 3279-3291.	1.5	6
24	On the channel induced by sneak-path errors in memristor arrays. , 2014, , .		6
25	ϵ -Imbalance WOM Codes for Reduced Inter-Cell Interference in Multi-Level NVMs. IEEE Journal on Selected Areas in Communications, 2016, 34, 2378-2390.	9.7	6
26	Write sneak-path constraints avoiding disturbs in memristor crossbar arrays. , 2016, , .		6
27	Spatially Coupled LDPC Codes With Sub-Block Locality. IEEE Transactions on Information Theory, 2021, 67, 2739-2757.	1.5	6
28	Short q-ary WOM codes with hot/cold write differentiation. , 2012, , .		5
29	Performance Coding: Codes for Fast Write and Read in Multi-Level NVMs. IEEE Transactions on Communications, 2015, 63, 581-591.	4.9	5
30	Spatially Coupled LDPC Codes with Random Access. , 2018, , .		5
31	Erasure Correction of Scalar Codes in the Presence of Stragglers. , 2018, , .		5
32	Array-code ensembles -or- two-dimensional LDPC codes. , 2011, , .		4
33	Algorithms and throughput analysis for MDS-coded switches. , 2015, , .		4
34	LDPC Codes Over the q -ary Multi-Bit Channel. IEEE Transactions on Information Theory, 2019, 65, 4293-4306.	1.5	4
35	Efficient Compression of Long Arbitrary Sequences With No Reference at the Encoder. IEEE Transactions on Information Theory, 2021, 67, 1-9.	1.5	4
36	Delete-and-Conquer: Rateless coding with constrained feedback. , 2013, , .		3

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37	Consecutive switch codes. , 2016, , .		3
38	LDPC codes for the q-ary bit-measurement channel. , 2016, , .		3
39	Detection and coding schemes for parallel interference in resistive memories. , 2017, , .		3
40	Error-Correcting WOM Codes: Concatenation and Joint Design. IEEE Transactions on Information Theory, 2019, 65, 5529-5546.	1.5	3
41	Efficient Distributed Source Coding of Fragmented Genomic Sequencing Data. , 2021, , .		3
42	Estimation of the decoding threshold of LDPC codes over the q-ary partial erasure channel. , 2014, , .		2
43	A constraint scheme for correcting massive asymmetric magnitude-1 errors in multi-level NVMs. , 2015, , .		2
44	Low-delay erasure-correcting codes with optimal average delay. , 2015, , .		2
45	d-imbalance WOM codes for reduced inter-cell interference in multi-level NVMs. , 2016, , .		2
46	A coding scheme for reliable in-memory hamming distance computation. , 2017, , .		2
47	On Decoding Random-Access SC-LDPC Codes. , 2019, , .		2
48	Design of Bilayer and Multi-Layer LDPC Ensembles From Individual Degree Distributions. IEEE Transactions on Information Theory, 2021, 67, 7096-7109.	1.5	2
49	Codes for high performance write and read processes in multi-level NVMs. , 2014, , .		1
50	Adaptive threshold read algorithms in multi-level non-volatile memories with uncertainty. , 2016, , .		1
51	Placement and read algorithms for high throughput in coded network switches. , 2016, , .		1
52	Optimal compression of element pairs in fixed-width memories. , 2017, , .		1
53	Coded Caching with Low Per-Request Complexity. , 2018, , .		1
54	Error-Correcting WOM Constructions through Concatenation and Joint Design. , 2018, , .		1

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55	Optimal Compression for Two-Field Entries in Fixed-Width Memories. IEEE Transactions on Information Theory, 2018, 64, 4309-4322.	1.5	1
56	Hierarchical erasure correction of linear codes. Finite Fields and Their Applications, 2020, 68, 101743.	0.6	1
57	Treeplication: An Erasure Code for Distributed Full Recovery Under the Random Multiset Channel. IEEE Transactions on Information Theory, 2021, 67, 3542-3556.	1.5	1
58	On the Decoding Performance of Spatially Coupled LDPC Codes with Sub-block Access. , 2021, , .		1
59	Adaptive threshold read algorithms in multi-level non-volatile memories. , 2013, , .		0
60	Design of LDPC codes for the q-ary partial erasure channel. , 2015, , .		0
61	Coded Network Switches for Improved Throughput. , 2016, , .		0
62	Coding for Improved Throughput Performance in Network Switches. IEEE/ACM Transactions on Networking, 2017, 25, 2802-2814.	2.6	0
63	Finite-length LDPC codes on the q-ary multi-bit channel. , 2017, , .		0
64	A Constrained Coding Scheme for Correcting Asymmetric Magnitude-1 Errors in q -Ary Channels. IEEE Transactions on Information Theory, 2018, 64, 918-932.	1.5	0
65	Treeplication: An Erasure Code that is Almost as Painless as Replication. , 2018, , .		0
66	On the Decoding Performance of Spatially Coupled LDPC Codes With Sub-Block Access. IEEE Transactions on Information Theory, 2022, 68, 3700-3718.	1.5	0
67	Optimizing Write Fidelity of MRAMs by Alternating Water-Filling Algorithm. IEEE Transactions on Communications, 2022, 70, 5825-5836.	4.9	0