

Min Zhang

List of Publications by Year in descending order

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791
citing authors

#	ARTICLE	IF	CITATIONS
1	Ship Detection in SAR Images Based on Multi-Scale Feature Extraction and Adaptive Feature Fusion. Remote Sensing, 2022, 14, 755.	1.8	40
2	Hyperspectral Anomaly Detection via Dual Dictionaries Construction Guided by Two-Stage Complementary Decision. Remote Sensing, 2022, 14, 1784.	1.8	23
3	Ship Detection in SAR Images Based on Feature Enhancement Swin Transformer and Adjacent Feature Fusion. Remote Sensing, 2022, 14, 3186.	1.8	17
4	High-Resolution Time-to-Digital Converters Implemented on 40-, 28-, and 20-nm FPGAs. IEEE Transactions on Instrumentation and Measurement, 2021, 70, 1-10.	2.4	8
5	Precise Frequency Comparison System Using Phase Coincidence Demarcation. IEEE Transactions on Instrumentation and Measurement, 2020, 69, 617-622.	2.4	1
6	Sensor Fault Detection and Diagnosis Method for AHU Using 1-D CNN and Clustering Analysis. Computational Intelligence and Neuroscience, 2019, 2019, 1-20.	1.1	15
7	An Algorithm for Natural Images Text Recognition Using Four Direction Features. Electronics (Switzerland), 2019, 8, 971.	1.8	6
8	Exploiting Elastically Supported Masses in Cantilever for Resonance Frequencies Down-Shifted Vibration Energy Harvester. Energies, 2019, 12, 2207.	1.6	3
9	An Algorithm for Scene Text Detection Using Multibox and Semantic Segmentation. Applied Sciences (Switzerland), 2019, 9, 1054.	1.3	10
10	An Efficient Super-Resolution Network Based on Aggregated Residual Transformations. Electronics (Switzerland), 2019, 8, 339.	1.8	4
11	Optimized Compression for Implementing Convolutional Neural Networks on FPGA. Electronics (Switzerland), 2019, 8, 295.	1.8	50
12	Pencil-on-paper flexible electronics for daily sensing applications. Circuit World, 2019, 45, 189-195.	0.7	6
13	Phase Difference Measurement Method Based on Progressive Phase Shift. Electronics (Switzerland), 2018, 7, 86.	1.8	12
14	Flexible, Stretchable Sensors for Wearable Health Monitoring: Sensing Mechanisms, Materials, Fabrication Strategies and Features. Sensors, 2018, 18, 645.	2.1	258
15	Digital-to-Time Converter with 3.93 ps Resolution Implemented on FPGA Chips. IEEE Access, 2017, 5, 6842-6848.	2.6	9
16	A 7.4 ps FPGA-Based TDC with a 1024-Unit Measurement Matrix. Sensors, 2017, 17, 865.	2.1	39
17	High-Resolution Digital-to-Time Converter Implemented in an FPGA Chip. Applied Sciences (Switzerland), 2017, 7, 52.	1.3	11
18	A new realization of time-to-digital converters based on FPGA internal routing resources. IEEE Transactions on Ultrasonics, Ferroelectrics, and Frequency Control, 2013, 60, 1787-1795.	1.7	10