

# Dirk Habich

## List of Publications by Year in descending order

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33  
papers

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citations

1478505

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1281871

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35  
all docs

35  
docs citations

35  
times ranked

128  
citing authors

#	ARTICLE	IF	CITATIONS
1	Aggregate-based Training Phase for ML-based Cardinality Estimation. Datenbank-Spektrum, 2022, 22, 45.	1.3	2
2	To share or not to share vector registers?. VLDB Journal, 2022, 31, 1215-1236.	4.1	1
3	BOUNCE: Memory-Efficient SIMD Approach for Lightweight Integer Compression. , 2022, , .		1
4	Mastering the NEC Vector Engine Accelerator for Analytical Query Processing. , 2021, , .		3
5	SIMD-MIMD cocktail in a hybrid memory glass. , 2021, , .		1
6	PostCENN. Proceedings of the VLDB Endowment, 2021, 14, 2715-2718.	3.8	5
7	LCTL: Lightweight Compression Template Library. , 2021, , .		0
8	Efficient compute node-local replication mechanisms for NVRAM-centric data structures. VLDB Journal, 2020, 29, 775-795.	4.1	0
9	MorphStore. Proceedings of the VLDB Endowment, 2020, 13, 2396-2410.	3.8	12
10	Polymorphic Compressed Replication of Columnar Data in Scale-Up Hybrid Memory Systems. , 2020, , .		2
11	To share or not to share vector registers?.. , 2020, , .		5
12	Scalable In-Memory Graph Pattern Matching on Symmetric Multiprocessor Systems. Communications in Computer and Information Science, 2020, , 49-62.	0.5	0
13	MorphStore - In-Memory Query Processing based on Morphing Compressed Intermediates LIVE. , 2019, , .		10
14	NeMeSys - A Showcase of Data Oriented Near Memory Graph Processing. , 2019, , .		2
15	Cardinality estimation with local deep learning models. , 2019, , .		46
16	Integer Compression in NVRAM-centric Data Stores. , 2019, , .		6
17	Evaluating the Vector Supercomputer SX-Aurora TSUBASA as a Co-Processor for In-Memory Database Systems. Datenbank-Spektrum, 2019, 19, 183-197.	1.3	1
18	From a Comprehensive Experimental Survey to a Cost-based Selection Strategy for Lightweight Integer Compression Algorithms. ACM Transactions on Database Systems, 2019, 44, 1-46.	2.8	27

#	ARTICLE	IF	CITATIONS
19	CSAR: the cross-sectional autoregression model for short and long-range forecasting. International Journal of Data Science and Analytics, 2019, 8, 165-181.	4.1	6
20	Trading Memory versus Workload Overhead in Graph Pattern Matching on Multiprocessor Systems. , 2019, , .		1
21	Make Larger Vector Register Sizes New Challenges?. , 2018, , .		6
22	Conflict Detection-Based Run-Length Encoding - AVX-512 CD Instruction Set in Action. , 2018, , .		8
23	Diversity of Processing Units. Datenbank-Spektrum, 2018, 18, 57-62.	1.3	1
24	Adaptive work placement for query processing on heterogeneous computing resources. Proceedings of the VLDB Endowment, 2017, 10, 733-744.	3.8	24
25	CSAR: The Cross-Sectional Autoregression Model. , 2017, , .		5
26	Partitioning Strategy Selection for In-Memory Graph Pattern Matching on Multiprocessor Systems. Lecture Notes in Computer Science, 2017, , 149-163.	1.3	4
27	Advancing a Gateway Infrastructure for Wind Turbine Data Analysis. Journal of Grid Computing, 2016, 14, 499-514.	3.9	7
28	Limitations of Intra-operator Parallelism Using Heterogeneous Computing Resources. Lecture Notes in Computer Science, 2016, , 291-305.	1.3	3
29	Energy Elasticity on Heterogeneous Hardware using Adaptive Resource Reconfiguration LIVE. , 2016, , .		3
30	Heterogeneity-Aware Operator Placement in Column-Store DBMS. Datenbank-Spektrum, 2014, 14, 211-221.	1.3	10
31	On-demand re-optimization of integration flows. Information Systems, 2014, 45, 1-17.	3.6	4
32	A high-throughput in-memory index, durable on flash-based SSD. SIGMOD Record, 2012, 41, 44-50.	1.2	3
33	Cost-based vectorization of instance-based integration processes. Information Systems, 2011, 36, 3-29.	3.6	14