Mahdi Shabany

List of Publications by Year
in descending order

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38 papers	1,075 citations	759055 12 h-index	610775 24 g-index
38 all docs	38 docs citations	38 times ranked	919 citing authors

#	Article	IF	Citations
1	Cuffless Blood Pressure Estimation Algorithms for Continuous Health-Care Monitoring. IEEE Transactions on Biomedical Engineering, 2017, 64, 859-869.	2.5	395
2	Cuff-less high-accuracy calibration-free blood pressure estimation using pulse transit time. , 2015, , .		159
3	Nonlinear Cuffless Blood Pressure Estimation of Healthy Subjects Using Pulse Transit Time and Arrival Time. IEEE Transactions on Instrumentation and Measurement, 2017, 66, 3299-3308.	2.4	98
4	A 675 Mbps, 4 $imes$ 4 64-QAM K-Best MIMO Detector in 0.13 $mu{m m}$ CMOS. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2012, 20, 135-147.	2.1	72
5	Novel MIMO Detection Algorithm for High-Order Constellations in the Complex Domain. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 834-847.	2.1	49
6	A Low-Latency Low-Power QR-Decomposition ASIC Implementation in 0.13 $mu{m m}$ CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 327-340.	3.5	36
7	Fast Fourier-Based Implementation of Synthetic Aperture Radar Algorithm for Multistatic Imaging System. IEEE Transactions on Instrumentation and Measurement, 2019, 68, 3339-3349.	2.4	34
8	Design and Implementation of Time and Frequency Synchronization in LTE. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 2970-2982.	2.1	29
9	Improved Two-Dimensional Millimeter-Wave Imaging for Concealed Weapon Detection Through Partial Fourier Sampling. Journal of Infrared, Millimeter, and Terahertz Waves, 2016, 37, 267-280.	1.2	25
10	High-Throughput 0.13-$mu{m m}$ CMOS Lattice Reduction Core Supporting 880 Mb/s Detection. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 848-861.	2.1	18
11	Efficient Compensation of the Nonlinearity of Solid-State Power Amplifiers Using Adaptive Sequential Monte Carlo Methods. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 3270-3283.	3.5	17
12	K-Space Aware Multi-Static Millimeter-Wave Imaging. IEEE Transactions on Image Processing, 2019, 28, 3613-3623.	6.0	14
13	Algorithm and VLSI Design for 1-Bit Data Detection in Massive MIMO-OFDM. IEEE Open Journal of Circuits and Systems, 2020, 1, 170-184.	1.4	13
14	Ultra high-throughput architectures for hard-output MIMO detectors in the complex domain. , 2011, , .		10
15	A Novel Area-Efficient VLSI Architecture for Recursion Computation in LTE Turbo Decoders. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015, 62, 568-572.	2.2	9
16	A 13 Gbps, 0.13 Î¼m CMOS, Multiplication-Free MIMO Detector. Journal of Signal Processing Systems, 2017, 88, 273-285.	1.4	9
17	Efficient Hardware Implementation of Real-Time Low-Power Movement Intention Detector System Using FFT and Adaptive Wavelet Transform. IEEE Transactions on Biomedical Circuits and Systems, 2017, 11, 585-596.	2.7	9
18	An efficient VLSI architecture of QPP interleaver/deinterleaver for LTE turbo coding. , 2013, , .		8

#	Article	IF	Citations
19	Spectral Redundancy Compensation in Multi-Static Millimeter-Wave Imaging. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 687-691.	2.2	8
20	A modified complex K-best scheme for high-speed hard-output MIMO detectors. , 2010, , .		7
21	An efficient max-log MAP algorithm for VLSI implementation of turbo decoders. , 2015, , .		7
22	K-Space Analysis of Aliasing in Millimeter-Wave Imaging Systems. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 1965-1973.	2.9	7
23	A low-complexity high-throughput ASIC for the SC-FDMA MIMO detectors. , 2012, , .		6
24	A real-time, low-power implementation for high-resolution eigenvalue-based spectrum sensing. Analog Integrated Circuits and Signal Processing, 2013, 77, 437-447.	0.9	6
25	A High-Throughput VLSI Architecture for Real-Time Optical OFDM Systems With an Efficient Phase Equalizer. Canadian Journal of Electrical and Computer Engineering, 2014, 37, 86-93.	1.5	5
26	A High-Throughput VLSI Architecture for Hard and Soft SC-FDMA MIMO Detectors. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 761-770.	3.5	5
27	Thorough approach toward cylindrical MMW image reconstruction using sparse antenna array. IET Image Processing, 2018, 12, 1458-1466.	1.4	5
28	A VLSI architecture for multiple antenna eigenvalue-based spectrum sensing. , 2012, , .		3
29	Efficient implementation of real-time ECG derived respiration system using cubic spline interpolation. , 2013, , .		2
30	TTCN: A new approach for low-power split-row LDPC decoders. , 2015, , .		2
31	A Machine Learning Approach for Material Classification in MMW Imaging Systems based on Frequency Spectra. , 2018, , .		2
32	Efficient millimetreâ€wave imaging structure for detecting axially rotated objects. IET Microwaves, Antennas and Propagation, 2018, 12, 416-424.	0.7	2
33	Improved CT Image Reconstruction Through Partial Fourier Sampling. Scientia Iranica, 2016, 23, 2908-2916.	0.3	2
34	An Efficient Architecture for Sequential Monte Carlo Receivers in Wireless Flat-Fading Channels. Journal of Signal Processing Systems, 2012, 68, 303-315.	1.4	1
35	A low-power 10-Bit 40-MS/s pipeline ADC using extended capacitor sharing. , 2014, , .		1
36	An efficient high-throughput VLSI architecture for a synchronization block applied to real-time optical OFDM systems. , 2014, , .		0

#	Article	IF	Citations
37	A low-complexity fully scalable interleaver/address generator based on a novel property of QPP interleavers. , 2017, , .		0
38	A 70â€¯pJ/b configurable 64-QAM soft MIMO detector. The Integration VLSI Journal, 2018, 63, 74-86.	1.3	0