

# Saeed Golestan

## List of Publications by Year in descending order

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88  
papers

6,173  
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87888

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69250

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88  
docs citations

88  
times ranked

3141  
citing authors

#	ARTICLE	IF	CITATIONS
1	Modeling of Converter Synchronization Stability Under Grid Faults: The General Case. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2022, 10, 2790-2804.	5.4	20
2	More-Stable EPLL. IEEE Transactions on Power Electronics, 2022, 37, 1003-1011.	7.9	17
3	Distributed Event-Triggered Control for Reactive, Unbalanced, and Harmonic Power Sharing in Islanded AC Microgrids. IEEE Transactions on Industrial Electronics, 2022, 69, 1548-1560.	7.9	33
4	Open-Loop Synchronization Systems for Grid-Tied Power Converters: Literature Overview, Design Considerations, Advantages, and Disadvantages. IEEE Industrial Electronics Magazine, 2022, 16, 14-22.	2.6	4
5	In-Loop Filters and Prefilters in Phase-Locked Loop Systems: Equivalent or Different Solutions?. IEEE Industrial Electronics Magazine, 2022, 16, 23-35.	2.6	5
6	Impedance Modeling of Three-Phase Grid-Connected Voltage Source Converters With Frequency-Locked-Loop-Based Synchronization Algorithms. IEEE Transactions on Power Electronics, 2022, 37, 4511-4525.	7.9	16
7	Design-Oriented Study of the Orthogonal Vector-Based Linear Kalman Filter: Enhancements and Applications. IEEE Transactions on Industrial Electronics, 2022, 69, 13359-13372.	7.9	2
8	LTP Modeling and Stability Assessment of Multiple Second-Order Generalized Integrator-Based Signal Processing/Synchronization Algorithms and Their Close Variants. IEEE Transactions on Power Electronics, 2022, 37, 5062-5077.	7.9	10
9	$dq$ -Frame Impedance Modeling of Three-Phase Grid-Tied Voltage Source Converters Equipped With Advanced PLLs. IEEE Transactions on Power Electronics, 2021, 36, 3524-3539.	7.9	45
10	Linear Time-Periodic Modeling, Examination, and Performance Enhancement of Grid Synchronization Systems With DC Component Rejection/Estimation Capability. IEEE Transactions on Power Electronics, 2021, 36, 4237-4253.	7.9	20
11	Harmonic Linearization and Investigation of Three-Phase Parallel-Structured Signal Decomposition Algorithms in Grid-Connected Applications. IEEE Transactions on Power Electronics, 2021, 36, 4198-4213.	7.9	22
12	LTP Modeling of Single-Phase $T/4$ Delay-Based PLLs. IEEE Transactions on Industrial Electronics, 2021, 68, 9003-9008.	7.9	11
13	Frequency-Locked Loops in Electrical Power and Energy Systems: Equivalent or Different to Phase-Locked Loops?. IEEE Industrial Electronics Magazine, 2021, 15, 54-64.	2.6	11
14	Passivity Enhancement of Voltage-Controlled Inverters in Grid-Connected Microgrids Considering Negative Aspects of Control Delay and Grid Impedance Variations. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2021, 9, 6637-6649.	5.4	19
15	Performance improvement of single-phase PLLs under adverse grid conditions: An FIR filtering-based approach. Electric Power Systems Research, 2021, 190, 106829.	3.6	10
16	Standard SOGI-FLL and Its Close Variants: Precise Modeling in LTP Framework and Determining Stability Region/Robustness Metrics. IEEE Transactions on Power Electronics, 2021, 36, 409-422.	7.9	70
17	Stability Enhancement of Inverters in Grid-Connected Microgrids Using FIR Filter. IEEE Journal of Emerging and Selected Topics in Industrial Electronics, 2021, 2, 122-131.	3.9	8
18	AC Microgrids Protection: A Digital Coordinated Adaptive Scheme. Applied Sciences (Switzerland), 2021, 11, 7066.	2.5	4

#	ARTICLE	IF	CITATIONS
19	DC-Link Voltage Control Aided for the Inertial Support During Severe Faults in Weak Grids. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2021, 9, 7296-7305.	5.4	9
20	An Overview of Grid-Forming Control for Wind Turbine Converters. , 2021, , .		1
21	Seamless Transition of Synchronous Inverters Using Synchronizing Virtual Torque and Flux Linkage. IEEE Transactions on Industrial Electronics, 2020, 67, 319-328.	7.9	35
22	Is Using A Complex Control Gain in Three-Phase FLLs Reasonable?. IEEE Transactions on Industrial Electronics, 2020, 67, 2480-2484.	7.9	19
23	A Hybrid Compensator Configuration for VAR Control and Harmonic Suppression in All-Electric Shipboard Power Systems. IEEE Transactions on Power Delivery, 2020, 35, 1379-1389.	4.3	15
24	All-Pass-Filter-Based PLL Systems: Linear Modeling, Analysis, and Comparative Evaluation. IEEE Transactions on Power Electronics, 2020, 35, 3558-3572.	7.9	56
25	A Resolution-Enhanced Sliding Matrix Pencil Method for Evaluation of Harmonics Distortion in Shipboard Microgrids. IEEE Transactions on Transportation Electrification, 2020, 6, 1290-1300.	7.8	7
26	Multimode Operation for On-Line Uninterruptible Power Supply System. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2019, 7, 1181-1196.	5.4	24
27	Advanced Single-Phase DSC-Based PLLs. IEEE Transactions on Power Electronics, 2019, 34, 3226-3238.	7.9	32
28	Modified Secondary-Control-Based Fault Current Limiter for Inverters. IEEE Transactions on Industrial Electronics, 2019, 66, 4798-4804.	7.9	13
29	Modeling and Stability Assessment of Single-Phase Grid Synchronization Techniques: Linear Time-Periodic Versus Linear Time-Invariant Frameworks. IEEE Transactions on Power Electronics, 2019, 34, 20-27.	7.9	32
30	Modeling and Tuning of Adaptive Complex Current Controller for Three-Phase Grid-Interfaced Power Converters. , 2019, , .		2
31	Effective and low-cost passive compensator system to improve the power quality of two electric generators. IET Power Electronics, 2019, 12, 1833-1840.	2.1	6
32	Single-Phase Frequency-Locked Loops: A Comprehensive Review. IEEE Transactions on Power Electronics, 2019, 34, 11791-11812.	7.9	129
33	Single-Phase FLLs Based on Linear Kalman Filter, Limit-Cycle Oscillator, and Complex Bandpass Filter: Analysis and Comparison With a Standard FLL in Grid Applications. IEEE Transactions on Power Electronics, 2019, 34, 11774-11790.	7.9	34
34	A GaN-Based Active Power Decoupling Approach for Enhancing the Efficiency and Reliability of Residential PV Microinverters. , 2019, , .		1
35	Fault location in microgrids: a communication-based high-frequency impedance approach. IET Generation, Transmission and Distribution, 2019, 13, 1229-1237.	2.5	18
36	A Study on Three-Phase FLLs. IEEE Transactions on Power Electronics, 2019, 34, 213-224.	7.9	84

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37	Modeling, Tuning, and Performance Comparison of Second-Order-Generalized-Integrator-Based FLLs. IEEE Transactions on Power Electronics, 2018, 33, 10229-10239.	7.9	141
38	A Voltage Modulated DPC Approach for Three-Phase PWM Rectifier. IEEE Transactions on Industrial Electronics, 2018, 65, 7612-7619.	7.9	65
39	Steady-State Linear Kalman Filter-Based PLLs for Power Applications: A Second Look. IEEE Transactions on Industrial Electronics, 2018, 65, 9795-9800.	7.9	48
40	Research On Variable-Length Transfer Delay and Delayed-Signal-Cancellation-Based PLLs. IEEE Transactions on Power Electronics, 2018, 33, 8388-8398.	7.9	34
41	DC-Link Protection and Control in Modular Uninterruptible Power Supply. IEEE Transactions on Industrial Electronics, 2018, 65, 3942-3953.	7.9	30
42	A Simple Approach to Enhance the Performance of Complex-Coefficient Filter-Based PLL in Grid-Connected Applications. IEEE Transactions on Industrial Electronics, 2018, 65, 5081-5085.	7.9	71
43	An Open-Loop Grid Synchronization Approach for Single-Phase Applications. IEEE Transactions on Power Electronics, 2018, 33, 5548-5555.	7.9	36
44	Matrix pencil method-based reference current generation for shunt active power filters. IET Power Electronics, 2018, 11, 772-780.	2.1	25
45	A Nonadaptive Window-Based PLL for Single-Phase Applications. IEEE Transactions on Power Electronics, 2018, 33, 24-31.	7.9	37
46	A PLL-Based Controller for Three-Phase Grid-Connected Power Converters. IEEE Transactions on Power Electronics, 2018, 33, 911-916.	7.9	32
47	An Enhanced State Observer for DC-Link Voltage Control of Three-Phase AC/DC Converters. IEEE Transactions on Power Electronics, 2018, 33, 936-942.	7.9	65
48	Morphological PLL for potential applications on renewable energy. Electric Power Systems Research, 2018, 156, 15-23.	3.6	3
49	An Adaptive Resonant Regulator for Single-Phase Grid-Tied VSCs. IEEE Transactions on Power Electronics, 2018, 33, 1867-1873.	7.9	45
50	Multiple-Complex Coefficient-Filter-Based PLL for Improving the Performance of Shunt Active Power Filter under Adverse Grid Conditions. , 2018, , .		15
51	An Adaptive Quadrature Signal Generation-Based Single-Phase Phase-Locked Loop for Grid-Connected Applications. IEEE Transactions on Industrial Electronics, 2017, 64, 2848-2854.	7.9	73
52	Three-Phase PLLs: A Review of Recent Advances. IEEE Transactions on Power Electronics, 2017, 32, 1894-1907.	7.9	562
53	Single-Phase PLLs: A Review of Recent Advances. IEEE Transactions on Power Electronics, 2017, 32, 9013-9030.	7.9	300
54	A Critical Examination of Frequency-Fixed Second-Order Generalized Integrator-Based Phase-Locked Loops. IEEE Transactions on Power Electronics, 2017, 32, 6666-6672.	7.9	70

#	ARTICLE	IF	CITATIONS
55	Analysis and design of notch filter-based PLLs for grid-connected applications. Electric Power Systems Research, 2017, 147, 62-69.	3.6	41
56	High-Order Frequency-Locked Loops: A Critical Analysis. IEEE Transactions on Power Electronics, 2017, 32, 3285-3291.	7.9	19
57	A Robust and Fast Synchronization Technique for Adverse Grid Conditions. IEEE Transactions on Industrial Electronics, 2017, 64, 3188-3194.	7.9	32
58	Analysis and controller design for stand-alone VSIs in synchronous reference frame. IET Power Electronics, 2017, 10, 1003-1012.	2.1	35
59	An Adaptive Least-Error Squares Filter-Based Phase-Locked Loop for Synchronization and Signal Decomposition Purposes. IEEE Transactions on Industrial Electronics, 2017, 64, 336-346.	7.9	22
60	Hybrid Adaptive/Nonadaptive Delayed Signal Cancellation-Based Phase-Locked Loop. IEEE Transactions on Industrial Electronics, 2017, 64, 470-479.	7.9	41
61	A True Open-Loop Synchronization Technique. IEEE Transactions on Industrial Informatics, 2016, 12, 1093-1103.	11.3	45
62	Non-frequency sensitive all-pass filter based single-phase PLLs. , 2016, , .		4
63	Five Approaches to Deal With Problem of DC Offset in Phase-Locked Loop Algorithms: Design Considerations and Performance Evaluations. IEEE Transactions on Power Electronics, 2016, 31, 648-661.	7.9	116
64	An Efficient Implementation of Generalized Delayed Signal Cancellation PLL. IEEE Transactions on Power Electronics, 2016, 31, 1085-1094.	7.9	126
65	DCOffset Rejection in Phase-locked loops: A Novel Approach. IEEE Transactions on Industrial Electronics, 2016, , 1-1.	7.9	37
66	PLL With MAF-Based Prefiltering Stage: Small-Signal Modeling and Performance Enhancement. IEEE Transactions on Power Electronics, 2016, 31, 4013-4019.	7.9	139
67	Inducverters: PLL-Less Converters With Auto-Synchronization and Emulated Inertia Capability. IEEE Transactions on Smart Grid, 2016, 7, 1660-1674.	9.0	109
68	Small-Signal Modeling, Stability Analysis and Design Optimization of Single-Phase Delay-Based PLLs. IEEE Transactions on Power Electronics, 2016, 31, 3517-3527.	7.9	99
69	Hybrid Synchronous/Stationary Reference-Frame-Filtering-Based PLL. IEEE Transactions on Industrial Electronics, 2015, 62, 5018-5022.	7.9	50
70	An Analysis of Modified Demodulation-Based Grid Voltage Parameter Estimator. IEEE Transactions on Power Electronics, 2015, 30, 6528-6533.	7.9	17
71	Low computational burden grid voltage estimation for grid connected voltage source converter-based power applications. IET Power Electronics, 2015, 8, 656-664.	2.1	20
72	A Systematic Approach to Design High-Order Phase-Locked Loops. IEEE Transactions on Power Electronics, 2015, 30, 2885-2890.	7.9	53

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73	<i>dq</i>-Frame Cascaded Delayed Signal Cancellation- Based PLL: Analysis, Design, and Comparison With Moving Average Filter-Based PLL. IEEE Transactions on Power Electronics, 2015, 30, 1618-1632.	7.9	231
74	Conventional Synchronous Reference Frame Phase-Locked Loop is an Adaptive Complex Filter. IEEE Transactions on Industrial Electronics, 2015, 62, 1679-1682.	7.9	140
75	MAF-PLL With Phase-Lead Compensator. IEEE Transactions on Industrial Electronics, 2014, , 1-1.	7.9	66
76	Analysis, Design, and Experimental Verification of a Synchronous Reference Frame Voltage Control for Single-Phase Inverters. IEEE Transactions on Industrial Electronics, 2014, 61, 258-269.	7.9	205
77	Moving Average Filter Based Phase-Locked Loops: Performance Analysis and Design Guidelines. IEEE Transactions on Power Electronics, 2014, 29, 2750-2763.	7.9	438
78	Performance Improvement of a Prefiltered Synchronous-Reference-Frame PLL by Using a PID-Type Loop Filter. IEEE Transactions on Industrial Electronics, 2014, 61, 3469-3479.	7.9	116
79	An Analysis of the PLLs With Secondary Control Path. IEEE Transactions on Industrial Electronics, 2014, 61, 4824-4828.	7.9	32
80	A Quasi-Type-1 Phase-Locked Loop Structure. IEEE Transactions on Power Electronics, 2014, 29, 6264-6270.	7.9	135
81	Second order generalized integrator based reference current generation method for single-phase shunt active power filters under adverse grid conditions. , 2013, , .		32
82	Dynamics Assessment of Advanced Single-Phase PLL Structures. IEEE Transactions on Industrial Electronics, 2013, 60, 2167-2177.	7.9	287
83	Design-Oriented Study of Advanced Synchronous Reference Frame Phase-Locked Loops. IEEE Transactions on Power Electronics, 2013, 28, 765-778.	7.9	419
84	Advantages and Challenges of a Type-3 PLL. IEEE Transactions on Power Electronics, 2013, 28, 4985-4997.	7.9	132
85	A New Synchronous Reference Frame-Based Method for Single-Phase Shunt Active Power Filters. Journal of Power Electronics, 2013, 13, 692-700.	1.5	39
86	Direct active and reactive power control of single-phase grid-tie converters. IET Power Electronics, 2012, 5, 1544.	2.1	90
87	Control strategies for single-phase grid integration of small-scale renewable energy sources: A review. Renewable and Sustainable Energy Reviews, 2012, 16, 4982-4993.	16.4	121
88	Design and Tuning of a Modified Power-Based PLL for Single-Phase Grid-Connected Power Conditioning Systems. IEEE Transactions on Power Electronics, 2012, 27, 3639-3650.	7.9	189