

Xueying Zhang

List of Publications by Year in descending order

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Version: 2024-02-01

27
papers

505
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623734

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27
all docs

27
docs citations

27
times ranked

609
citing authors

#	ARTICLE	IF	CITATIONS
1	Experimental demonstration of skyrmionic magnetic tunnel junction at room temperature. Science Bulletin, 2022, 67, 691-699.	9.0	52
2	The Biomechanics Effect of Hamstring Flexibility on the Risk of Osgood-Schlatter Disease. Journal of Healthcare Engineering, 2022, 2022, 1-8.	1.9	0
3	Unconventional applications of skyrmions. , 2021, , 393-416.		0
4	Theoretical Conditions for Field-Free Magnetization Switching Induced by Spin-Orbit Torque and Dzyaloshinskii-Moriya Interaction. IEEE Electron Device Letters, 2021, 42, 148-151.	3.9	5
5	Spin-Torque Memristors Based on Perpendicular Magnetic Tunnel Junctions for Neuromorphic Computing. Advanced Science, 2021, 8, 2004645.	11.2	34
6	Prediction of crossing nodal-lines and large intrinsic spin Hall conductivity in topological Dirac semimetal Ta3As family. Npj Computational Materials, 2021, 7, .	8.7	14
7	Weak Kondo effect in the monocrystalline transition metal dichalcogenide $ZrTe_5$. Physical Review B, 2021, 103, .		
8	Manipulating density of magnetic skyrmions via multilayer repetition and thermal annealing. Physical Review B, 2021, 104, .	3.2	12
9	Enhanced interfacial Dzyaloshinskii-Moriya interactions in annealed Pt/Co/MgO structures. Nanotechnology, 2020, 31, 155705.	2.6	24
10	Spin-orbit torque driven multi-level switching in He+ irradiated W/CoFeB/MgO Hall bars with perpendicular anisotropy. Applied Physics Letters, 2020, 116, .	3.3	19
11	Compact Model of Dzyaloshinskii Domain Wall Motion-Based MTJ for Spin Neural Networks. IEEE Transactions on Electron Devices, 2020, 67, 2621-2626.	3.0	6
12	Skyrmion-Induced Memristive Magnetic Tunnel Junction for Ternary Neural Network. IEEE Journal of the Electron Devices Society, 2019, 7, 529-533.	2.1	13
13	Ultra-efficient spin-orbit torque induced magnetic switching in W/CoFeB/MgO structures. Nanotechnology, 2019, 30, 335707.	2.6	20
14	Ring-shaped content addressable memory based on spin orbit torque driven chiral domain wall motions. , 2019, , .		0
15	Ultra-Dense Ring-Shaped Racetrack Memory Cache Design. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 215-225.	5.4	31
16	Direct Observation of Domain-Wall Surface Tension by Deflating or Inflating a Magnetic Bubble. Physical Review Applied, 2018, 9, .	3.8	27
17	Skyrmions in Magnetic Tunnel Junctions. ACS Applied Materials & Interfaces, 2018, 10, 16887-16892.	8.0	68
18	Size dependence of the spin-orbit torque induced magnetic reversal in W/CoFeB/MgO nanostructures. Applied Physics Letters, 2018, 112, 142410.	3.3	17

#	ARTICLE	IF	CITATIONS
19	Extrinsic pinning of magnetic domain walls in CoFeB-MgO nanowires with perpendicular anisotropy. AIP Advances, 2018, 8, .	1.3	11
20	Advanced Nanoscale Magnetic Tunnel Junctions for Low Power Computing (Invited). , 2018, , .		2
21	Tuning the Dzyaloshinskiiâ€Moriya interaction in Pt/Co/MgO heterostructures through the MgO thickness. Nanoscale, 2018, 10, 12062-12067.	5.6	66
22	Magneto-resistive sensors based on the elasticity of domain walls. Nanotechnology, 2018, 29, 365502.	2.6	9
23	A Christmas-Tree-Like Magnetic Field Sensor Based on Domain Wall Depinning in a Notched Nanowire. IEEE Magnetics Letters, 2018, 9, 1-4.	1.1	5
24	Domain-Wall Motion Driven by Laplace Pressure in $\langle \text{mml:math} \text{xmlns:mml="http://www.w3.org/1998/Math/MathML"} \text{display="inline"} \langle \text{mml:mrow} \langle \text{mml:mi} \text{Co} \langle \text{mml:mi} \text{Fe} \langle \text{mml:mtext} \hat{\text{a}} \langle \text{mml:mtext} \text{MgO} \langle \text{mml:mtext} \hat{\text{a}} \langle \text{mml:mtext} \text{B} \langle \text{mml:mo} \rangle \langle \text{mml:mo} \rangle \langle \text{mml:mi} \text{MgO} \langle \text{mml:mi} \rangle \langle \text{mml:mrow} \rangle \langle \text{mml:math} \rangle \text{Nanodots with Perpendicular Anisotropy. Physical Review Applied, 2018, 9, .$	1.6	20
25	Perspectives of Racetrack Memory for Large-Capacity On-Chip Memory: From Device to System. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 629-638.	5.4	18
26	Ring-shaped Racetrack memory based on spin orbit torque driven chiral domain wall motions. Scientific Reports, 2016, 6, 35062.	3.3	17
27	Spin transfer torque memories and logic gates. , 2014, , .		0