

Taewook Kim

List of Publications by Year in descending order

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papers

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14
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155
citing authors

#	ARTICLE	IF	CITATIONS
1	An All-Digital Scalable and Reconfigurable Wide-Input Range Stochastic ADC Using Only Standard Cells. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015, 62, 731-735.	3.0	50
2	A 7.2 mW 75.3 dB SNDR 10 MHz BW CT Delta-Sigma Modulator Using Gm-C-Based Noise-Shaped Quantizer and Digital Integrator. IEEE Journal of Solid-State Circuits, 2016, 51, 1840-1850.	5.4	28
3	A Fully Synthesized 77-dB SFDR Reprogrammable SRMC Filter Using Digital Standard Cells. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 1126-1138.	3.1	27
4	A 4th-Order Continuous-Time Delta-Sigma Modulator Using 6-bit Double Noise-Shaped Quantizer. IEEE Journal of Solid-State Circuits, 2017, 52, 3248-3261.	5.4	19
5	A nano-ampere 2nd order temperature-compensated CMOS current reference using only single resistor for wide-temperature range applications. , 2016, , .		12
6	Noise-Shaped Residue-Discharging Delta-Sigma ADCs With Time-Modulated Pulse Feedback. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 2796-2804.	5.4	4
7	Sturdy-MASH delta-sigma modulator with noise-shaped integrating quantizer and dual-DAC DWA. , 2015, , .		4
8	High-speed and high-linearity ring oscillator based pulse width modulator. , 2015, , .		1
9	Multi-stage delta-sigma modulator with a relaxed opamp gain using a back-end digital integrator. , 2016, , .		1
10	SMASH-MASH delta-sigma modulator using noise-shaping quantizers. , 2016, , .		0
11	A 200MS/s 7bit time domain ring quantizer. , 2017, , .		0
12	A continuous-time delta-sigma modulator with self-ELD compensated quantizer. , 2018, , .		0
13	A Self-Reset Transconductance Integrating Leakage Latch (STILL) for Ultra-Low Power Sensor Interfacing. , 2019, , .		0
14	Study of Quantizer-Bandwidth in Continuous-Time Delta-Sigma Modulators. , 2019, , .		0