

Mohd Adil Raushan

List of Publications by Year in descending order

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11
papers

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68
citing authors

#	ARTICLE	IF	CITATIONS
1	Investigation of Gate Material Engineering in Junctionless Transistor for Digital and Analog Applications. Silicon, 2022, 14, 2851-2862.	3.3	5
2	Performance Improvement of Dopingless Transistor for Low Power Applications. Silicon, 2022, 14, 8009-8020.	3.3	4
3	Impact of Pocket in a Doping-Less Tunnel Field Effect Transistor. Lecture Notes in Networks and Systems, 2021, , 189-196.	0.7	2
4	Reducing off-state leakage current in dopingless transistor employing dual metal drain. Semiconductor Science and Technology, 2020, 35, 015016.	2.0	6
5	Design approach to improve the performance of JAMFETs. IET Circuits, Devices and Systems, 2020, 14, 333-339.	1.4	2
6	Electrostatically doped drain junctionless transistor for low-power applications. Journal of Computational Electronics, 2019, 18, 864-871.	2.5	6
7	Impact of asymmetric dual-k spacers on tunnel field effect transistors. Journal of Computational Electronics, 2018, 17, 756-765.	2.5	20
8	Dopingless Tunnel Field-Effect Transistor With Oversized Back Gate: Proposal and Investigation. IEEE Transactions on Electron Devices, 2018, 65, 4701-4708.	3.0	37
9	Performance Enhancement of Junctionless Tunnel Field Effect Transistor Using Dual-k Spacers. Journal of Nanoelectronics and Optoelectronics, 2018, 13, 912-920.	0.5	14
10	Achievements and perspectives of GaN based light emitting diodes: A critical review. , 2017, , .		5
11	Introduction of Metal Layer in Junctionless Accumulation Mode FET:-Proposal and Analysis. Silicon, 0, , 1.	3.3	0