

# Robert Szczygiel

## List of Publications by Year in descending order

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164  
papers

4,290  
citations

201385

27  
h-index

110170

64  
g-index

164  
all docs

164  
docs citations

164  
times ranked

6523  
citing authors

#	ARTICLE	IF	CITATIONS
1	The ATLAS Experiment at the CERN Large Hadron Collider. Journal of Instrumentation, 2008, 3, S08003-S08003.	0.5	1,752
2	Challenges in QCD matter physics –The scientific programme of the Compressed Baryonic Matter experiment at FAIR. European Physical Journal A, 2017, 53, 1.	1.0	222
3	Charged-particle multiplicities in pp interactions at $\sqrt{s} = 900$ GeV measured with the ATLAS detector at the LHC. Physics Letters, Section B: Nuclear, Elementary Particle and High-Energy Physics, 2010, 688, 21–42.	1.5	115
4	Measurement of inclusive jet and dijet cross sections in proton-proton collisions at 7 TeV centre-of-mass energy with the ATLAS detector. European Physical Journal C, 2011, 71, 1.	1.4	114
5	Search for New Particles in Two-Jet Final States in 7 TeV Proton-Proton Collisions with the ATLAS Detector at the LHC. Physical Review Letters, 2010, 105, 161801.	2.9	113
6	Design and performance of the ABCD3TA ASIC for readout of silicon strip detectors in the ATLAS semiconductor tracker. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2005, 552, 292-328.	0.7	104
7	Measurements of Matching and Noise Performance of a Prototype Readout Chip in 40 nm CMOS Process for Hybrid Pixel Detectors. IEEE Transactions on Nuclear Science, 2015, 62, 359-367.	1.2	80
8	The barrel modules of the ATLAS semiconductor tracker. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2006, 568, 642-671.	0.7	79
9	Design and Tests of the Vertically Integrated Photon Imaging Chip. IEEE Transactions on Nuclear Science, 2014, 61, 663-674.	1.2	76
10	A Prototype Pixel Readout IC for High Count Rate X-Ray Imaging Systems in 90 nm CMOS Technology. IEEE Transactions on Nuclear Science, 2010, 57, 1664-1674.	1.2	73
11	FPDR90 – A Low Noise, Fast Pixel Readout Chip in 90 nm CMOS Technology. IEEE Transactions on Nuclear Science, 2011, 58, 1361-1369.	1.2	71
12	The ATLAS semiconductor tracker end-cap module. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2007, 575, 353-389.	0.7	65
13	Measurement of the $W \rightarrow \ell \bar{\nu}_\ell$ and $Z \rightarrow \ell \ell$ production cross sections in proton-proton collisions at $\sqrt{s} = 7$ TeV with the ATLAS detector. Journal of High Energy Physics, 2010, 2010, 1.	1.6	64
14	Search for quark contact interactions in dijet angular distributions in pp collisions at $\sqrt{s} = 7$ TeV measured with the ATLAS detector. Physics Letters, Section B: Nuclear, Elementary Particle and High-Energy Physics, 2011, 694, 327-345.	1.5	63
15	32k Channel Readout IC for Single Photon Counting Pixel Detectors with Pitch, Dead Time of 85 ns, Offset Spread and 2% rms Gain Spread. IEEE Transactions on Nuclear Science, 2016, 63, 1155-1161.	1.2	56
16	18k Channels single photon counting readout circuit for hybrid pixel detector. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2013, 697, 32-39.	0.7	52
17	N-XYTER, a CMOS read-out ASIC for high resolution time and amplitude measurements on high rate multi-channel counting mode neutron detectors. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2006, 568, 301-308.	0.7	47
18	Beam tests of ATLAS SCT silicon strip detector modules. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2005, 538, 384-407.	0.7	42

#	ARTICLE	IF	CITATIONS
19	Combined performance tests before installation of the ATLAS Semiconductor and Transition Radiation Tracking Detectors. <i>Journal of Instrumentation</i> , 2008, 3, P08003-P08003.	0.5	42
20	Sub-microsecond-resolved multi-speckle X-ray photon correlation spectroscopy with a pixel array detector. <i>Journal of Synchrotron Radiation</i> , 2018, 25, 1408-1416.	1.0	41
21	Pole-Zero Cancellation Circuit With Pulse Pile-Up Tracking System for Low Noise Charge-Sensitive Amplifiers. <i>IEEE Transactions on Nuclear Science</i> , 2008, 55, 583-590.	1.2	37
22	Study of energy response and resolution of the ATLAS barrel calorimeter to hadrons of energies from 20 to 350 GeV. <i>Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment</i> , 2010, 621, 134-150.	0.7	34
23	Design and performance of the ABCD chip for the binary readout of silicon strip detectors in the ATLAS semiconductor tracker. <i>IEEE Transactions on Nuclear Science</i> , 2000, 47, 1843-1850.	1.2	33
24	Dynamic Scaling of Colloidal Gel Formation at Intermediate Concentrations. <i>Physical Review Letters</i> , 2017, 119, 178006.	2.9	31
25	The ATLAS TRT Barrel Detector. <i>Journal of Instrumentation</i> , 2008, 3, P02014-P02014.	0.5	30
26	Fully 3-D Integrated Pixel Detectors for X-Rays. <i>IEEE Transactions on Electron Devices</i> , 2016, 63, 205-214.	1.6	30
27	The ATLAS TRT end-cap detectors. <i>Journal of Instrumentation</i> , 2008, 3, P10003-P10003.	0.5	27
28	The ATLAS Transition Radiation Tracker (TRT) proportional drift tube: design and performance. <i>Journal of Instrumentation</i> , 2008, 3, P02013-P02013.	0.5	25
29	Submillisecond X-ray photon correlation spectroscopy from a pixel array detector with fast dual gating and no readout dead-time. <i>Journal of Synchrotron Radiation</i> , 2016, 23, 679-684.	1.0	25
30	Hard-sphere-like dynamics in highly concentrated alpha-crystallin suspensions. <i>Physical Review E</i> , 2018, 97, 020601.	0.8	24
31	Front-end readout electronics considerations for Silicon Tracking System and Muon Chamber. <i>Journal of Instrumentation</i> , 2016, 11, C02024-C02024.	0.5	23
32	Single Photon-Counting Pixel Readout Chip Operating Up to 1.2 Gcps/mm <sup>2</sup> for Digital X-Ray Imaging Systems. <i>IEEE Journal of Solid-State Circuits</i> , 2018, 53, 2651-2662.	3.5	22
33	Characterization of the STS/MUCH-XYTER2, a 128-channel time and amplitude measurement IC for gas and silicon microstrip sensors. <i>Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment</i> , 2018, 908, 225-235.	0.7	21
34	64 Channel Neural Recording Amplifier with Tunable Bandwidth in 180 nm CMOS Technology. <i>Metrology and Measurement Systems</i> , 2011, 18, .	1.4	20
35	A protocol for hit and control synchronous transfer for the front-end electronics at the CBM experiment. <i>Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment</i> , 2016, 835, 66-73.	0.7	20
36	Status of design and construction of the Transition Radiation Tracker (TRT) for the ATLAS experiment at the LHC. <i>Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment</i> , 2004, 522, 131-145.	0.7	18

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37	Performance of the ATLAS detector using first collision data. Journal of High Energy Physics, 2010, 2010, 1.	1.6	18
38	VIPIC IC &#x2014; Design and test aspects of the 3D pixel chip. , 2010, , .		18
39	Characterization of the Photon Counting CHASE Jr., Chip Built in a 40-nm CMOS Process With a Charge Sharing Correction Algorithm Using a Collimated X-Ray Beam. IEEE Transactions on Nuclear Science, 2017, 64, 2561-2568.	1.2	18
40	Characterization and performance evaluation of the XSPA-500k detector using synchrotron radiation. Journal of Synchrotron Radiation, 2021, 28, 439-447.	1.0	18
41	Fast nanoparticle rotational and translational diffusion in synovial fluid and hyaluronic acid solutions. Science Advances, 2021, 7, .	4.7	18
42	The ATLAS TRT electronics. Journal of Instrumentation, 2008, 3, P06007-P06007.	0.5	17
43	STS-XYTER, a high count-rate self-triggering silicon strip detector readout IC for high resolution time and energy measurements. , 2014, , .		17
44	Active Feedback With Leakage Current Compensation for Charge Sensitive Amplifier Used in Hybrid Pixel Detector. IEEE Transactions on Nuclear Science, 2019, 66, 664-673.	1.2	16
45	Nanoscale Critical Phenomena in a Complex Fluid Studied by X-Ray Photon Correlation Spectroscopy. Physical Review Letters, 2020, 125, 125504.	2.9	16
46	Analysis of full charge reconstruction algorithms for X-ray pixelated detectors. , 2011, , .		15
47	GBT based readout in the CBM experiment. Journal of Instrumentation, 2017, 12, C02061-C02061.	0.5	15
48	The integration and engineering of the ATLAS SemiConductor Tracker Barrel. Journal of Instrumentation, 2008, 3, P10006-P10006.	0.5	13
49	Algorithms for minimization of charge sharing effects in a hybrid pixel detector taking into account hardware limitations in deep submicron technology. Journal of Instrumentation, 2012, 7, C12020-C12020.	0.5	13
50	Back-end and interface implementation of the STS-XYTER2 prototype ASIC for the CBM experiment. Journal of Instrumentation, 2016, 11, C11018-C11018.	0.5	13
51	An Effective Multilevel Offset Correction Technique for Single Photon Counting Pixel Detectors. IEEE Transactions on Nuclear Science, 2016, 63, 1194-1201.	1.2	13
52	Characterisation of silicon strip detectors with a binary readout chip for X-ray imaging. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2000, 454, 214-220.	0.7	12
53	Recent aging studies for the ATLAS transition radiation tracker. IEEE Transactions on Nuclear Science, 2004, 51, 960-967.	1.2	11
54	Evaluation of the UFXC32k photon-counting detector for pump&#x2014;probe experiments using synchrotron radiation. Journal of Synchrotron Radiation, 2018, 25, 413-418.	1.0	11

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55	SCT128B â€” a prototype chip for binary readout of silicon strip detectors. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 1999, 421, 303-315.	0.7	10
56	Performance of a 128 channel analogue front-end chip for readout of Si strip detector modules for LHC experiments. IEEE Transactions on Nuclear Science, 2000, 47, 1434-1441.	1.2	10
57	Study of the response of the ATLAS central calorimeter to pions of energies from 3 to 9GeV. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2009, 607, 372-386.	0.7	10
58	Combined performance studies for electrons at the 2004 ATLAS combined test-beam. Journal of Instrumentation, 2010, 5, P11006-P11006.	0.5	10
59	Results of Tests of Three-Dimensionally Integrated Chips Bonded to Sensors. IEEE Transactions on Nuclear Science, 2015, 62, 349-358.	1.2	10
60	Multichannel mixed-mode IC for digital readout of silicon strip detectors. Microelectronics Reliability, 2002, 42, 427-436.	0.9	9
61	High-speed readout solution for single-photon counting ASICs. Journal of Instrumentation, 2016, 11, C02057-C02057.	0.5	9
62	RG64â€”High Count Rate Low Noise Multichannel ASIC With Energy Window Selection and Continuous Readout Mode. IEEE Transactions on Nuclear Science, 2009, 56, 487-495.	1.2	8
63	A bidirectional 64-channel neurochip for recording and stimulation neural network activity. , 2011, , .		8
64	Development of a Four-Side Buttable X-Ray Detection Module With Low Dead Area Using the UFXC32k Chips With TSVs. IEEE Transactions on Nuclear Science, 2017, 64, 2433-2440.	1.2	8
65	Î±-Synuclein Sterically Stabilizes Spherical Nanoparticle-Supported Lipid Bilayers. ACS Applied Bio Materials, 2019, 2, 1413-1419.	2.3	8
66	Progress in the development of the DTMROC time measurement chip for the ATLAS Transition Radiation Tracker (TRT). IEEE Transactions on Nuclear Science, 2001, 48, 514-519.	1.2	7
67	Low-noise multichannel ASIC for high count rate X-ray diffractometry applications. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2009, 607, 229-232.	0.7	7
68	Time and energy measuring front-end electronics for long silicon strip detectors readout. , 2013, , .		7
69	An on-chip charge cluster reconstruction technique in the miniVIPIC pixel readout chip for X-ray counting and timing. , 2014, , .		7
70	Offset correction system for 128-channel self-triggering readout chip with in-channel 5-bit energy measurement functionality. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2015, 780, 114-118.	0.7	7
71	High frame rate measurements of semiconductor pixel detector readout IC. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2012, 680, 56-60.	0.7	6
72	Low power analog readout front-end electronics for time and energy measurements. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2014, 748, 54-60.	0.7	6

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73	Design of versatile ASIC and protocol tester for CBM readout system. Journal of Instrumentation, 2017, 12, C02060-C02060.	0.5	6
74	Evolution of structure and dynamics of thermo-reversible nanoparticle gels – A combined XPCS and rheology study. Journal of Chemical Physics, 2019, 151, 104902.	1.2	6
75	Engineering for the ATLAS SemiConductor Tracker (SCT) End-cap. Journal of Instrumentation, 2008, 3, P05002-P05002.	0.5	5
76	Design and measurements of 64-channel ASIC for neural signal recording. , 2009, 2009, 528-31.		5
77	TOT01, a time-over-threshold based readout chip in 180nm CMOS technology for silicon strip detectors. Journal of Instrumentation, 2011, 6, C01026-C01026.	0.5	5
78	TOT02, a time-over-threshold based readout chip in 180 nm CMOS process for long silicon strip detectors. , 2011, , .		5
79	Characterisation of the UFXC32k hybrid pixel detector for time-resolved pump-probe diffraction experiments at Synchrotron SOLEIL. Journal of Instrumentation, 2017, 12, C03057-C03057.	0.5	5
80	Fabrication of 3D Hybrid Pixel Detector Modules Based on TSV Processing and Advanced Flip Chip Assembly of Thin Read Out Chips. , 2017, , .		5
81	Advancing Chemical Separations: Unraveling the Structure and Dynamics of Phase Splitting in Liquid-Liquid Extraction. Journal of Physical Chemistry B, 2022, 126, 2420-2429.	1.2	5
82	Comparison of Two Pole-Zero Cancellation Circuits for Fast Charge Sensitive Amplifier in CMOS Technology. , 2007, , .		4
83	MSGCROC - an ASIC for high count rate readout of position sensitive Microstrip Gas Chambers for thermal neutrons. , 2008, , .		4
84	Development of very high rate and resolution neutron detectors with novel readout and DAQ hard- and software in DETNI. , 2008, , .		4
85	Design and measurements of low power multichannel chip for recording and stimulation of neural activity. , 2012, 2012, 4470-4.		4
86	FPGA Simulations of Charge Sharing Effect Compensation Algorithms for Implementation in Deep Sub-Micron Technologies. , 2013, , .		4
87	Results of tests of three-dimensionally integrated chips bonded to sensors. , 2013, , .		4
88	23552-channel IC for single photon counting pixel detectors with 75 $\mu\text{m}$ pitch, ENC of 89 e <sup>+</sup> rms, 19 e <sup>+</sup> rms offset spread and 3% rms gain spread. , 2014, , .		4
89	Comparison of the charge sharing effect in two hybrid pixel detectors of different thickness. Journal of Instrumentation, 2015, 10, C02006-C02006.	0.5	4
90	Measurements of Ultra-Fast single photon counting chip with energy window and 75 $\mu\text{m}$ pixel pitch with Si and CdTe detectors. Journal of Instrumentation, 2017, 12, C03064-C03064.	0.5	4

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91	Pattern Recognition algorithm for charge sharing compensation in single photon counting pixel detectors. Journal of Instrumentation, 2019, 14, C01017-C01017.	0.5	4
92	SPC Pixel IC with 9.4 e <sup>+</sup> rms Offset Spread, 60 e <sup>+</sup> rms ENC and 70 kfps Frame Rate. , 2019, , .		4
93	A state of the art rad-hard digital ASIC design for high energy physics experiments. Measurement Science and Technology, 2007, 18, 2413-2417.	1.4	3
94	MSGCROC -a selftriggered ASIC for readout of hybrid gas microstrip neutron detectors for event rates of 10 &sup>8&/sup> and 2D spatial resolutions &#x0226A;100 &#x003BC;m FWHM. , 2007, , .		3
95	A digital X-ray imaging system based on silicon strip detectors working in edge-on configuration. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2009, 608, 410-416.	0.7	3
96	Low Noise 64-Channel ASIC for AC and DC Coupled Strip Detectors. IEEE Transactions on Nuclear Science, 2011, 58, 187-193.	1.2	3
97	Time-over-threshold processing implementation for silicon detectors with large capacitances. , 2012, , .		3
98	A pixel readout chip in 40 nm CMOS process for high count rate imaging systems with minimization of charge sharing effects. , 2013, , .		3
99	Interface and protocol development for STS read-out ASIC in the CBM experiment at FAIR. Proceedings of SPIE, 2014, , .	0.8	3
100	HyPix-3000 - a large area single-photon counting detector with two discriminator thresholds. , 2014, , .		3
101	Noise optimization of the time and energy measuring ASIC for silicon tracking system. , 2015, , .		3
102	UFXC32k based camera module with a custom soft processor and USB 3.0 for large area detectors. Journal of Instrumentation, 2018, 13, P01017-P01017.	0.5	3
103	High rate proton detection with single photon counting hybrid pixel detector. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2020, 956, 163333.	0.7	3
104	Single software platform used for high speed data transfer implementation in a 65k pixel camera working in single photon counting mode. Journal of Instrumentation, 2015, 10, C12025-C12025.	0.5	3
105	Pixel readout IC for CdTe detectors operating in single photon counting mode with interpixel communication. Journal of Instrumentation, 2022, 17, C01036.	0.5	3
106	Performance of the binary readout of silicon strip detectors using the radiation hard SCT128B chip. IEEE Transactions on Nuclear Science, 1998, 45, 310-314.	1.2	2
107	RG64 &#x2014; high count rate multichannel ASIC with energy window selection for X-ray imaging applications. , 2008, , .		2
108	64 Channel ASIC for Neurobiology Experiments. International Journal of Electronics and Telecommunications, 2010, 56, 375-380.	0.5	2

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109	Photon reconstruction in the ATLAS Inner Detector and Liquid Argon Barrel Calorimeter at the 2004 Combined Test Beam. Journal of Instrumentation, 2011, 6, P04001-P04001.	0.5	2
110	PXD18k - fast single photon counting chip with energy window for hybrid pixel detector. , 2011, , .		2
111	Effective noise minimization in multichannel recording circuits processed in modern technologies for neurobiology experiments. , 2014, , .		2
112	Testing multistage gain and offset trimming in a single photon counting IC with a charge sharing elimination algorithm. Journal of Instrumentation, 2015, 10, C12003-C12003.	0.5	2
113	Methodology of automation process of wafer tests. , 2015, , .		2
114	Digitally assisted low noise and fast signal processing charge sensitive amplifier for single photon counting systems. , 2015, , .		2
115	Characterization of Seamless CdTe Photon Counting X-Ray Detector. IEEE Transactions on Instrumentation and Measurement, 2021, 70, 1-11.	2.4	2
116	Development of a fast readout system for the detection of low energy X rays. AIP Conference Proceedings, 2000, , .	0.3	1
117	Performance of a radiation hard 128 channel analogue front-end chip for the readout of a silicon-based hybrid photon detector. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2003, 500, 362-370.	0.7	1
118	DEDIX - Development of Fully Integrated Multichannel ASIC for High Count Rate Digital X-ray Imaging Systems. , 2006, , .		1
119	On the detection performance of semi-insulating GaAs detectors coupled to multichannel ASIC DX64 for X-ray imaging applications. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2008, 591, 101-104.	0.7	1
120	The CBM Collaboration. Nuclear Physics A, 2009, 830, 942c-944c.	0.6	1
121	A low noise, Fast Pixel Readout IC working in single photon counting mode with energy window selection in 90 nm CMOS. , 2011, , .		1
122	A fast 300k X-Ray camera with an energy window selection and continuous readout mode. , 2013, , .		1
123	Design of the low area monotonic trim DAC in 40 nm CMOS technology for pixel readout chips. Journal of Instrumentation, 2014, 9, C12046-C12046.	0.5	1
124	ADCs in deep submicron technologies for ASICs of pixel architecture. , 2014, , .		1
125	Ultra fast X-ray detection systems in nanometer and 3D technologies. , 2014, , .		1
126	Low noise single photon counting chip with energy window for hybrid pixel detector with precise offset and gain correction. , 2014, , .		1



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127	Charge sensitive amplifier for nanoseconds pulse processing time in CMOS 40 nm technology. , 2015, , .		1
128	32k Channels readout IC for single photon counting detectors with 75 $\mu$ m pitch, ENC of 123 e <sup>-</sup> rms, 9 e <sup>-</sup> rms offset spread and 2% rms gain spread. , 2015, , .		1
129	Simulation approach to charge sharing compensation algorithms with experimental cross-check. Journal of Instrumentation, 2017, 12, C03071-C03071.	0.5	1
130	Multithreshold Pattern Recognition Algorithm for Charge Sharing Compensation in Hybrid Pixel Detectors. , 2018, , .		1
131	Addressing of imperfection of a hybrid pixel sensor for X-ray detection with a circuit for charge sharing cancellation implemented. Journal of Instrumentation, 2018, 13, C12014-C12014.	0.5	1
132	Hybrid Detector with Interpixel Communication for Color X-ray Imaging. , 2021, , .		1
133	X-ray imaging of moving objects using on-chip TDI and MDX methods with single photon counting CdTe hybrid pixel detector. Journal of Instrumentation, 2021, 16, C12014.	0.5	1
134	Performance of a radiation hard 128 channel analogue front-end chip for the readout of a Si-based hybrid photon detector. , 0, , .		0
135	Implementation of the DTMROC-S ASIC for the ATLAS TRT detector in a 0.25 $\mu$ m CMOS technology. , 0, , .		0
136	Radiation hardening of ASICs in deep submicron CMOS technologies. , 2005, 5775, 103.		0
137	Radiation hardness of the mixed-mode ASIC's dedicated for the future high energy physics experiments. , 2005, , .		0
138	Tests of A Roman Pot Prototype for the Totem Experiment. , 0, , .		0
139	Development of a 512-Channel Module for Digital X-Ray Imaging Systems with Silicon Strip Detectors. AIP Conference Proceedings, 2008, , .	0.3	0
140	A pixel readout chip designed in 90nm CMOS process for high count rate imaging systems. , 2009, , .		0
141	Test results of a self-triggering silicon strip detector readout chip. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2009, 607, 244-246.	0.7	0
142	FPDR90 a low noise, fast pixel readout chip in 90 nm CMOS. , 2010, , .		0
143	Multichannel integrated system for fast X-ray imaging applications. , 2010, , .		0
144	Measurements and performance of a low noise 64-channel ASIC with CdTe strip detectors. , 2010, , .		0

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145	Development of a fast readout chip in deep submicron technology for pixel hybrid detectors. , 2011, , .		0
146	Measurements of low noise 64 channel counting ASIC for Si and CdTe strip detectors. Journal of Instrumentation, 2011, 6, C01043-C01043.	0.5	0
147	Readout Electronics for Pixel Detectors in Deep Submicron and 3D Technologies. International Journal of Electronics and Telecommunications, 2011, 57, .	0.5	0
148	A layer correlation technique for pion energy calibration at the 2004 ATLAS Combined Beam Test. Journal of Instrumentation, 2011, 6, P06001-P06001.	0.5	0
149	Prototype readout electronics and silicon strip detector study for the silicon tracking system at compressed baryonic matter experiment. Proceedings of SPIE, 2011, , .	0.8	0
150	Tests of FPDR90 IC for hybrid detector readout for high frame rate X-ray applications. , 2011, , .		0
151	Comparison of two different architectures of multichannel readout ASICs for neurobiological experiments. , 2011, , .		0
152	Tuning the low cut-off frequency in multichannel neural recording amplifiers by the on-chip correction DACs. , 2011, , .		0
153	Minimization of charge sharing effect in silicon hybrid pixel X-ray detectors based on pattern recognition algorithm. , 2012, , .		0
154	Dual Stage Time-over-Threshold processing chain for silicon detectors with large capacitance. , 2013, , .		0
155	ASICs in nanometer and 3D technologies for readout of hybrid pixel detectors. Proceedings of SPIE, 2013, , .	0.8	0
156	CBM Collaboration. Nuclear Physics A, 2014, 931, 1222-1227.	0.6	0
157	Design of system-on-chip in 180 nm technology for multi-channel wireless recording of the nerve tissue electrical activity. , 2014, , .		0
158	Design and testing of integrated circuit of pixel architecture for fast x-ray imaging applications. , 2014, , .		0
159	Design for the testability of the multichannel neural recording and stimulating integrated circuit. , 2015, , .		0
160	Trimming the threshold dispersion below 10 e-rms in a large area readout IC working in a single photon counting mode. Journal of Instrumentation, 2016, 11, C01067-C01067.	0.5	0
161	Microstrip and gas electron multiplier readout ASIC for physics experiment at FAIR. , 2017, , .		0
162	Single photon counting integrated circuit operating with CdTe pixel detector. , 2017, , .		0

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163	Design of Matrix Controller for Hybrid Pixel Detectors. , 2018, , .		0
164	Development of On-Chip Calibration for Hybrid Pixel Detectors. , 2021, , .		0