Chanon Warisarn

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/6419005/publications.pdf

Version: 2024-02-01

1684188 1588992 12 67 5 8 citations h-index g-index papers 12 12 12 26 docs citations times ranked citing authors all docs

#	Article	IF	CITATIONS
1	Constrained codes in single-reader/two-track reading bit-patterned magnetic recording. AIP Advances, 2021, 11, 015009.	1.3	4
2	Soft-information flipper based on long-short term memory networks for ultra-high density magnetic recording. AIP Advances, 2021, 11 , .	1.3	3
3	A simple skew angle detection and suppression method for bit-patterned magnetic recording. AIP Advances, 2021, 11 , .	1.3	4
4	Long-Short Term Memory-Based Application on Adaptive Cross-Platform Decoder for Bit Patterned Magnetic Recording. IEEE Access, 2020, 8, 155248-155259.	4.2	7
5	Optimal Array-Reader and Track Misregistration Mitigation Method in a Three-Reader/Four-Track Reading Bit-Patterned Magnetic Recording System. IEEE Magnetics Letters, 2020, 11, 1-5.	1.1	2
6	Soft-information flipping scheme based on a priori LLRs summation for ultra-high density magnetic recording. AIP Advances, 2020, 10, 025217.	1.3	2
7	Modified Multitrack Joint Two-Dimensional Viterbi Detectors for Rate-4/5 and Rate-5/6 Modulation Codes in Bit-Patterned Magnetic Recording Systems. IEEE Magnetics Letters, 2020, 11, 1-5.	1.1	3
8	Mitigating the Effects of Track Mis-Registration in Single-Reader/Two-Track Reading BPMR Systems. IEEE Transactions on Magnetics, 2019, 55, 1-6.	2.1	8
9	An Iterative Two-Head Two-Track Detection Method for Staggered Bit-Patterned Magnetic Recording Systems. IEEE Transactions on Magnetics, 2019, 55, 1-7.	2.1	12
10	Improvement in Bit Error Rate With a Combination of a Rate-3/4 Modulation Code and Intertrack Interference Subtraction for Array-Reader-Based Magnetic Record Letters, 2019, 10, 1-5.	lin g.1 EEE 1	Magnetics
11	Reduced Complexity Multi-Track Joint Detector for Sidetrack Data Estimation in High Areal Density BPMR. IEEE Transactions on Magnetics, 2018, 54, 1-5.	2.1	8
12	Mitigation of TMR Using Energy Ratio and Bit-Flipping Techniques in Multitrack Multihead BPMR Systems. IEEE Transactions on Magnetics, 2017, 53, 1-4.	2.1	3