Satyabrata Dash

List of Publications by Year in descending order

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		2258059	2053705	
18	75	3	5	
papers	citations	h-index	g-index	
18 all docs	18 docs citations	18 times ranked	55 citing authors	

#	Article	IF	CITATIONS
1	RiverOpt: A Multiobjective Optimization Framework Based on Modified River Formation Dynamics Heuristic. , 2019, , .		1
2	StormOptimus: A Single Objective Constrained Optimizer Based on Brainstorming Process for VLSI Circuits. Adaptation, Learning, and Optimization, 2019, , 221-243.	0.6	0
3	A Cooperative Co-evolution based Scalable Framework for Solving Large-Scale Global optimization Problems. , 2019, , .		O
4	Convergence Analysis of River Formation Dynamics Algorithm., 2019,,.		0
5	Analysis, Modeling and Optimization of Equal Segment Based Approximate Adders. IEEE Transactions on Computers, 2019, 68, 314-330.	3.4	15
6	A hierarchy in mutation of genetic algorithm and its application to multi-objective analog/RF circuit optimization. Analog Integrated Circuits and Signal Processing, 2018, 94, 27-47.	1.4	5
7	Minimizing area of VLSI power distribution networks using river formation dynamics. Journal of Systems and Information Technology, 2018, 20, 417-429.	1.7	4
8	Multiobjective analog/RF circuit sizing using an improved brain storm optimization algorithm. Memetic Computing, 2018, 10, 423-440.	4.0	8
9	PGIREM: Reliability-Constrained IR Drop Minimization and Electromigration Assessment of VLSI Power Grid Networks Using Cooperative Coevolution. , 2018, , .		9
10	Optimization of 2.4 GHz CMOS Low Noise Amplifier Using Hybrid Particle Swarm Optimization with LA@vy Flight. , 2017, , .		8
11	Markov Chain Model Using Lévy Flight for VLSI Power Grid Analysis. , 2017, , .		5
12	Analog circuit optimization using adjoint network based sensitivity analysis. AEU - International Journal of Electronics and Communications, 2017, 82, 221-225.	2.9	3
13	Applying River Formation Dynamics to Analyze VLSI Power Grid Networks. , 2016, , .		2
14	CMOS analog circuit optimization via river formation dynamics. , 2016, , .		5
15	Analog Circuit Optimization Based on Hybrid Particle Swarm Optimization. , 2015, , .		8
16	Power grid analysis on parallel computing platforms. , 2015, , .		1
17	A method of analog circuit optimization using adjoint sensitivity analysis. , 2015, , .		1
18	Parallel two step random walk algorithm to analyze VLSI power grid networks. , 2015, , .		0