

Edoardo Bonizzoni

List of Publications by Year in descending order

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times ranked

691
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#	ARTICLE	IF	CITATIONS
1	A MEMS-CMOS Microsystem for Contact-Less Temperature Measurements. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 75-87.	5.4	7
2	An Incremental- $\Sigma\Delta$ ADC With 106-dB DR for Reconfigurable Class-D Audio Amplifiers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 929-933.	3.0	2
3	Special Issue on the 2022 IEEE International Symposium on Circuits and Systems. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 2393-2393.	3.0	0
4	High Responsivity Thermopile Sensors Featuring a Mosaic Structure. Micromachines, 2022, 13, 934.	2.9	1
5	Acquisition RX Chain for PMUT-Based Highly Integrated Ultrasound Imaging Systems. , 2022, , .		2
6	A 2+1 Hybrid Incremental MASH Converter. , 2022, , .		0
7	A Fully-Differential Delay-Line Based Control for Resonant Switched-Capacitor Converter. , 2022, , .		0
8	Design of an LLC Resonant DC-DC Converter with MOSFET-Based Active Rectifier. , 2022, , .		0
9	Study of a Voltage-Mode Readout Configuration for Micromachined CMOS Transistors for Uncooled IR Sensing. , 2021, , .		4
10	Thermal Sensors for Contactless Temperature Measurements, Occupancy Detection, and Automatic Operation of Appliances during the COVID-19 Pandemic: A Review. Micromachines, 2021, 12, 148.	2.9	31
11	Guest Editorial Special Issue on the 2021 IEEE International Symposium on Circuits and Systems. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 1565-1565.	3.0	0
12	Guest Editorial Special Issue on the 2021 ISICAS: A CAS Journal Track Symposium. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3037-3037.	3.0	0
13	On the Linearity of BJT-Based Current-Mode DAC Drivers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3138-3142.	3.0	2
14	A Hybrid Single-Inductor Bipolar-Output DC-DC Converter With Floating Negative Output for AMOLED Displays. IEEE Journal of Solid-State Circuits, 2021, 56, 2760-2769.	5.4	13
15	A Power Switch Size Optimization Strategy for Multi-Switch DC-DC Converters. , 2021, , .		0
16	A 170.7-dB FoM-DR 0.45/0.6-V Inverter-Based Continuous-Time Sigma- Δ Modulator. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 1384-1388.	3.0	12
17	Guest Editorial Special Issue on the 2020 ISICAS: A CAS Journal Track Symposium. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 1493-1493.	3.0	0
18	A Power-Efficient Hybrid Single-Inductor Bipolar-Output DC-DC Converter with Floating Negative Output for AMOLED Displays. , 2020, , .		4

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19	Guest Editorial Special Issue on the 2020 IEEE International Symposium on Circuits and Systems. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 805-805.	3.0	0
20	6.25-6.5 GHz, 1-µV input resolution auxiliary circuit assisted comparator in 65-nm CMOS process. IET Circuits, Devices and Systems, 2020, 14, 340-346.	1.4	5
21	A SAR-ADC-Assisted DC-DC Buck Converter With Fast Transient Recovery. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 1669-1673.	3.0	7
22	A 1-V, 3-GHz Strong-Arm Latch Voltage Comparator for High Speed Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 2918-2922.	3.0	26
23	An 86% Efficiency, Wide-V _{in} SIMO DC-DC Converter Embedded in a Car-Radio IC. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3598-3609.	5.4	8
24	An Integrated Micromachined Thermopile Sensor With a Chopper Interface Circuit for Contact-Less Temperature Measurements. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3402-3413.	5.4	16
25	A Behavioral Model for Solar Cells With Transient Irradiation and Temperature Assessment. IEEE Access, 2019, 7, 90882-90890.	4.2	7
26	An Integrated Thermopile-Based Sensor with a Chopper-Stabilized Interface Circuit for Presence Detection. Sensors, 2019, 19, 3999.	3.8	7
27	Study of DAC Architectures for Integrated Laser Driver Systems. , 2019, , .		1
28	A 5-Bit 10-GS/sec Flash ADC with Resolution Enhancement using Metastability Detection. , 2019, , .		1
29	A Chopper Interface Circuit for Thermopile-Based Thermal Sensors. , 2019, , .		3
30	Guest Editorial Special Issue on the 2019 ISICAS: A CAS Journal Track Symposium. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1607-1607.	3.0	0
31	Analysis of Parasitic Effects in Filamentary-Switching Memristive Memories Using an Approximated Verilog-A Memristor Model. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 1935-1947.	5.4	2
32	A Voltage-Time Model for Memristive Devices. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 1452-1460.	3.1	6
33	Capacitance Super Multiplier for Sub-Hertz Low-Pass Integrated Filters. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 301-305.	3.0	14
34	Guest Editorial Special Issue on the 2018 ISICAS: A CAS Journal Track Symposium. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1289-1289.	3.0	0
35	A 10-MHz Bandwidth Two-Path Third-Order $\Sigma\Delta$ Modulator With Cross-Coupling Branches. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1410-1414.	3.0	3
36	Magneto-resistive Biosensors for On-Chip Detection and Localization of Paramagnetic Particles. IEEE Journal of Electromagnetics, RF and Microwaves in Medicine and Biology, 2018, 2, 179-185.	3.4	14

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37	An Approximated Verilog-A Model for Memristive Devices. , 2018, , .		3
38	A 0.4-V Supply Curvature-Corrected Reference Generator With 84.5-ppm/°C Average Temperature Coefficient Within $\hat{\sim}40$ °C to 130 °C. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 362-366.	3.0	20
39	High-Resolution Time-Interleaved Eight-Channel ADC for Li-Ion Battery Stacks. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 620-624.	3.0	4
40	A low-power low-noise CMOS voltage reference with improved PSR for wearable sensor systems. , 2017, , .		8
41	Feasibility Study of an Ultra High Speed Current-Mode SAR ADC. , 2017, , .		1
42	A capacitive sensor interface for high-resolution acquisitions in hostile environments. , 2016, , .		1
43	CMOS Vertical Hall Magnetic Sensors on Flexible Substrate. IEEE Sensors Journal, 2016, 16, 8736-8743.	4.7	55
44	Mismatch and parasitics limits in capacitors-based SAR ADCs. , 2016, , .		3
45	A 10-b 200-kS/s 250-nA Self-Clocked Coarse-Fine SAR ADC. IEEE Transactions on Circuits and Systems II: Express Briefs, 2016, 63, 924-928.	3.0	14
46	Very-low-voltage and ultra-low-power analog circuits for nomadic applications. , 2016, , .		0
47	A mode-of-operation based switching technique for SIDO Buck-boost converter. , 2016, , .		2
48	A pipeline ADC for very high conversion rates. , 2016, , .		1
49	An 8-bit 0.7-GS/s single channel flash-SAR ADC in 65-nm CMOS technology. , 2016, , .		7
50	Energy-efficient switching method for SAR ADCs with bottom plate sampling. Electronics Letters, 2016, 52, 690-692.	1.0	10
51	Smart-DEM for Energy-Efficient Incremental ADCs. , 2016, , 3-22.		1
52	Design of an op-amp free voltage reference with PWM regulation. , 2015, , .		0
53	Optimal geometry of CMOS voltage-mode and current-mode vertical magnetic hall sensors. , 2015, , .		8
54	A high-resolution low-power and multi-bit incremental converter with smart-DEM. Analog Integrated Circuits and Signal Processing, 2015, 82, 663-674.	1.4	4

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55	Interference rejection in quadrature band-pass $\Sigma\Delta$ modulators. Analog Integrated Circuits and Signal Processing, 2015, 83, 95-101.	1.4	0
56	A CMOS Current-Mode Magnetic Hall Sensor With Integrated Front-End. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 1270-1278.	5.4	79
57	Design of a low power time to digital converter for flow metering applications. , 2015, , .		0
58	Voltage reference architectures for low-supply-voltage low-power applications. Microelectronics Journal, 2015, 46, 1012-1019.	2.0	9
59	A single Op-Amp $\Sigma\Delta$ modulator. , 2015, , .		0
60	A split transconductor high-speed SAR ADC. , 2015, , .		0
61	A 2+1 multi-bit incremental architecture using Smart-DEM algorithm. , 2014, , .		0
62	Analysis and modeling of four-folded vertical Hall devices in current domain. , 2014, , .		10
63	A 0.18- μ m CMOS current-mode Hall magnetic sensor with very low bias current and high sensitive front-end. , 2014, , .		13
64	High-order multi-bit incremental converter with Smart-DEM algorithm. , 2013, , .		7
65	A 105-dB SNDR, 10 kSps multi-level second-order incremental converter with smart-DEM consuming 280 μ W and 3.3-V supply. , 2013, , .		14
66	A 1-V 1.1-MHz BW digitally assisted multi-bit multi-rate hybrid CT $\Sigma\Delta$ with 78-dB SFDR. , 2012, , .		2
67	High-resolution multi-bit second-order incremental converter with 1.5- μ V residual offset and 94-dB SFDR. Analog Integrated Circuits and Signal Processing, 2012, 72, 531-539.	1.4	16
68	A 88-dB DR, 84-dB SNDR Very Low-Power Single Op-Amp Third-Order $\Sigma\Delta$ Modulator. IEEE Journal of Solid-State Circuits, 2012, 47, 2107-2118.	5.4	36
69	Interference rejection in delay line based quadrature band-pass $\Sigma\Delta$ modulators. , 2012, , .		0
70	Exact design of continuous-time sigma-delta modulators with multiple feedback DACs. Analog Integrated Circuits and Signal Processing, 2012, 73, 255-264.	1.4	0
71	70-MHz IF 10-MHz bandwidth bandpass $\Sigma\Delta$ modulator for WCDMA applications. Analog Integrated Circuits and Signal Processing, 2012, 71, 411-419.	1.4	0
72	Mono-rate and multi-rate hybrid continuous-time $\Sigma\Delta$ modulators with SC feedback DAC. , 2011, , .		0

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73	A low-power third-order $\Sigma\Delta$ modulator using a single operational amplifier. , 2011, , .		0
74	Two op-amps third-order $\Sigma\Delta$ modulator with 61-dB SNDR, 6-MHz bandwidth and 6-mW power consumption. Analog Integrated Circuits and Signal Processing, 2011, 66, 381-388.	1.4	15
75	A high efficiency 4-output single inductor DC-DC buck converter with self boosted snubber. Analog Integrated Circuits and Signal Processing, 2011, 67, 169-177.	1.4	2
76	A 200-mA, 93% peak power efficiency, single-inductor, dual-output DC-DC buck converter. Analog Integrated Circuits and Signal Processing, 2010, 62, 121-129.	1.4	14
77	An ultra-low power successive approximation A/D converter with time-domain comparator. Analog Integrated Circuits and Signal Processing, 2010, 64, 183-190.	1.4	24
78	Low-power ripple-free chopper amplifier with correlated double sampling de-chopping. , 2010, , .		2
79	Digitally assisted multi-Bit $\Sigma\Delta$ modulator. , 2010, , .		2
80	Design of a 70-MHz IF 10-MHz bandwidth bandpass $\Sigma\Delta$ modulator for WCDMA applications. , 2010, , .		1
81	A Micropower Chopper-CDS Operational Amplifier. IEEE Journal of Solid-State Circuits, 2010, 45, 2521-2529.	5.4	46
82	A two op-amps third-order $\Sigma\Delta$ modulator with complex conjugate NTF zeros. , 2010, , .		0
83	Pseudorandom sequence generation for mismatch analog compensation of ADCs. , 2010, , .		6
84	High efficiency DC-DC buck converter with 60/120-MHz switching frequency and 1-A output current. , 2009, , .		9
85	Time variant digital sigma-delta modulator for fractional-N frequency synthesizers. , 2009, , .		4
86	$\Sigma\Delta$ time interleaved current steering DAC with dynamic elements matching. , 2009, , .		4
87	Slew-rate and gain enhancement in two stage operational amplifiers. , 2009, , .		34
88	Design of an ultra-low power SA-ADC with medium/high resolution and speed. , 2008, , .		13
89	40 MHz IF 1 MHz Bandwidth Two-Path Bandpass $\Sigma\Delta$ Modulator With 72 dB DR Consuming 16 mW. IEEE Journal of Solid-State Circuits, 2008, 43, 1648-1656.	5.4	27
90	Third-order $\Sigma\Delta$ modulator with 61-dB SNR and 6-MHz bandwidth consuming 6 mW. , 2008, , .		15

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91	Design of an ultra-low power time interleaved SAR converter. , 2008, , .		5
92	A voltage-to-pulse converter for very high frequency DC-DC converters. , 2008, , .		7
93	On the design of single-inductor double-output DC-DC buck, boost and buck-boost converters. , 2008, , .		13
94	Band-pass " architectures with single and two parallel paths. , 2008, , .		0
95	On the design of single-inductor multiple-output DC-DC buck converters. , 2008, , .		29
96	Design of a current mode 6-bit 100 MS/s flash A/D converter with 0.75 pJ/conv-lev FoM. , 2007, , .		1
97	Staircase-down SET programming approach for phase-change memories. Microelectronics Journal, 2007, 38, 1064-1069.	2.0	12