

MarÃ-a JosÃ© Avedillo de Juan

List of Publications by Year in descending order

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43
papers

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docs citations

43
times ranked

109
citing authors

#	ARTICLE	IF	CITATIONS
1	Using multi-threshold threshold gates in RTD-based logic design: A case study. <i>Microelectronics Journal</i> , 2008, 39, 241-247.	2.0	28
2	How Frequency Injection Locking Can Train Oscillatory Neural Networks to Compute in Phase. <i>IEEE Transactions on Neural Networks and Learning Systems</i> , 2022, 33, 1996-2009.	11.3	21
3	Oscillatory Neural Networks Using VO ₂ Based Phase Encoded Logic. <i>Frontiers in Neuroscience</i> , 2021, 15, 655823.	2.8	15
4	Comparative Analysis of Projected Tunnel and CMOS Transistors for Different Logic Application Areas. <i>IEEE Transactions on Electron Devices</i> , 2016, 63, 5012-5020.	3.0	13
5	Improved Nanopipelined RTD Adder Using Generalized Threshold Gates. <i>IEEE Nanotechnology Magazine</i> , 2011, 10, 155-162.	2.0	12
6	Operation Limits for RTD-Based MOBILE Circuits. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2009, 56, 350-363.	5.4	11
7	Reducing the Impact of Reverse Currents in Tunnel FET Rectifiers for Energy Harvesting Applications. <i>IEEE Journal of the Electron Devices Society</i> , 2017, 5, 530-534.	2.1	7
8	Correct DC Operation in RTD-based Ternary Inverters. , 2007, , .		6
9	RTD-€CMOS Pipelined Networks for Reduced Power Consumption. <i>IEEE Nanotechnology Magazine</i> , 2011, 10, 1217-1220.	2.0	6
10	Bifurcation diagrams in MOS-NDR frequency divider circuits. , 2012, , .		6
11	Simplified single-phase clock scheme for MOBILE networks. <i>Electronics Letters</i> , 2011, 47, 648.	1.0	5
12	A Practical Parallel Architecture for Stacks Filters. <i>Journal of Signal Processing Systems</i> , 2004, 38, 91-100.	1.0	4
13	A novel contribution to the RTD-based threshold logic family. , 2008, , .		4
14	Domino inspired MOBILE networks. <i>Electronics Letters</i> , 2012, 48, 292.	1.0	4
15	Monostable-Bistable Transition Logic Elements: Threshold Logic vs. Boolean Logic Comparison. , 2006, , .		3
16	Experimental Validation of a Two-Phase Clock Scheme for Fine-Grained Pipelined Circuits Based on Monostable to Bistable Logic Elements. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2014, 22, 2238-2242.	3.1	3
17	Impact of the RT-€level architecture on the power performance of tunnel transistor circuits. <i>International Journal of Circuit Theory and Applications</i> , 2018, 46, 647-655.	2.0	3
18	Phase Transition Device for Phase Storing. <i>IEEE Nanotechnology Magazine</i> , 2020, 19, 107-112.	2.0	3

#	ARTICLE	IF	CITATIONS
19	LP-LV high-performance monolithic DTMF receiver with on-chip test facilities. , 2003, , .		2
20	Limits to a Correct Evaluation in RTD-based Ternary Inverters. , 2006, , .		2
21	Holding preserving in RTD-based multiple-valued quantizers. , 2007, , .		2
22	Limits to a Correct Evaluation in RTD-Based Quaternary Inverters. Multiple-Valued Logic (ISMVL), Proceedings of the International Symposium on, 2007, , .	0.0	2
23	Design of RTD-Based NMIN/NMAX Gates. , 2008, , .		2
24	Fast and area efficient multi-input Muller C-element based on MOS-NDR. , 2009, , .		2
25	Evaluation of RTD-CMOS Logic Gates. , 2010, , .		2
26	Single phase MOS-NDR mobile networks. , 2010, , .		2
27	Two-Phase RTD-CMOS Pipelined Circuits. IEEE Nanotechnology Magazine, 2012, 11, 1063-1066.	2.0	2
28	Novel pipeline architectures based on Negative Differential Resistance devices. Microelectronics Journal, 2013, 44, 807-813.	2.0	2
29	Impact of pipeline in the power performance of tunnel transistor circuits. , 2016, , .		2
30	Insights Into the Dynamics of Coupled VO2 Oscillators for ONNs. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3356-3360.	3.0	2
31	COPAS: A New Algorithm for the Partial Input Encoding Problem. VLSI Design, 2002, 14, 171-181.	0.5	2
32	DC Correct Operation in MOBILE Inverters. Midwest Symposium on Circuits and Systems, 2006, , .	1.0	1
33	Analysis of the critical rise time in MOBILE-based circuits. , 2008, , .		1
34	Limits to a correct operation in RTD-based ternary inverters. , 2008, , .		1
35	An improved RNS generator 2^k based on threshold logic. , 2010, , .		1
36	Approaching the Design of Energy Recovery Logic Circuits Using TFETs. IEEE Nanotechnology Magazine, 2020, 19, 500-507.	2.0	1

#	ARTICLE	IF	CITATIONS
37	Compact and Power Efficient MOS-NDR Muller C-Elements. International Federation for Information Processing, 2012, , 437-442.	0.4	1
38	Pass-transistor based implementations of threshold logic gates for WOS filtering. Microelectronics Journal, 2004, 35, 869-873.	2.0	0
39	A quasi-differential quantizer based on SMOBILE. , 2007, , .		0
40	Operation limits in RTD-based ternary quantizers. , 2007, , .		0
41	Evaluation of MOBILE-based gate-level pipelining augmenting CMOS with RTDs. , 2011, , .		0
42	Efficient realization of RTD-CMOS logic gates. , 2011, , .		0
43	Two-Phase MOBILE Interconnection Schemes for Ultra-Grain Pipeline Applications. Lecture Notes in Computer Science, 2013, , 166-174.	1.3	0