

Jinbao Zhang

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/6376646/publications.pdf>

Version: 2024-02-01

15
papers

62
citations

1684188

5
h-index

1588992

8
g-index

15
all docs

15
docs citations

15
times ranked

45
citing authors

#	ARTICLE	IF	CITATIONS
1	A Differential Fault Attack on Security Vehicle System Applied SIMON Block Cipher. IEEE Transactions on Intelligent Transportation Systems, 2023, 24, 12900-12911.	8.0	3
2	An efficient differential fault attack against SIMON key schedule. Journal of Information Security and Applications, 2022, 66, 103155.	2.5	3
3	HoneyComb ROS: A 6 Å— 6 Non-Blocking Optical Switch with Optimized Reconfiguration for ONOCs. Electronics (Switzerland), 2019, 8, 844.	3.1	7
4	A Countermeasure against DPA on SIMON with an Area-Efficient Structure. Electronics (Switzerland), 2019, 8, 240.	3.1	1
5	Securing the AES Cryptographic Circuit Against Both Power and Fault Attacks. Journal of Electrical Engineering and Technology, 2019, 14, 2171-2180.	2.0	8
6	A novel differential fault analysis using two-byte fault model on AES Key schedule. IET Circuits, Devices and Systems, 2019, 13, 661-666.	1.4	17
7	An Algorithmic Framework to Construct Optical Switch via Scaling From N-to-2N Ports for Optical Network on Chip. IEEE Access, 2019, 7, 101427-101440.	4.2	1
8	A new method for resisting collision attack based on parallel random delay S-box. IEICE Electronics Express, 2019, 16, 20190192-20190192.	0.8	2
9	Fault attack hardware Trojan detection method based on ring oscillator. IEICE Electronics Express, 2019, 16, 20190143-20190143.	0.8	0
10	A Novel Differential Fault Analysis on the Key Schedule of SIMON Family. Electronics (Switzerland), 2019, 8, 93.	3.1	4
11	A Compact Hardware Implementation for the SCA-resistant PRESENT Cipher. , 2019, , .		3
12	High performance AES-GCM implementation based on efficient AES and FR-KOA multiplier. IEICE Electronics Express, 2018, 15, 20180559-20180559.	0.8	5
13	Countermeasure against fault sensitivity analysis based on clock check block. IEICE Electronics Express, 2018, 15, 20180433-20180433.	0.8	1
14	Against transient-steady effect attack using time check blocks. , 2017, , .		1
15	Against fault attacks based on random infection mechanism. IEICE Electronics Express, 2016, 13, 20160872-20160872.	0.8	6