

# Javier Ardila

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/637029/publications.pdf>

Version: 2024-02-01

9  
papers

41  
citations

3311381

1  
h-index

3475538

1  
g-index

9  
all docs

9  
docs citations

9  
times ranked

38  
citing authors

#	ARTICLE	IF	CITATIONS
1	On the Cross-Correlation Based Loop Gain Adaptation for Bang-Bang CDRs. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 1169-1180.	5.4	1
2	A Family of Compact Trim-Free CMOS Nano-Ampere Current References. , 2019, , .		7
3	A Novel Loop Gain Adaptation Method for Digital CDRs Based on the Cross-Correlation Function. , 2019, , .		2
4	An All-Thin-Devices Level Shifter in Standard-Cell Format for Auto Place-and-Route Flow. , 2019, , .		3
5	Stochastic resonance in bang-bang phase detector gain and the impact on CDR locking. , 2018, , .		1
6	A system-on-chip platform for the internet of things featuring a 32-bit RISC-V based microcontroller. , 2017, , .		9
7	A Digital Offset Reduction Method for Dynamic Comparators Based on Phase Measurement. , 2017, , .		3
8	On the impact of channel loss on CDR locking. , 2016, , .		1
9	A 32-bit RISC-V AXI4-lite bus-based microcontroller with 10-bit SAR ADC. , 2016, , .		14