

Nobutaka Kuroki

List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

63
papers

540
citations

9
h-index

21
g-index

64
ext. papers

710
ext. citations

1
avg, IF

3.68
L-index

#	Paper	IF	Citations
63	Combination of Convolutional Neural Network Architecture and its Learning Method for Rotation-Invariant Handwritten Digit Recognition. <i>IEEJ Transactions on Electrical and Electronic Engineering</i> , 2021 , 16, 161-163	1	2
62	A 35-mV supply ring oscillator consisting of stacked body bias inverters for extremely low-voltage LSIs. <i>IEICE Electronics Express</i> , 2021 , 18, 20210065-20210065	0.5	0
61	Automated Fish Bone Detection in X-Ray Images with Convolutional Neural Network and Synthetic Image Generation. <i>IEEJ Transactions on Electrical and Electronic Engineering</i> , 2021 , 16, 1510	1	1
60	Multi-Category Image Super-Resolution with Convolutional Neural Network and Multi-Task Learning. <i>IEICE Transactions on Information and Systems</i> , 2021 , E104.D, 183-193	0.6	2
59	Detecting Tampered Region in video using LSTM and U-Net. <i>IEEJ Transactions on Electronics, Information and Systems</i> , 2020 , 140, 476-483	0.1	
58	An 11.8 nA ultra-low power active diode using a hysteresis common gate comparator for low-power energy harvesting systems. <i>IEICE Electronics Express</i> , 2020 , 17, 20200103-20200103	0.5	0
57	Super-Resolution with Multi-Path Convolutional Neural Networks. <i>IEEJ Transactions on Electronics, Information and Systems</i> , 2020 , 140, 638-650	0.1	1
56	Improvement of Luminance Isotropy for Convolutional Neural Networks-Based Image Super-Resolution. <i>IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences</i> , 2020 , E103.A, 955-958	0.4	1
55	Detecting tampered region in video using LSTM and U-Net. <i>Electronics and Communications in Japan</i> , 2020 , 103, 15-25	0.4	
54	Detecting tampered regions in JPEG images via CNN 2020 ,		1
53	A 34-mV Startup Ring Oscillator Using Stacked Body Bias Inverters for Extremely Low-Voltage Thermoelectric Energy Harvesting 2020 ,		1
52	Sub-0.1V Input, Low-Voltage CMOS Driver Circuit for Multi-Stage Switched Capacitor Voltage Boost Converter 2019 ,		2
51	A Sub-1- μ s Start-Up Time, Fully-Integrated 32-MHz Relaxation Oscillator for Low-Power Intermittent Systems. <i>IEICE Transactions on Electronics</i> , 2018 , E101.C, 161-169	0.4	0
50	Super-Resolution with Horizontal and Vertical Convolutional Neural Networks. <i>IEEJ Transactions on Electronics, Information and Systems</i> , 2018 , 138, 957-963	0.1	
49	Detecting Doctored Region in JPEG Image using Convolutional Neural Networks. <i>IEEJ Transactions on Electronics, Information and Systems</i> , 2018 , 138, 1417-1424	0.1	2
48	An ultra-low power active diode using a hysteresis common gate comparator for low-voltage and low-power energy harvesting systems 2018 ,		1
47	Switched-Capacitor Voltage Buck Converter with Step-Down-Ratio and Clock-Frequency Controllers for Ultra-Low-Power IoT Devices 2018 ,		3

46	An Area-Efficient Resistor-less On-Chip Frequency Reference for Ultra-Low Power Real-Time Clock Application. <i>IEEJ Transactions on Electrical and Electronic Engineering</i> , 2018 , 13, 1633-1641	1	2
45	Analytical Study of Multi-stage Switched-Capacitor Voltage Boost Converter for Ultra-low Voltage Energy Harvesting 2018 ,		3
44	An 80-mV-to-1.8-V Conversion-Range Low-Energy Level Shifter for Extremely Low-Voltage VLSIs. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2017 , 64, 2026-2035	3.9	20
43	Ultralow-quiescent-current and wide-load-range low-dropout linear regulator with self-biasing technique for micropower battery management. <i>Japanese Journal of Applied Physics</i> , 2017 , 56, 04CF11	1.4	3
42	Impedance matching in magnetic-coupling-resonance wireless power transfer for small implantable devices 2017 ,		3
41	A 0.10.6 V input range voltage boost converter with low-leakage driver for low-voltage energy harvesting 2017 ,		8
40	An ultra-low-power supercapacitor voltage monitoring system for low-voltage energy harvesting 2017 ,		3
39	Multi-Channel Convolutional Neural Networks for Image Super-Resolution. <i>IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences</i> , 2017 , E100.A, 572-580	0.4	6
38	Detecting Doctored JPEG Image Based on Block Noise Analysis and Double JPEG Analysis. <i>IEEJ Transactions on Electronics, Information and Systems</i> , 2017 , 137, 742-749	0.1	4
37	Fully-Integrated High-Conversion-Ratio Dual-Output Voltage Boost Converter With MPPT for Low-Voltage Energy Harvesting. <i>IEEE Journal of Solid-State Circuits</i> , 2016 , 51, 2398-2407	5.5	46
36	Image super-resolution with multi-channel convolutional neural networks 2016 ,		4
35	Locally Weighted Averaging for Denoising of Medical Tomographic Images. <i>Journal of Signal Processing</i> , 2016 , 20, 217-220	0.2	2
34	A Highly Efficient Switched-Capacitor Voltage Boost Converter with Nano-Watt MPPT Controller for Low-Voltage Energy Harvesting. <i>IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences</i> , 2016 , E99.A, 2491-2499	0.4	8
33	A 0.38- μ W stand-by power, 50-nA-to-1-mA load current range DC-DC converter with self-biased linear regulator for ultra-low power battery management 2016 ,		5
32	A 0.21-V minimum input, 73.6% maximum efficiency, fully integrated 3-terminal voltage converter with MPPT for low-voltage energy harvesters 2015 ,		5
31	A 0.19-V minimum input low energy level shifter for extremely low-voltage VLSIs 2015 ,		15
30	A fully-integrated, high-conversion-ratio and dual-output voltage boost converter with MPPT for low-voltage energy harvesting 2015 ,		5
29	Energy-efficient AES SubBytes transformation circuit using asynchronous circuits for ultra-low voltage operation. <i>IEICE Electronics Express</i> , 2015 , 12, 20141157-20141157	0.5	2

28	An Energy-Efficient 24T Flip-Flop Consisting of Standard CMOS Gates for Ultra-Low Power Digital VLSIs. <i>IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences</i> , 2015 , E98.A, 2600-2606	0.4	2
27	A 0.21-V minimum input, 73.6% maximum efficiency, fully integrated voltage boost converter with MPPT for low-voltage energy harvesters 2014 ,		15
26	A 24-transistor static flip-flop consisting of nors and inverters for low-power digital vlsis 2014 ,		2
25	1.2-V Supply, 100-nW, 1.09-V Bandgap and 0.7-V Supply, 52.5-nW, 0.55-V Subbandgap Reference Circuits for Nanowatt CMOS LSIs. <i>IEEE Journal of Solid-State Circuits</i> , 2013 , 48, 1530-1538	5.5	143
24	A 32.55-kHz, 472-nW, 120ppm/°C, fully on-chip, variation tolerant CMOS relaxation oscillator for a real-time clock application 2013 ,		15
23	A nano-watt power CMOS amplifier with adaptive biasing for power-aware analog LSIs 2012 ,		8
22	A Low-Power Level Shifter With Logic Error Correction for Extremely Low-Voltage Digital CMOS LSIs. <i>IEEE Journal of Solid-State Circuits</i> , 2012 , 47, 1776-1783	5.5	67
21	A 6.66-kHz, 940-nW, 56ppm/°C, fully on-chip PVT variation tolerant CMOS relaxation oscillator 2012 ,		5
20	A low-power single-slope analog-to-digital converter with digital PVT calibration 2012 ,		6
19	A level shifter circuit design by using input/output voltage monitoring technique for ultra-low voltage digital CMOS LSIs 2011 ,		5
18	A 18.9-nA standby current comparator with adaptive bias current generator 2011 ,		6
17	A wide input voltage range level shifter circuit for extremely low-voltage digital LSIs. <i>IEICE Electronics Express</i> , 2011 , 8, 890-896	0.5	3
16	A level shifter with logic error correction circuit for extremely low-voltage digital CMOS LSIs 2011 ,		4
15	A 105-nW CMOS thermal sensor for power-aware applications 2011 ,		1
14	A 95-nA, 523ppm/°C, 0.6- μ W CMOS current reference circuit with subthreshold MOS resistor ladder 2011 ,		2
13	High current efficiency sense amplifier using body-bias control for ultra-low-voltage SRAM 2011 ,		2
12	Subthreshold SRAM with Write Assist Technique Using On-Chip Threshold Voltage Monitoring Circuit. <i>IEICE Transactions on Electronics</i> , 2011 , E94-C, 1042-1048	0.4	1
11	Robust Subthreshold CMOS Digital Circuit Design with On-Chip Adaptive Supply Voltage Scaling Technique. <i>IEICE Transactions on Electronics</i> , 2011 , E94-C, 80-88	0.4	1

10	Super-resolution technique for thermography with dual-camera system 2010 ,	3
9	A CMOS bandgap and sub-bandgap voltage reference circuits for nanowatt power LSIs 2010 ,	23
8	Nano-ampere CMOS current reference with little temperature dependence using small offset voltage 2010 ,	6
7	A nano-ampere current reference circuit and its temperature dependence control by using temperature characteristics of carrier mobilities 2010 ,	54
6	An Error Diagnosis Technique Based on Clustering of Elements. <i>IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences</i> , 2010 , E93-A, 2490-2496	0.4
5	Switching-voltage detection and compensation circuits for ultra-low-voltage CMOS inverters 2009 ,	1
4	Delay-compensation techniques for ultra-low-power subthreshold CMOS digital LSIs 2009 ,	3
3	An Error Diagnosis Technique Based on Location Sets to Rectify Subcircuits. <i>IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences</i> , 2009 , E92-A, 3136-3142	0.4
2	Layered blind deconvolution with interband prediction. <i>Systems and Computers in Japan</i> , 2000 , 31, 77-83	1
1	Haar wavelet transform with interband prediction and its application to image coding. <i>Electronics and Communications in Japan, Part III: Fundamental Electronic Science (English Translation of Denshi Tsushin Gakkai Ronbunshi)</i> , 1995 , 78, 103-114	0