Nobutaka Kuroki

List of Publications by Citations

63 papers	540 citations	9 h-index	21 g-index
64 ext. papers	710 ext. citations	1 avg, IF	3.68 L-index

#	Paper	IF	Citations
63	1.2-V Supply, 100-nW, 1.09-V Bandgap and 0.7-V Supply, 52.5-nW, 0.55-V Subbandgap Reference Circuits for Nanowatt CMOS LSIs. *IEEE Journal of Solid-State Circuits*, **2013**, 48, 1530-1538	5.5	143
62	A Low-Power Level Shifter With Logic Error Correction for Extremely Low-Voltage Digital CMOS LSIs. *IEEE Journal of Solid-State Circuits*, **2012**, 47, 1776-1783	5.5	67
61	A nano-ampere current reference circuit and its temperature dependence control by using temperature characteristics of carrier mobilities **2010**,		54
60	Fully-Integrated High-Conversion-Ratio Dual-Output Voltage Boost Converter With MPPT for Low-Voltage Energy Harvesting. *IEEE Journal of Solid-State Circuits*, **2016**, 51, 2398-2407	5.5	46
59	A CMOS bandgap and sub-bandgap voltage reference circuits for nanowatt power LSIs **2010**,		23
58	An 80-mV-to-1.8-V Conversion-Range Low-Energy Level Shifter for Extremely Low-Voltage VLSIs. *IEEE Transactions on Circuits and Systems I: Regular Papers*, **2017**, 64, 2026-2035	3.9	20
57	A 0.19-V minimum input low energy level shifter for extremely low-voltage VLSIs **2015**,		15
56	A 0.21-V minimum input, 73.6% maximum efficiency, fully integrated voltage boost converter with MPPT for low-voltage energy harvesters **2014**,		15
55	A 32.55-kHz, 472-nW, 120ppm/℃, fully on-chip, variation tolerant CMOS relaxation oscillator for a real-time clock application **2013**,		15
54	A 0.1□0.6 V input range voltage boost converter with low-leakage driver for low-voltage energy harvesting **2017**,		8
53	A nano-watt power CMOS amplifier with adaptive biasing for power-aware analog LSIs **2012**,		8
52	A Highly Efficient Switched-Capacitor Voltage Boost Converter with Nano-Watt MPPT Controller for Low-Voltage Energy Harvesting. *IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences*, **2016**, E99.A, 2491-2499	0.4	8
51	Multi-Channel Convolutional Neural Networks for Image Super-Resolution. *IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences*, **2017**, E100.A, 572-580	0.4	6
50	A 18.9-nA standby current comparator with adaptive bias current generator **2011**,		6
49	Nano-ampere CMOS current reference with little temperature dependence using small offset voltage **2010**,		6
48	A low-power single-slope analog-to-digital converter with digital PVT calibration **2012**,		6
47	A 0.21-V minimum input, 73.6% maximum efficiency, fully integrated 3-terminal voltage converter with MPPT for low-voltage energy harvesters **2015**,		5

46	A fully-integrated, high-conversion-ratio and dual-output voltage boost converter with MPPT for low-voltage energy harvesting **2015**,		5
45	A 6.66-kHz, 940-nW, 56ppm/°C, fully on-chip PVT variation tolerant CMOS relaxation oscillator **2012**,		5
44	A level shifter circuit design by using input/output voltage monitoring technique for ultra-low voltage digital CMOS LSIs **2011**,		5
43	A 0.38-μW stand-by power, 50-nA-to-1-mA load current range DC-DC converter with self-biased linear regulator for ultra-low power battery management **2016**,		5
42	Image super-resolution with multi-channel convolutional neural networks **2016**,		4
41	A level shifter with logic error correction circuit for extremely low-voltage digital CMOS LSIs **2011**,		4
40	Detecting Doctored JPEG Image Based on Block Noise Analysis and Double JPEG Analysis. *IEEJ Transactions on Electronics, Information and Systems*, **2017**, 137, 742-749	0.1	4
39	Ultralow-quiescent-current and wide-load-range low-dropout linear regulator with self-biasing technique for micropower battery management. *Japanese Journal of Applied Physics*, **2017**, 56, 04CF11	1.4	3
38	Impedance matching in magnetic-coupling-resonance wireless power transfer for small implantable devices **2017**,		3
37	An ultra-low-power supercapacitor voltage monitoring system for low-voltage energy harvesting **2017**,		3
36	A wide input voltage range level shifter circuit for extremely low-voltage digital LSIs. *IEICE Electronics Express*, **2011**, 8, 890-896	0.5	3
35	Super-resolution technique for thermography with dual-camera system **2010**,		3
34	Delay-compensation techniques for ultra-low-power subthreshold CMOS digital LSIs **2009**,		3
33	Switched-Capacitor Voltage Buck Converter with Step-Down-Ratio and Clock-Frequency Controllers for Ultra-Low-Power IoT Devices **2018**,		3
32	Analytical Study of Multi-stage Switched-Capacitor Voltage Boost Converter for Ultra-low Voltage Energy Harvesting **2018**,		3
31	A 24-transistor static flip-flop consisting of nors and inverters for low-power digital vlsis **2014**,		2
30	Energy-efficient AES SubBytes transformation circuit using asynchronous circuits for ultra-low voltage operation. *IEICE Electronics Express*, **2015**, 12, 20141157-20141157	0.5	2
29	A 95-nA, 523ppm/°C, 0.6-μW CMOS current reference circuit with subthreshold MOS resistor ladder **2011**,		2

28	High current efficiency sense amplifier using body-bias control for ultra-low-voltage SRAM **2011**,		2
27	An Energy-Efficient 24T Flip-Flop Consisting of Standard CMOS Gates for Ultra-Low Power Digital VLSIs. *IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences*, **2015**, E98.A, 2600-2606	0.4	2
26	Locally Weighted Averaging for Denoising of Medical Tomographic Images. *Journal of Signal Processing*, **2016**, 20, 217-220	0.2	2
25	Detecting Doctored Region in JPEG Image using Convolutional Neural Networks. *IEEJ Transactions on Electronics, Information and Systems*, **2018**, 138, 1417-1424	0.1	2
24	Sub-0.1V Input, Low-Voltage CMOS Driver Circuit for Multi-Stage Switched Capacitor Voltage Boost Converter **2019**,		2
23	Combination of Convolutional Neural Network Architecture and its Learning Method for Rotation-Invariant Handwritten Digit Recognition. *IEEJ Transactions on Electrical and Electronic Engineering*, **2021**, 16, 161-163	1	2
22	An Area-Efficient Resistor-less On-Chip Frequency Reference for Ultra-Low Power Real-Time Clock Application. *IEEJ Transactions on Electrical and Electronic Engineering*, **2018**, 13, 1633-1641	1	2
21	Multi-Category Image Super-Resolution with Convolutional Neural Network and Multi-Task Learning. *IEICE Transactions on Information and Systems*, **2021**, E104.D, 183-193	0.6	2
20	A 105-nW CMOS thermal sensor for power-aware applications **2011**,		1
19	Switching-voltage detection and compensation circuits for ultra-low-voltage CMOS inverters **2009**,		1
18	Layered blind deconvolution with interband prediction. *Systems and Computers in Japan*, **2000**, 31, 77-83		1
17	Subthreshold SRAM with Write Assist Technique Using On-Chip Threshold Voltage Monitoring Circuit. *IEICE Transactions on Electronics*, **2011**, E94-C, 1042-1048	0.4	1
16	Super-Resolution with Multi-Path Convolutional Neural Networks. *IEEJ Transactions on Electronics, Information and Systems*, **2020**, 140, 638-650	0.1	1
15	Improvement of Luminance Isotropy for Convolutional Neural Networks-Based Image Super-Resolution. *IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences*, **2020**, E103.A, 955-958	0.4	1
14	Robust Subthreshold CMOS Digital Circuit Design with On-Chip Adaptive Supply Voltage Scaling Technique. *IEICE Transactions on Electronics*, **2011**, E94-C, 80-88	0.4	1
13	Detecting tampered regions in JPEG images via CNN **2020**,		1
12	A 34-mV Startup Ring Oscillator Using Stacked Body Bias Inverters for Extremely Low-Voltage Thermoelectric Energy Harvesting **2020**,		1
11	An ultra-low power active diode using a hysteresis common gate comparator for low-voltage and low-power energy harvesting systems **2018**,		1

10	Automated Fish Bone Detection in X-Ray Images with Convolutional Neural Network and Synthetic Image Generation. *IEEJ Transactions on Electrical and Electronic Engineering*, **2021**, 16, 1510	1	1
9	Haar wavelet transform with interband prediction and its application to image coding. *Electronics and Communications in Japan, Part III: Fundamental Electronic Science (English Translation of Denshi Tsushin Gakkai Ronbunshi)*, **1995**, 78, 103-114		0
8	A Sub-1-μs Start-Up Time, Fully-Integrated 32-MHz Relaxation Oscillator for Low-Power Intermittent Systems. *IEICE Transactions on Electronics*, **2018**, E101.C, 161-169	0.4	0
7	An 11.8 nA ultra-low power active diode using a hysteresis common gate comparator for low-power energy harvesting systems. *IEICE Electronics Express*, **2020**, 17, 20200103-20200103	0.5	0
6	A 35-mV supply ring oscillator consisting of stacked body bias inverters for extremely low-voltage LSIs. *IEICE Electronics Express*, **2021**, 18, 20210065-20210065	0.5	0
5	Detecting Tampered Region in video using LSTM and U-Net. *IEEJ Transactions on Electronics, Information and Systems*, **2020**, 140, 476-483	0.1	
4	Super-Resolution with Horizontal and Vertical Convolutional Neural Networks. *IEEJ Transactions on Electronics, Information and Systems*, **2018**, 138, 957-963	0.1	
3	Detecting tampered region in video using LSTM and U-Net. *Electronics and Communications in Japan* , **2020**, 103, 15-25	0.4	
2	An Error Diagnosis Technique Based on Location Sets to Rectify Subcircuits. *IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences*, **2009**, E92-A, 3136-3142	0.4	
1	An Error Diagnosis Technique Based on Clustering of Elements. *IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences*, **2010**, E93-A, 2490-2496	0.4	