

Xiaoxiao Liu

List of Publications by Year in descending order

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Version: 2024-02-01

13
papers

237
citations

2258059

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h-index

2550090

3
g-index

13
all docs

13
docs citations

13
times ranked

380
citing authors

#	ARTICLE	IF	CITATIONS
1	Exploring Applications of STT-RAM in GPU Architectures. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 238-249.	5.4	5
2	Neuromorphic computing's yesterday, today, and tomorrow – an evolutionary view. The Integration VLSI Journal, 2018, 61, 49-61.	2.1	25
3	TEMP. , 2016, , .		8
4	Heterogeneous systems with reconfigurable neuromorphic computing accelerators. , 2016, , .		6
5	Harmonica: A Framework of Heterogeneous Computing Systems With Memristor-Based Neuromorphic Computing Accelerators. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 617-628.	5.4	49
6	Hardware acceleration for neuromorphic computing: An evolving view. , 2015, , .		0
7	RENO. , 2015, , .		111
8	The applications of memristor devices in next-generation cortical processor designs. , 2015, , .		5
9	An efficient STT-RAM-based register file in GPU architectures. , 2015, , .		19
10	Emerging memristor technology enabled next generation cortical processor. , 2014, , .		2
11	A heterogeneous computing system with memristor-based neuromorphic accelerators. , 2014, , .		0
12	Neuromorphic hardware acceleration enabled by emerging technologies (Invited paper). , 2014, , .		0
13	STD-TLB: A STT-RAM-based dynamically-configurable translation lookaside buffer for GPU architectures. , 2014, , .		7