Xiaoxiao Liu

List of Publications by Year
in descending order

13	237	2258059	2550090
papers	citations	3	3
		h-index	g-index

13	13	13	380
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	RENO. , 2015, , .		111
2	Harmonica: A Framework of Heterogeneous Computing Systems With Memristor-Based Neuromorphic Computing Accelerators. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 617-628.	5.4	49
3	Neuromorphic computing's yesterday, today, and tomorrow â€" an evolutionary view. The Integration VLSI Journal, 2018, 61, 49-61.	2.1	25
4	An efficient STT-RAM-based register file in GPU architectures. , 2015, , .		19
5	TEMP. , 2016, , .		8
6	STD-TLB: A STT-RAM-based dynamically-configurable translation lookaside buffer for GPU architectures. , 2014, , .		7
7	Heterogeneous systems with reconfigurable neuromorphic computing accelerators. , 2016, , .		6
8	The applications of memristor devices in next-generation cortical processor designs. , 2015, , .		5
9	Exploring Applications of STT-RAM in GPU Architectures. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 238-249.	5.4	5
10	Emerging memristor technology enabled next generation cortical processor. , 2014, , .		2
11	A heterogeneous computing system with memristor-based neuromorphic accelerators. , 2014, , .		0
12	Neuromorphic hardware acceleration enabled by emerging technologies (Invited paper). , 2014, , .		0
13	Hardware acceleration for neuromorphic computing: An evolving view. , 2015, , .		0