

Carlos SÃ¡nchez-Azqueta

List of Publications by Year in descending order

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100
papers

564
citations

840585

11
h-index

794469

19
g-index

100
all docs

100
docs citations

100
times ranked

431
citing authors

#	ARTICLE	IF	CITATIONS
1	Programmable delay lines on different LUT implementations for CRO-PUF. , 2022, , .		1
2	FPGA Implementation of a New PUF Based on Galois Ring Oscillators. , 2021, , .		5
3	Digitization Algorithms in Ring Oscillator Physically Unclonable Functions as a Main Factor Achieving Hardware Security. IEEE Access, 2021, 9, 147343-147356.	2.6	5
4	High-Sensitivity Large-Area Photodiode Read-Out Using a Divide-and-Conquer Technique. Sensors, 2020, 20, 6316.	2.1	1
5	Model-based teaching of physics in higher education: a review of educational strategies and cognitive improvements. Journal of Applied Research in Higher Education, 2020, 13, 33-47.	1.1	4
6	Security Analysis of a New FPE Stream Cipher. , 2020, , .		0
7	Proposal and Analysis of a Novel Class of PUFs Based on Galois Ring Oscillators. IEEE Access, 2020, 8, 157830-157839.	2.6	11
8	Introduction to Physically Unclonable Functions: Properties and Applications. , 2020, , .		10
9	A New Approach to Analysis the Security of Compensated Measuring PUFs. , 2020, , .		0
10	Self-Synchronized Encryption for Physical Layer in Gigabit Ethernet Optical Links. IEEE Access, 2020, 8, 89727-89740.	2.6	1
11	A New Method for Format Preserving Encryption in High-Data Rate Communications. IEEE Access, 2020, 8, 21003-21016.	2.6	5
12	Quick response codes as a complement for the teaching of Electronics in laboratory activities. International Journal of Electrical Engineering and Education, 2020, , 002072092091643.	0.4	0
13	Enhanced Molecular Spin-Photon Coupling at Superconducting Nanoconstrictions. ACS Nano, 2020, 14, 8707-8715.	7.3	37
14	Physical Layer Encryption for Industrial Ethernet in Gigabit Optical Links. IEEE Transactions on Industrial Electronics, 2019, 66, 3287-3295.	5.2	14
15	Methodology for Performance Optimization in Noise- and Distortion-Canceling LNA. , 2019, , .		4
16	A 1 Gbps Chaos-Based Stream Cipher Implemented in 0.18 μ m CMOS Technology. Electronics (Switzerland), 2019, 8, 623.	1.8	6
17	A New Lightweight CSPRNG Implemented in a 0.18 μ m CMOS Technology. , 2019, , .		2
18	Self-Synchronized Encryption Using an FPE Block Cipher for Gigabit Ethernet. , 2019, , .		1

#	ARTICLE	IF	CITATIONS
19	A Highly Linear Low-Noise Transimpedance Amplifier for Indoor Fiber-Wireless Remote Antenna Units. Electronics (Switzerland), 2019, 8, 437.	1.8	12
20	ICT-Based Didactic Strategies to Build Knowledge Models in Electronics in Higher Education. , 2019, , .		1
21	Chaotic Encryption Applied to Optical Ethernet in Industrial Control Systems. IEEE Transactions on Instrumentation and Measurement, 2019, 68, 4876-4886.	2.4	13
22	A Reconfigurable Radio-Frequency Converter IC in 0.18 Åµm CMOS. Electronics (Switzerland), 2019, 8, 1146.	1.8	1
23	Self-Synchronized Encryption for Physical Layer in 10Gbps Optical Links. IEEE Transactions on Computers, 2019, 68, 899-911.	2.4	4
24	Chaotic Encryption for 10-Gb Ethernet Optical Links. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 859-868.	3.5	12
25	Chaos-Based Bitwise Dynamical Pseudorandom Number Generator On FPGA. IEEE Transactions on Instrumentation and Measurement, 2019, 68, 291-293.	2.4	76
26	USING TWITTER TO PROMOTE THE TEACHING-LEARNING OF SCIENTIFIC DISCIPLINES. , 2019, , .		0
27	OPEN EDUCATIONAL RESOURCES TO IMPLEMENT AN ONLINE TUTORING. , 2019, , .		0
28	Fully-differential transimpedance amplifier for reliable wireless communications. Microelectronics Reliability, 2018, 83, 25-28.	0.9	2
29	A New Technique For Improving the Security of Chaos Based Cryptosystems. , 2018, , .		5
30	A new simple technique for improving the random properties of chaos-based cryptosystems. AIP Advances, 2018, 8, 035004.	0.6	4
31	A Robust 10-Gb/s Duobinary Transceiver in 0.13-Î¼m SOI CMOS for Short-Haul Optical Networks. IEEE Transactions on Industrial Electronics, 2018, 65, 1518-1525.	5.2	2
32	Analysis of the Influence of Component Mismatch on Integrated Passive Polyphase Filters. , 2018, , .		0
33	Highly-linear transimpedance amplifier for remote antenna units. , 2018, , .		3
34	Chaos-based stream cipher for gigabit ethernet. , 2018, , .		0
35	A new randomness-enhancement method for chaos-based cryptosystem. , 2018, , .		3
36	Using a Chaotic Cipher to Encrypt Ethernet Traffic. , 2018, , .		5

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37	Using hyperdata in a laboratory of electronics QR codes applied to experimental learning. , 2018, , .		5
38	Low-EVM CMOS Transimpedance Amplifier for Intermediate Frequency over Fiber. , 2018, , .		3
39	Analysis of mismatch impact on image rejection ratio for passive polyphase filters. International Journal of Circuit Theory and Applications, 2018, 46, 1838-1847.	1.3	1
40	Design of a CMOS multi-rate adaptive continuous-time equalizer based on power spectrum estimation. International Journal of Circuit Theory and Applications, 2017, 45, 2226-2242.	1.3	1
41	3.125 Gbit/s CMOS transceiver for duobinary modulation over 50-µm Si-POF channels. Electronics Letters, 2017, 53, 258-260.	0.5	2
42	MEMS-based seed generator applied to a chaotic stream cipher. , 2017, , .		1
43	Multi-Rate Adaptive Equalizer for Transmission Over Up to 50-m SI-POF. IEEE Photonics Technology Letters, 2017, 29, 587-590.	1.3	7
44	Latch-Type Optical Receiver With Integrated <i>pin</i> Photodiodes. IEEE Photonics Technology Letters, 2017, 29, 675-678.	1.3	0
45	Continuous-time equalizer for CMOS integrated photodiodes. , 2017, , .		1
46	Fast and secure chaotic stream cipher with a MEMS-based seed generator. , 2017, , .		8
47	Transimpedance amplifier with programmable gain and bandwidth for capacitive MEMS accelerometers. , 2017, , .		4
48	Programmable differential capacitance-to-voltage converter for MEMS accelerometers. Proceedings of SPIE, 2017, , .	0.8	1
49	An Adaptive Bitrate Clock and Data Recovery Circuit for Communication Signal Analyzers. IEEE Transactions on Instrumentation and Measurement, 2017, 66, 191-193.	2.4	6
50	A methodology to design continuous-time adaptive equalizers. International Journal of Circuit Theory and Applications, 2017, 45, 1203-1217.	1.3	1
51	Design of a low-power quadrature LC-VCO in 65 nm CMOS. , 2017, , .		5
52	Impact of non-idealities on passive polyphase filter performance. , 2017, , .		3
53	A new multiple ciphering scheme for improving randomness. , 2017, , .		1
54	Programmable Low-Power Low-Noise Capacitance to Voltage Converter for MEMS Accelerometers. Sensors, 2017, 17, 67.	2.1	7

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55	Application of a MEMS-Based TRNG in a Chaotic Stream Cipher. <i>Sensors</i> , 2017, 17, 646.	2.1	24
56	Equalizing Si photodetectors fabricated in standard CMOS processes. <i>Proceedings of SPIE</i> , 2017, , .	0.8	0
57	Synchronous OEIC Integrating Receiver for Optically Reconfigurable Gate Arrays. <i>Sensors</i> , 2016, 16, 761.	2.1	2
58	Synchronous OEIC Integrating Receiver for ORGA Applications. <i>Procedia Engineering</i> , 2016, 168, 1291-1295.	1.2	0
59	Sensor-Based Seeds for a Chaotic Stream Cipher. <i>Procedia Engineering</i> , 2016, 168, 1663-1666.	1.2	1
60	Multi-rate clock and data recovery circuit for short-reach optical links. , 2016, , .		0
61	High-resolution wide-band LC-VCO for reliable operation in phase-locked loops. <i>Microelectronics Reliability</i> , 2016, 63, 251-255.	0.9	7
62	Precoder and decoder for duobinary modulation over equalized 50-m SI-POF. , 2016, , .		1
63	Lightweight ciphers based on chaotic Map - LFSR architectures. , 2016, , .		4
64	Secure communication system based on a logistic map and a linear feedback shift register. , 2016, , .		8
65	CMOS transimpedance amplifier with controllable gain for RF overlay. , 2016, , .		15
66	Using the Wiimote to Learn MEMS in a Physics Degree Program. <i>IEEE Transactions on Education</i> , 2016, 59, 169-174.	2.0	5
67	A phaseâ€space model to describe bangâ€bang phase detectors. <i>International Journal of Circuit Theory and Applications</i> , 2015, 43, 829-839.	1.3	1
68	A new equalizer for 2 Gb/s short-reach SI-POF links. , 2015, , .		0
69	A multi-rate continuous-time adaptive equalizer for high-speed serial links. <i>Proceedings of SPIE</i> , 2015, , .	0.8	1
70	1-V continuous-time linear equalizer for up to 2 Gb/s over 50-m SI-POF. , 2015, , .		1
71	A 2.5-Gb/s multi-rate continuous-time adaptive equalizer for short reach optical links. , 2015, , .		5
72	CMOS front-end for duobinary data over 50-m SI-POF links. <i>Proceedings of SPIE</i> , 2015, , .	0.8	0

#	ARTICLE	IF	CITATIONS
73	A CMOS merged CDR and continuous-time adaptive equalizer. Proceedings of SPIE, 2015, , .	0.8	0
74	Single-Chip Receiver for 1.25 Gb/s Over 50-m SI-POF. IEEE Photonics Technology Letters, 2015, 27, 1220-1223.	1.3	4
75	A 1.7-GHz wide-band CMOS LC-VCO with 7-Bit coarse control. , 2015, , .		9
76	Continuous-Time Linear Equalizer for Multigigabit Transmission Through SI-POF in Factory Area Networks. IEEE Transactions on Industrial Electronics, 2015, 62, 6530-6532.	5.2	8
77	1- μ V continuous-time equalizers for multi-Gigabit short-haul optical fiber communications. International Journal of Circuit Theory and Applications, 2014, 42, 146-164.	1.3	6
78	Design considerations for loop filters in continuous-time adaptive equalizers. , 2014, , .		1
79	Nanoscale constrictions in superconducting coplanar waveguide resonators. Applied Physics Letters, 2014, 105, .	1.5	31
80	A 1-V CMOS double loop continuous-time adaptive equalizer for short-haul optical networks. , 2014, , .		1
81	A double loop continuous-time adaptive equalizer. , 2014, , .		0
82	Power comparator for continuous-time adaptive equalization in Ethernet-based instrumentation. Measurement Science and Technology, 2014, 25, 087002.	1.4	2
83	Applets for Physical Electronics learning. , 2014, , .		1
84	MEMS: From the classroom to the Wii. , 2014, , .		0
85	A Low-Power CMOS Receiver for 1.25 Gb/s Over 1- mm SI-POF Links. IEEE Transactions on Industrial Electronics, 2014, 61, 4246-4254.	5.2	11
86	Bang-bang phase detector model revisited. , 2013, , .		2
87	CMOS receiver with equalizer and CDR for short-reach optical communications. , 2013, , .		2
88	Design criteria for loop filters in spectrum balancing technique-based adaptive equalisers. , 2013, , .		4
89	A CMOS quasi-digital temperature sensor for battery operated systems. , 2013, , .		5
90	A 1-V 1.25-Gbps CMOS analog front-end for short reach optical links. , 2013, , .		8

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91	New Multilevel Bang-Bang Phase Detector. IEEE Transactions on Instrumentation and Measurement, 2013, 62, 3384-3386.	2.4	11
92	A comparative study of continuous-time analog adaptive equalizers. , 2013, , .		1
93	Gigabit Receiver Over 1 mm SI-POF For Home Area Networks. Journal of Lightwave Technology, 2012, 30, 2668-2674.	2.7	14
94	A multi-level phase detector in 90 nm CMOS. , 2012, , .		0
95	A CMOS continuous-time equalizer for short-reach optical communications. , 2011, , .		7
96	A phase detection scheme for clock and data recovery applications. , 2011, , .		8
97	A 3.125 GHz four stage voltage controlled ring oscillator in 0.18 CMOS. , 2011, , .		4
98	A 0.18 μ m CMOS ring VCO for clock and data recovery applications. Microelectronics Reliability, 2011, 51, 2351-2356.	0.9	37
99	Optimization of YIG/Bi stacks for spin-to-charge conversion and influence of aging. Journal Physics D: Applied Physics, 0, , .	1.3	5
100	Projects to encourage female students in STEM areas. , 0, , .		0