Dominik Macko

List of Publications by Year
in descending order

39	157	1478505	1588992
papers	citations	6	8
		h-index	g-index

39	39	39	70
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	Upper Confidence Bound Based Communication Parameters Selection to Improve Scalability of LoRa@FIIT Communication. IEEE Sensors Journal, 2022, 22, 12415-12427.	4.7	2
2	Simplified introduction of power intent into a register-transfer level model. Design Automation for Embedded Systems, 2021, 25, 297-324.	1.0	1
3	Survey: Classification of the IoT Technologies for Better Selection to Real Use. , 2020, , .		4
4	Automation of Dynamic Power Management in FPGA-Based Energy-Constrained Systems. IEEE Access, 2020, 8, 165894-165903.	4.2	7
5	PESL: System-Level Estimation of Power-Management Effect on Dynamic Energy Consumption. Electronics (Switzerland), 2020, 9, 1313.	3.1	2
6	Optimization of LoRa Devices Communication for Applications in Healthcare. , 2020, , .		7
7	HIP-Based Security in IoT Networks: A comparison. , 2020, , .		1
8	Web-Application Based System for Automated Testing of Network-Devices Configurations. , 2020, , .		0
9	Optimizing Energy Efficiency of Secured IoT Communication by OpenHip. , 2019, , .		2
10	Automated Integration of Dynamic Power Management into FPGA-Based Design. , 2019, , .		2
11	Increasing Energy Efficiency by Minimizing Collisions in Long-Range IoT Networks. , 2019, , .		8
12	A New Planning-Based Collision-Prevention Mechanism in Long-Range IoT Networks. IEEE Internet of Things Journal, 2019, 6, 9439-9446.	8.7	13
13	EEMIP: Energy-Efficient Communication Using Timing Channels and Prioritization in ZigBee. Sensors, 2019, 19, 2246.	3.8	7
14	Increasing Bluetooth Low Energy communication efficiency by presetting protocol parameters. Turkish Journal of Electrical Engineering and Computer Sciences, 2019, 27, 3486-3499.	1.4	1
15	E-HIP: An Energy-Efficient OpenHIP-Based Security in Internet of Things Networks. Sensors, 2019, 19, 4921.	3.8	8
16	Adopting High-level Synthesis Approach to Accelerate Power Management Design. , 2018, , .		1
17	Exploration of the LoRa Technology Utilization Possibilities in Healthcare IoT Devices. , 2018, , .		9
18	Network Tester: A Generation and Evaluation of Diagnostic Communication in IP Networks. , 2018, , .		4

#	ARTICLE	IF	CITATIONS
19	Automated Evaluation of a Network Device Configuration. , 2018, , .		1
20	Simplifying low-power SoC top-down design using the system-level abstraction and the increased automation. The Integration VLSI Journal, 2018, 63, 101-114.	2.1	8
21	Contribution to Automated Generating of System Power-Management Specification. , 2018, , .		2
22	Verification of Power-Management Specification at Early Stages of Power-Constrained Systems Design. Journal of Circuits, Systems and Computers, 2017, 26, 1740002.	1.5	7
23	Rapid Estimation of Power-Management Unit Overhead from System-Level Specification. , 2017, , .		0
24	PMS2UPF: An automated transition from ESL to RTL power-intent specification. , 2017, , .		3
25	Rapid power-management exploration using post-processing of the system-level simulation results. , 2017, , .		1
26	IP networks diagnostic communication generator. , 2017, , .		2
27	A New Automated Power-Estimation Method for SystemC Hardware Design. Journal of Low Power Electronics, 2017, 13, 545-550.	0.6	1
28	Early-stage verification of power-management specification in low-power systems design. , 2016, , .		4
29	Adoption of abstract power-management specification to FPGA-based design. , 2016, , .		2
30	Automated formal verification of the refined specification of digital systems in HSSL. , 2016, , .		0
31	PMHLS 2.0: An automated optimization of power management during high-level synthesis. , 2016, , .		1
32	Power-Management Specification in SystemC. , 2015, , .		11
33	Power-management high-level synthesis. , 2015, , .		9
34	Power-efficient power-management logic. , 2014, , .		2
35	Self-managing power management unit. , 2014, , .		4
36	Managing digital-system power at the system level. , 2013, , .		11

#	Article	IF	Citations
37	VHDLVisualizer: HDL model visualization with simulation-based verification. , 2012, , .		4
38	Logic circuit design verification support tool - Fit Board. Procedia, Social and Behavioral Sciences, 2011, 28, 305-310.	0.5	2
39	VHDL structural model visualization. , 2011, , .		3