

# Paula LÃ³pez MartÃ­nez

## List of Publications by Year in descending order

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68  
papers

404  
citations

1039406

9  
h-index

887659

17  
g-index

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all docs

68  
docs citations

68  
times ranked

363  
citing authors

#	ARTICLE	IF	CITATIONS
1	Deep Learning-Based Multiple Object Visual Tracking on Embedded System for IoT and Mobile Edge Computing Applications. IEEE Internet of Things Journal, 2019, 6, 5423-5431.	5.5	70
2	Distance Measurement Error in Time-of-Flight Sensors Due to Shot Noise. Sensors, 2015, 15, 4624-4642.	2.1	28
3	Improved thermal analysis of buried landmines. IEEE Transactions on Geoscience and Remote Sensing, 2004, 42, 1965-1975.	2.7	26
4	Micro-Energy Harvesting System Including a PMU and a Solar Cell on the Same Substrate With Cold Startup From 2.38 nW and Input Power Range up to 10 $\mu$ W Using Continuous MPPT. IEEE Transactions on Power Electronics, 2019, 34, 5105-5116.	5.4	25
5	On-Chip Solar Energy Harvester and PMU With Cold Start-Up and Regulated Output Voltage for Biomedical Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 1103-1114.	3.5	21
6	Performance analysis of high-speed MOS transistors with different layout styles. , 0, , .		14
7	Dynamic Model of Switched-Capacitor DC-DC Converters in the Slow-Switching Limit Including Charge Reusing. IEEE Transactions on Power Electronics, 2017, 32, 5293-5311.	5.4	14
8	Thermal infrared identification of buried landmines. , 2005, 5794, 198.		12
9	A Verilog-AMS photodiode model including lateral effects. Microelectronics Journal, 2012, 43, 980-984.	1.1	12
10	A Review of CMOS Photodiode Modeling and the Role of the Lateral Photoresponse. IEEE Transactions on Electron Devices, 2016, 63, 16-25.	1.6	12
11	0.6-V- $I_N$ 7.0-nA- $I_Q$ 0.75-mA- $I_L$ CMOS Capacitor-Less LDO for Low-Voltage Micro-Energy-Harvested Supplies. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 599-608.	3.5	11
12	Analytical modelling of size effects on the lateral photoresponse of CMOS photodiodes. Solid-State Electronics, 2012, 73, 15-20.	0.8	9
13	CNN-based 3D thermal modeling of the soil for antipersonnel mine detection. , 0, , .		8
14	Closed-Form and Explicit Analytical Model for Crosstalk in CMOS Photodiodes. IEEE Transactions on Electron Devices, 2013, 60, 3459-3464.	1.6	8
15	Wireless sensor mote for snail pest detection. , 2014, , .		8
16	An 11 mA Capacitor-Less LDO With 3.08 nA Quiescent Current and SSF-Based Adaptive Biasing. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 844-848.	2.2	8
17	Design and training of multilayer discrete time cellular neural networks for antipersonnel mine detection using genetic algorithms. , 0, , .		7
18	Dynamic joint model of capacitive charge pumps and on-chip photovoltaic cells for CMOS micro-energy harvesting. International Journal of Circuit Theory and Applications, 2016, 44, 1874-1894.	1.3	6

#	ARTICLE	IF	CITATIONS
19	Inâ€pixel analog memories for a pixelâ€based background subtraction algorithm on CMOS vision sensors. International Journal of Circuit Theory and Applications, 2018, 46, 1631-1647.	1.3	6
20	Robustness oriented design tool for multilayer DTCNN applications. International Journal of Circuit Theory and Applications, 2002, 30, 195-210.	1.3	5
21	Improved Analytical I-V model for polygonal-shape enclosed layout transistors. , 2007, , .		5
22	Experimental characterization of peripheral photocurrent in CMOS photodiodes down to 65 nm technology. Semiconductor Science and Technology, 2013, 28, 045011.	1.0	5
23	Analytical Model for Crosstalk in p-n<sub>well</sub> Photodiodes. IEEE Transactions on Electron Devices, 2015, 62, 580-586.	1.6	5
24	Detection of perturbations in thermal IR signatures: an inverse problem for buried land mine detection. , 2003, , .		4
25	Bottom collection of photodiode-based CMOS APS. , 2008, , .		4
26	Efficient softwareâ€hardware 3D heat equation solver with applications on the non-destructive evaluation of minefields. Computers and Geosciences, 2009, 35, 2239-2249.	2.0	4
27	Ultralow power voltage reference circuit for implantable devices in standard CMOS technology. International Journal of Circuit Theory and Applications, 2019, 47, 991-1005.	1.3	4
28	Soft-Hard 3D FD-TD Solver for Non Destructive Evaluation. , 2007, , .		3
29	A 2D model for radiation-hard CMOS annular transistors. Semiconductor Science and Technology, 2009, 24, 125009.	1.0	3
30	FPGA computation of the 3D heat equation. Computational Geosciences, 2010, 14, 649-664.	1.2	3
31	The dickson charge pump as voltage booster for light energy harvesting on CMOS vision chips. , 2014, , .		3
32	Simplification and hardware implementation of the feature descriptor vector calculation in the SIFT algorithm. , 2014, , .		3
33	Dark current in standard CMOS pinned photodiodes for Time-of-Flight sensors. , 2014, , .		3
34	Effect of temporal and spatial noise on the performance of hardware oriented background extraction algorithms. , 2017, , .		3
35	A CMOS Vision Sensor for Background Subtraction. , 2020, , .		3
36	1.88 nA Quiescent Current Capacitor-Less LDO with Adaptive Biasing Based on a SSF Absolute Voltage Difference Meter. , 2020, , .		3

#	ARTICLE	IF	CITATIONS
37	Antipersonnel mine detection on infrared images. , 0, , .		2
38	Proposal for analog synthesis of a land mine detection system. , 2003, 5046, 253.		2
39	FPGA Implementation of 3-D Thermal Model Simulator. , 2006, , .		2
40	FPGA-based hardware accelerator of the heat equation with applications on infrared thermography. , 2008, , .		2
41	DT-CNN emulator: 3D heat equation solver with applications on the non-destructive soil inspection. , 2008, , .		2
42	Non-destructive soil inspection using an efficient 3D softwareâ€™ hardware heat equation solver. Inverse Problems in Science and Engineering, 2009, 17, 755-775.	1.2	2
43	A dc $I-V$ model for short-channel polygonal enclosed layout transistors. International Journal of Circuit Theory and Applications, 2009, 37, 163-177.	1.3	2
44	Evidence of the lateral collection significance in small CMOS photodiodes. , 2012, , .		2
45	Four-transistor pinned photodiodes in standard CMOS technologies for time-of-flight sensors. Semiconductor Science and Technology, 2015, 30, 045002.	1.0	2
46	Capacitance-based wireless sensor mote for snail pest detection. , 2015, , .		2
47	Design for maximum power transfer efficiency of thermoelectric generators using mixed mode simulations. , 2016, , .		2
48	On-Chip Solar Cell and PMU on the Same Substrate with Cold Start-Up from nW and 80 dB of Input Power Range for Biomedical Applications. , 2019, , .		2
49	Genetic algorithm based training for multilayer discrete-time cellular neural networks. Lecture Notes in Computer Science, 1999, , 467-476.	1.0	1
50	FPGA finite difference time domain solver for thermal simulation. , 0, , .		1
51	A study of CMOS radiation tolerant transistors using green functions. , 2009, , .		1
52	Analytical model of short-channel gate enclosed transistors using Green functions. Solid-State Electronics, 2009, 53, 514-519.	0.8	1
53	Modeling and simulation of CMOS APS. , 2009, , .		1
54	Enclosed layout transistors in saturation. , 2009, , .		1

#	ARTICLE	IF	CITATIONS
55	Analytical model for p-n junctions under point source illumination. , 2010, , .		1
56	Modeling and experimental results of short channel annular MOS transistors. , 2011, , .		1
57	CMOS photodiode model and HDL implementation. , 2013, , .		1
58	Custom design of pinned photodiodes in standard CMOS technologies for time-of-flight sensors. , 2014, , .		1
59	Live demonstration: Wireless sensor network for snail pest detection. , 2016, , .		1
60	Study of the thermoelectric properties of non-typical semiconductor materials with conventional CAD tools. , 2016, , .		1
61	Practical considerations on doughnut transistors design. , 0, , .		0
62	Voltage boosters for on-chip solar cells on focal-plane processors. , 2013, , .		0
63	Dark current optimization of 4-transistor pixel topologies in standard CMOS technologies for time-of-flight sensors. , 2015, , .		0
64	Time-of-flight chip in standard CMOS technology with in-pixel adaptive number of accumulations. , 2016, , .		0
65	Dynamic model of on-chip inverting capacitive charge pumps with charge reusing. , 2016, , .		0
66	Pulsed time-of-flight pixel with on-chip 20klux background light suppression in standard CMOS technology. International Journal of Circuit Theory and Applications, 2018, 46, 987-1005.	1.3	0
67	Time-of-Flight Pixel with Homodyne Phase Demodulation in Standard CMOS Technology. , 2019, , .		0
68	Corrections to "Wireless Sensor Network With Perpetual Motes for Terrestrial Snail Activity Monitoring": IEEE Sensors Journal, 2019, 19, 6553-6553.	2.4	0