

# Ghobad Zarrinchian

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/6313976/publications.pdf>

Version: 2024-02-01

4  
papers

36  
citations

2682572

2  
h-index

2272923

4  
g-index

4  
all docs

4  
docs citations

4  
times ranked

37  
citing authors

#	ARTICLE	IF	CITATIONS
1	Latch-Based Structure: A High Resolution and Self-Reference Technique for Hardware Trojan Detection. IEEE Transactions on Computers, 2017, 66, 100-113.	3.4	27
2	Improving inter-node communications in multi-core clusters using a contention-free process mapping algorithm. Journal of Supercomputing, 2013, 66, 488-513.	3.6	6
3	Combinational Counters: A Low Overhead Approach to Address DPA Attacks. Journal of Circuits, Systems and Computers, 2020, 29, 2050097.	1.5	2
4	A New Process Placement Algorithm in Multi-core Clusters Aimed to Reducing Network Interface Contention. Advances in Intelligent Systems and Computing, 2012, , 1041-1050.	0.6	1