

Nguyen Anh Vu Doan

List of Publications by Year in descending order

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Version: 2024-02-01

17
papers

93
citations

1684188

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1372567

10
g-index

17
all docs

17
docs citations

17
times ranked

88
citing authors

#	ARTICLE	IF	CITATIONS
1	An alternative weight sensitivity analysis for PROMETHEE II rankings. Omega, 2018, 80, 166-174.	5.9	24
2	GenMap: A Genetic Algorithmic Approach for Optimizing Spatial Mapping of Coarse-Grained Reconfigurable Architectures. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 2383-2396.	3.1	23
3	A Lightweight Nonlinear Methodology to Accurately Model Multicore Processor Power. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 3152-3164.	2.7	12
4	Collaborative decision-making in sustainable mobility: identifying possible consensuses in the multi-actor multi-criteria analysis based on inverse mixed-integer linear optimization. International Journal of Sustainable Development and World Ecology, 2021, 28, 64-74.	5.9	12
5	MigSpIke: A Migration Based Algorithms and Architecture for Scalable Robust Neuromorphic Systems. IEEE Transactions on Emerging Topics in Computing, 2021, , 1-1.	4.6	10
6	Multi-objective Optimization for Application Mapping and Body Bias Control on a CGRA. , 2017, , .		2
7	A Hybrid NoC Enabling Fail-Operational and Hard Real-Time Communication in MPSoC. Lecture Notes in Computer Science, 2019, , 31-44.	1.3	2
8	Fine-Grained Power Modeling of Multicore Processors Using FFNNs. International Journal of Parallel Programming, 2022, 50, 243-266.	1.5	2
9	Multi-Objective Optimization of Channel Mapping for Fail-Operational Hybrid TDM NoCs. , 2019, , .		1
10	Region of Interest-Based Parameter Optimization for Approximate Image Processing on FPGAs. International Journal of Networking and Computing, 2021, 11, 438-462.	0.4	1
11	Network Coding in Networks-on-Chip with Lossy Links. Lecture Notes in Computer Science, 2019, , 308-321.	1.3	1
12	Model-based design space exploration for FPGA-based image processing applications employing parameterizable approximations. Microprocessors and Microsystems, 2021, , 104386.	2.8	1
13	Exploring a Hybrid Voting-based Eviction Policy for Caches and Sparse Directories on Manycore Architectures. Microprocessors and Microsystems, 2021, 87, 104384.	2.8	1
14	HyVE: A Hybrid Voting-based Eviction Policy for Caches. , 2020, , .		1
15	Multi-objective optimization and multi-criteria decision aid applied to the design of 3D-stacked integrated circuits. 4or, 2016, 14, 329-330.	1.6	0
16	Protection switching schemes and mapping strategies for fail-operational hard real-time NoCs. Microprocessors and Microsystems, 2021, 87, 104385.	2.8	0
17	Exploring Task and Channel Mapping Strategies in Fail-Operational and Hard Real-Time NoCs. , 2020, , .		0