

Jafar Sobhi

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/6276310/publications.pdf>

Version: 2024-02-01

44
papers

240
citations

1040056

9
h-index

1058476

14
g-index

44
all docs

44
docs citations

44
times ranked

189
citing authors

#	ARTICLE	IF	CITATIONS
1	A new Mylar-based triboelectric energy harvester with an innovative design for mechanical energy harvesting applications. <i>Energy Conversion and Management</i> , 2021, 244, 114489.	9.2	29
2	Pre-distortion technique to improve linearity of low noise amplifier. <i>Microelectronics Journal</i> , 2017, 61, 95-105.	2.0	26
3	Linearity improvement of gm-boosted common gate LNA: Analysis to design. <i>Microelectronics Journal</i> , 2016, 56, 156-162.	2.0	22
4	A linear ultra wide band low noise amplifier using pre-distortion technique. <i>AEU - International Journal of Electronics and Communications</i> , 2017, 79, 172-183.	2.9	22
5	An Inductor-less Sub-mW Low Noise Amplifier for Wireless Sensor Network Applications. <i>The Integration VLSI Journal</i> , 2016, 52, 316-322.	2.1	13
6	Nano Watt CMOS temperature sensor. <i>Analog Integrated Circuits and Signal Processing</i> , 2013, 75, 343-348.	1.4	12
7	A sub-2-dB noise figure linear wideband low noise amplifier in 0.18 μm CMOS. <i>Microelectronics Journal</i> , 2017, 67, 135-142.	2.0	11
8	A new high-speed low-power and low-offset dynamic comparator with a current-mode offset compensation technique. <i>AEU - International Journal of Electronics and Communications</i> , 2017, 81, 163-170.	2.9	10
9	A sub-mW 2.9-dB noise figure Inductor-less low noise amplifier for wireless sensor network applications. <i>AEU - International Journal of Electronics and Communications</i> , 2018, 93, 132-139.	2.9	10
10	Effect of the inherent capacitance optimization on the output performance of triboelectric nanogenerators. <i>Nano Energy</i> , 2022, 92, 106740.	16.0	10
11	A linear, low power, 2.5-dB NF LNA for UWB application in a 0.18 μm CMOS. <i>Microelectronics Journal</i> , 2015, 46, 1398-1408.	2.0	8
12	An ultra-linear CMOS image sensor for a high-accuracy imaging system. <i>International Journal of Circuit Theory and Applications</i> , 2018, 46, 1593-1605.	2.0	7
13	A low power wideband RGC-based modified-MIC trans-impedance amplifier in 0.18 μm CMOS process. <i>Microelectronics Journal</i> , 2020, 96, 104682.	2.0	6
14	Cost-effective fabrication approaches for improving output performance of triboelectric energy harvesters. <i>Journal of Electrostatics</i> , 2022, 115, 103640.	1.9	6
15	Ultra low power frequency divider for 2.45GHz ZigBee frequency synthesizer. <i>Analog Integrated Circuits and Signal Processing</i> , 2013, 74, 97-103.	1.4	5
16	A Simple and Efficient Charge Injection Error Compensation Structure for MOS Sampling Switches. <i>Journal of Circuits, Systems and Computers</i> , 2018, 27, 1850130.	1.5	5
17	Design of a 12-bit high-speed CMOS D/A converter using a new 3D digital decoder structure useful for wireless transmitter applications. <i>Analog Integrated Circuits and Signal Processing</i> , 2011, 68, 315-328.	1.4	4
18	An 87-dB-SNDR 1MS/s Bilateral Bootstrapped CMOS Switch for Sample-and-Hold Circuit. , 2020, , .		4

#	ARTICLE	IF	CITATIONS
19	A novel low power high CMRR pseudo-differential CMOS OTA with common-mode feedforward technique. , 2015, , .		3
20	Very linear open-loop CMOS sample-and-hold structure for high precision and high speed ADCs. Analog Integrated Circuits and Signal Processing, 2016, 88, 23-30.	1.4	3
21	A 670mW inductorless low noise amplifier employing dual capacitive cross coupling and dual negative feedback. , 2016, , .		3
22	Design of efficient power amplifier for low power transmitters. Analog Integrated Circuits and Signal Processing, 2017, 90, 563-571.	1.4	3
23	Design of a reconfigurable front-end for a multistandard receiver for the frequency range of 800MHz to 2.5GHz. International Journal of Circuit Theory and Applications, 2018, 46, 1144-1165.	2.0	3
24	A high efficiency DC-DC Converter using a new in-package structure of Bonding-Wire inductor. IEICE Electronics Express, 2012, 9, 1005-1011.	0.8	2
25	Efficiency Analysis of Low Power Class-E Power Amplifier. Modern Applied Science, 2014, 8, 19.	0.6	2
26	A 500MHz low offset fully differential latched comparator. Analog Integrated Circuits and Signal Processing, 2017, 92, 233-245.	1.4	2
27	Design of a programmable bandgap reference circuit. , 2009, , .		1
28	A fully digital Background calibration technique for pipeline analog-to-digital converters. , 2009, , .		1
29	A high speed power efficient pipeline ADC in 0.18µm CMOS. , 2013, , .		1
30	A -10dBm 5Mbps Energy-Efficient Injection-Locked FSK Transceiver for Wireless Body Sensor Networks. IETE Journal of Research, 2016, 62, 257-264.	2.6	1
31	A 2.4GHz integer-N frequency synthesizer for ZigBee applications. Analog Integrated Circuits and Signal Processing, 2019, 99, 167-175.	1.4	1
32	A FHD 1080, 120 fps CMOS image sensor with two step SS-ADC. Analog Integrated Circuits and Signal Processing, 2019, 99, 339-347.	1.4	1
33	Recovery of Oversaturated Pixels in a Low-Cost Solder Paste Inspection Setup. , 2020, , .		1
34	An Open-Loop Time Amplifier With Zero-Gain Delay in Output for Coarse-Fine Time to Digital Converters. , 2021, , .		1
35	Application of Folded Cascode-Based Gain-Boosting Circuit in Declining the Current Mismatch Characteristics. , 2020, , .		1
36	A Simple Background Interstage Gain Calibration Technique for Pipeline ADCs. , 2009, , .		0

#	ARTICLE	IF	CITATIONS
37	A mixed mode background calibration technique for pipeline ADCs. , 2009, , .		0
38	A simple background interstage gain calibration technique for pipeline ADCs. , 2009, , .		0
39	A wide-band multipath CMOS OTA for high speed applications. IEICE Electronics Express, 2011, 8, 449-453.	0.8	0
40	A novel topology for modular frequency dividers with enhanced speed and power efficiency. Analog Integrated Circuits and Signal Processing, 2015, 84, 161-171.	1.4	0
41	Design of Analog Baseband Chain Circuits for IEEE 802.11a/b/g WLAN Receivers. , 2018, , .		0
42	A new direct multiplexing based pulse shaping idea for bandwidth efficient OQPSK transmitters in biomedical applications. Analog Integrated Circuits and Signal Processing, 2020, 102, 523-540.	1.4	0
43	A low power MASH digital delta-sigma modulator with accurate output average value. Microelectronics Journal, 2022, 121, 105381.	2.0	0
44	A low-power wideband LNA exploiting current reuse and noise cancelation techniques. International Journal of Circuit Theory and Applications, 0, , .	2.0	0