

# Kihun Choe

## List of Publications by Year in descending order

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16  
papers

204  
citations

1307594

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h-index

1199594

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g-index

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all docs

16  
docs citations

16  
times ranked

145  
citing authors

#	ARTICLE	IF	CITATIONS
1	3D AND-Type Ferroelectric Transistors for Compute-in-Memory and the Variability Analysis. IEEE Electron Device Letters, 2022, 43, 304-307.	3.9	15
2	Multigate Ferroelectric Transistor Design Toward 3-nm Technology Node. IEEE Transactions on Electron Devices, 2021, 68, 5908-5911.	3.0	7
3	Variability Study of Ferroelectric Field-Effect Transistors Towards 7nm Technology Node. IEEE Journal of the Electron Devices Society, 2021, 9, 1131-1136.	2.1	16
4	Variability Analysis for Ferroelectric Field-Effect Transistors. , 2021, , .		10
5	Impact of Random Phase Distribution in Ferroelectric Transistors-Based 3-D NAND Architecture on In-Memory Computing. IEEE Transactions on Electron Devices, 2021, 68, 2543-2548.	3.0	15
6	Experimental RF Characterization of Ferroelectric Hafnium Zirconium Oxide Material at GHz for Microwave Applications. , 2021, , .		8
7	Ferroelectric HfO <sub>2</sub> -based synaptic devices: recent trends and prospects. Semiconductor Science and Technology, 2021, 36, 104001.	2.0	25
8	Compute-in-Memory: From Device Innovation to 3D System Integration. , 2021, , .		5
9	Theoretical study of ferroelectric-gated nanoelectromechanical diode nonvolatile memory cell. Solid-State Electronics, 2020, 163, 107662.	1.4	3
10	Gate-induced drain leakage (GIDL) in MFMS and MFIS negative capacitance FinFETs. Current Applied Physics, 2020, 20, 1222-1225.	2.4	9
11	Direct comparison of ferroelectric properties in Hf <sub>0.5</sub> Zr <sub>0.5</sub> O <sub>2</sub> between thermal and plasma-enhanced atomic layer deposition. Nanotechnology, 2020, 31, 505707.	2.6	45
12	Energy-Delay Sensitivity Analysis of a Nanoelectromechanical Relay With the Negative Capacitance of a Ferroelectric Capacitor. IEEE Journal of the Electron Devices Society, 2020, 8, 365-372.	2.1	3
13	Impact of Random Phase Distribution in 3D Vertical NAND Architecture of Ferroelectric Transistors on In-Memory Computing. , 2020, , .		5
14	Impact of negative capacitance on the energy-delay property of an electromechanical relay. Japanese Journal of Applied Physics, 2019, 58, 051003.	1.5	5
15	Ferroelectric-Gated Nanoelectromechanical Nonvolatile Memory Cell. IEEE Transactions on Electron Devices, 2019, 66, 407-412.	3.0	8
16	Adjusting the Operating Voltage of an Nanoelectromechanical Relay Using Negative Capacitance. IEEE Transactions on Electron Devices, 2017, 64, 5270-5273.	3.0	25