

Behrooz Parhami

List of Publications by Year in descending order

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Version: 2024-02-01

40
papers

493
citations

932766

10
h-index

996533

15
g-index

40
all docs

40
docs citations

40
times ranked

291
citing authors

#	ARTICLE	IF	CITATIONS
1	Reliability Inversion: A Cautionary Tale. Computer, 2020, 53, 28-33.	1.2	3
2	Virtual network embedding on massive substrate networks. Transactions on Emerging Telecommunications Technologies, 2020, 31, e3849.	2.6	2
3	Reliability and Modelability Advantages of Distributed Switching for Reconfigurable 2D Processor Arrays. , 2020, , .		0
4	Virtual Network Embedding Through Graph Eigenspace Alignment. IEEE Transactions on Network and Service Management, 2019, 16, 632-646.	3.2	18
5	Use of Logical Puzzles to Promote Techeracy for Non-Science/Engineering Students. , 2018, , .		1
6	A Case for Table-Based Approximate Computing. , 2018, , .		2
7	Parallelism in Computer Arithmetic: A Historical Perspective (Invited Paper). , 2018, , .		0
8	Symmetric Agency Graphs Facilitate and Improve the Quality of Virtual Network Embedding. Symmetry, 2018, 10, 63.	1.1	3
9	Tight Bounds on the Ratio of Network Diameter to Average Internode Distance. , 2018, , .		0
10	A theoretical analysis of square versus rectangular component multipliers in recursive multiplication. , 2016, , .		1
11	Interconnection Networks with Hypercubic Skeletons. Journal of Interconnection Networks, 2015, 15, 1550006.	0.6	0
12	Truncated ternary multipliers. IET Computers and Digital Techniques, 2015, 9, 101-105.	0.9	8
13	High-speed and low-power scalable Hamming weight comparator based on a non-weighted switched-capacitor array. Analog Integrated Circuits and Signal Processing, 2013, 75, 417-434.	0.9	3
14	A logarithmic approach to energy-efficient GPU arithmetic for mobile devices. , 2013, , .		7
15	A CLASS OF DATA-CENTER NETWORK MODELS OFFERING SYMMETRY, SCALABILITY, AND RELIABILITY. Parallel Processing Letters, 2012, 22, 1250013.	0.4	6
16	Biswapped networks: a family of interconnection architectures with advantages over swapped or OTIS networks. International Journal of Computer Mathematics, 2011, 88, 2669-2684.	1.0	14
17	Construction of vertex-disjoint paths in alternating group networks. Journal of Supercomputing, 2010, 54, 206-228.	2.4	26
18	On equivalences and fair comparisons among residue number systems with special moduli. , 2010, , .		6

#	ARTICLE	IF	CITATIONS
19	Redundant-Digit Floating-Point Addition Scheme Based on a Stored Rounding Value. IEEE Transactions on Computers, 2010, 59, 694-706.	2.4	6
20	Swapped (OTIS) Networks Built of Connected Basis Networks Are Maximally Fault Tolerant. IEEE Transactions on Parallel and Distributed Systems, 2009, 20, 361-366.	4.0	14
21	Design and evaluation of decimal array multipliers. , 2009, , .		7
22	On routing and diameter of metacyclic graphs. International Journal of Computer Mathematics, 2009, 86, 21-30.	1.0	0
23	Motivating Computer Engineering Freshmen Through Mathematical and Logical Puzzles. IEEE Transactions on Education, 2009, 52, 360-364.	2.0	13
24	Digital/analog arithmetic with continuous-valued residues. , 2009, , .		1
25	Efficient Hamming Weight Comparators for Binary Vectors Based on Accumulative and Up/Down Parallel Counters. IEEE Transactions on Circuits and Systems II: Express Briefs, 2009, 56, 167-171.	2.2	42
26	Unified Approach to the Design of Modulo-($2^n \pm 1$) Adders Based on Signed-LSB Representation of Residues. , 2009, , .		25
27	Comments on "Low Diameter Interconnections for Routing in High-Performance Parallel Systems," with Connections and Extensions to Arc Coloring of Coset Graphs. IEEE Transactions on Computers, 2008, 57, 1726-1728.	2.4	1
28	A puzzle-based seminar for computer engineering freshmen. Computer Science Education, 2008, 18, 261-277.	2.7	7
29	Distributed Interval Voting with Node Failures of Various Types. , 2007, , .		5
30	Biswapped Networks and Their Topological Properties. , 2007, , .		24
31	A Group Construction Method with Applications to Deriving Pruned Interconnection Networks. IEEE Transactions on Parallel and Distributed Systems, 2007, 18, 637-643.	4.0	12
32	Extended clustering coefficients: Generalization of clustering coefficients in small-world networks. Journal of Systems Science and Systems Engineering, 2007, 16, 370-382.	0.8	3
33	Fault-Tolerant Reversible Circuits. , 2006, , .		169
34	An Efficient Universal Addition Scheme for All Hybrid-Redundant Representations with Weighted Bit-Set Encoding. Journal of Signal Processing Systems, 2006, 42, 149-158.	1.0	12
35	Some mathematical properties of cayley digraphs with applications to interconnection network design. International Journal of Computer Mathematics, 2005, 82, 521-528.	1.0	28
36	Scalable Linear Array Architecture with Data-Driven Control for Ultrahigh-Speed Vector Quantization. Journal of Signal Processing Systems, 2001, 28, 235-243.	1.0	1

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37	Scalability of Programmable FIR Digital Filters. Journal of Signal Processing Systems, 1999, 21, 31-35.	1.0	1
38	FFT computation with linear processor arrays using a data-driven control scheme. Journal of Signal Processing Systems, 1996, 13, 57-66.	1.0	3
39	A GENERALIZATION OF HYPERCUBIC NETWORKS BASED ON THEIR CHORDAL RING STRUCTURES. Parallel Processing Letters, 1996, 06, 469-477.	0.4	4
40	THRESHOLD VOTING IS FUNDAMENTALLY SIMPLER THAN PLURALITY VOTING. International Journal of Reliability, Quality and Safety Engineering, 1994, 01, 95-102.	0.4	15